

Thursday 31 January

09:00	Instrument status and programmatics - LAD: Instrument status and programmatics - LAD
	Session Location: University of Geneva, Route de Lausanne 266A, 1292 Chambesy, Geneva, Switzerland
	09:00-09:30 Summary of requirements and specifications
	09:30-09:45 Status of Design - open/critical points
	09:45-10:00 LAD: FEE electronics
	Speaker Yannick Favre
10:30	10:00-10:30 TRL: required technology development and potential plan
11:00	
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	Session Location: University of Geneva, Route de Lausanne 2004, 1292 Chambesy, Geneva, Switzenand
	11:00-11:30 Potential ITAR issues and management
	11:30-12:00 Product Tree and Work Breakdown Structure
12:30	12:00-12:30 Model philosophy and development schedule
13:30	Instrument status and programmatics - LAD: Instrument status and programmatics - LAD
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	13:30-14:00
	Cost estimate and expected/needed contributions from MS: critical areas, mitigation actions, plan B.
	14:00-14:30 Expected funding schedule in the MS vs project schedule
15:00	14:30-15:00 Management structure