

# TCAD Simulation of Stitching for Passive CMOS Strip Detectors

*Monday 1 July 2024 17:43 (1 minute)*

Most of the tracking detectors for high energy particle experiments are covered by silicon detectors since they are radiation hard, they can give very small spatial resolution and they can take advantage of the silicon electronics foundries' developments and production lines.

Big area strip detectors are very useful to cover large areas for tracking purposes. The majority of particle physics experiments use conventional silicon strip detectors fabricated in foundries that do not use stitching, relying on a very small number of foundries worldwide that can provide large area detectors. For this production we fabricated strip detectors in a CMOS foundry using two  $1\text{cm}^2$  reticles stitched three and five times, showing that the stitching of two reticles does not affect the performance of the strip detectors.

For this presentation, we will show an overview of the results of passive CMOS strip detectors fabricated for this project and an in-depth TCAD simulation of the possible impacts the stitching can have on the performance of the strips.

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**Session Classification:** Poster Session