CMS Instrumentation and Spin Off Applications

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CMS DETECTOR PHASE 2 UPGRADES



Trigger/DAQ

- L1 with track up to 750 kHz $-12.5 \ \mu s$ latency
- HLT output up to 7.5 kHz

Barrel EM calorimeter

- Replace FE
- electronics
- Cool detector APDs

Muon systems

- Replace DT & CSC FE electronics
- Complete RPC coverage
- Muon tagging 2.4 < η < 3

Replace Endcap Calorimeters

Rad. Tolerant - higher granularity

Replace Tracker

- High granularity less material- b eff- p_{τ} resolution
- Selective readout of outer tracker at 40 MHz for L1 trigger
- Extend η coverage to 4



CMS DETECTOR PHASE 2 UPGRADES

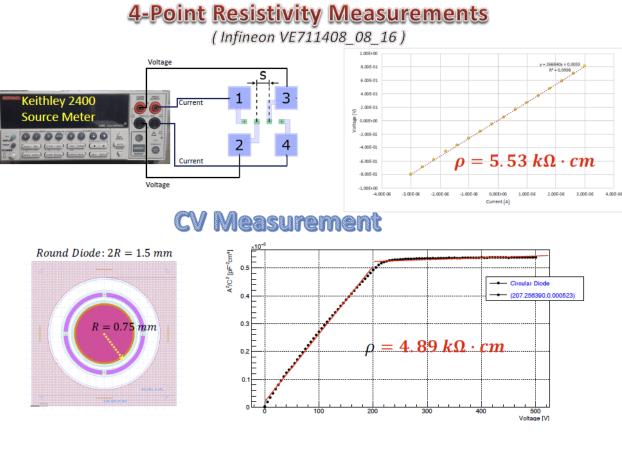
CCMS where a series of the ser

Mask Layout А N+ implant С В D điri. P-stop implant P+ implant Nitride Contact chain Polysilicon Metal MOS capaciti Contact 2-S Sensor 1 Strip Metal vdF Bulk vdP linewidth Polysilicon P-stop Edge vdP Polysilico CBKR Inewidth 2 경우 수가 영문 수 꽃은 수 꽃은 N K Sheet resistance and linewidth Sheet resistance and linewidth Rhoot parktages and linewidth 3 II HO 4-wire resistivity **Field effect transistors** Dielectric breakdown Bulk resistivity Diode, van-der-Pauw, 4-wire p-type (111) Si Oxide quality MOS, GCD, capacitors with implant Measurement on Test Interstrip resistance FET, van-der-Pauw Structures \rightarrow Extract useful information about the Sheet resistances Meander, van-der-Pauw, linewidth

sensor itself





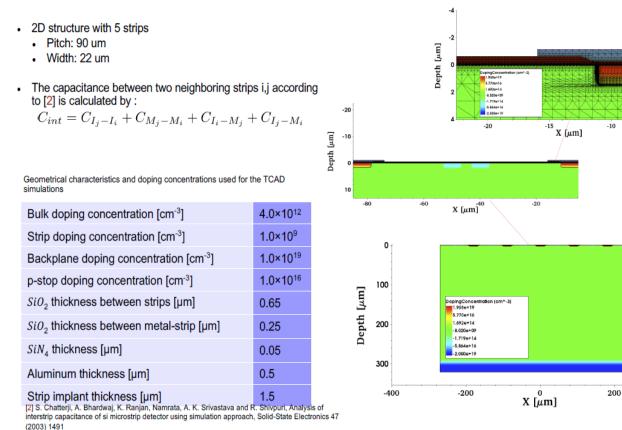


Contact persons: <u>kyriakis@inp.demokritos.gr</u>, <u>loukas@inp.demokritos.gr</u> Candidate Profile: 1)Physicist/Engineer 2) Programming skills-> C++/ROOT/python



Subject 2: TCAD Test Structure Simulations





Candidate Profile: 1)Physicist/Engineer 2) Programming skills-> TCAD

400

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- New generation of Pixel Readout Chips are being developed for the Phase-2 Upgrade of the Inner Tracker and INPP of NCSR "Demokritos" has undertaken the following responsibilities:
 - Development of the Data Acquisition System for the Control and Readout of the prototype chips



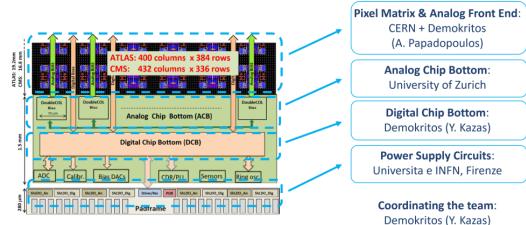
Firmware Suite



Hardware

FC7 Board, Developed by CERN

- o Coordinatization of the testing and characterization campaign for the chips
 - Functional validation, performance evaluation, temperature studies, irradiation campaigns etc.



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Candidate Profile: 1)Physicist/Engineer 2) Programming skills-> Python/C++, FPGA programming (VHDL)

Radioactive Source Localization Lab (AILAB)

More Information can be found at site: http://ailab.inp.demokritos.gr

or at the educational video:

https://www.youtube.com/watch?v=Xo-

LDNK9yQ4&list=PLcNicqge3dtPV1C_FG2Ea7qhzbzyt7rEA&index=3

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1) Radioactive Source Localization by a Network of CZT Sensors

Main task: Localization of light shielded and/or moving sources

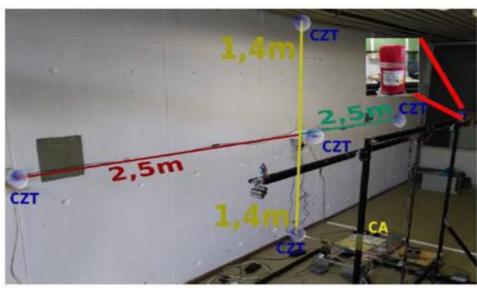


Figure 2 : Network of five radiation detectors in cruciform configuration and the light shielded (1cm of Pb surrounding the source)¹³⁷Cs source used for test bed of the localization algorithms

Using Planar spectroscopic CZT sensor topology study the capability to localize Radioactive Sources in an open area using MVA techniques.

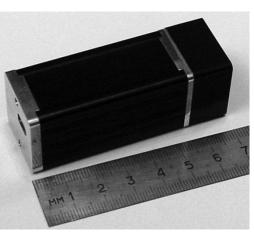
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2)Radioactive Source Localization by an autonomous rover equipped with CZT Sensors

Main task: write a stand alone software in python to retrieve data from CZT sensor and analyze them



- 1. Rover Body
- 2. Tablet
- 3. Radio Sensor
- 4. Pb shield
- 5. Adruino Controller
- 6. Motor Driver
- 7. Servo Radar
- 3. LIDAR
- 9. Battery source



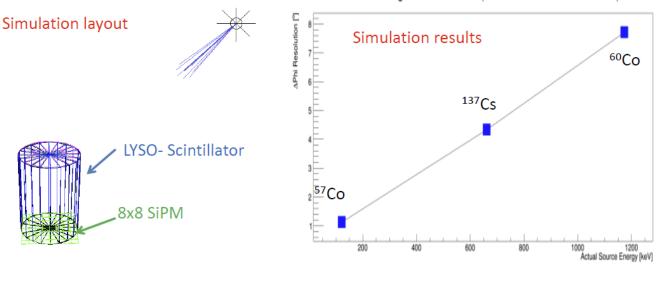
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Figure 3: Semi-Autonomous rover platform equipped with radiation sensor

3) Simulation studies of Radioactive Source Localization by an Anger type camera.

Main task: Analyze simulated events of a Anger type camera with analytical and MVA techniques (Deep Neural Networks) to find the direction of the radioactive source

An Anger type gamma ray detection system based on a 2" x 3" LYSO scintillator crystal and read out by an array of 8x8 SiPM. The system can detect a wide range of sparse radioactive sources with energies from a few tens of keV to 1.5MeV ideal for homeland security

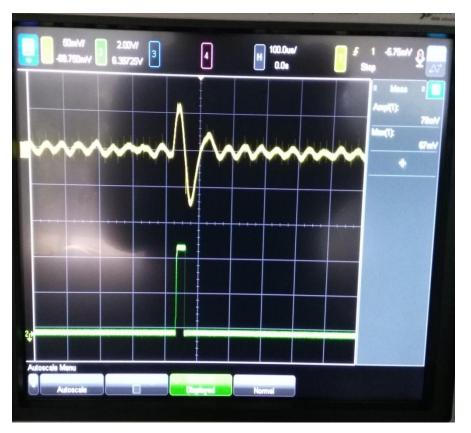


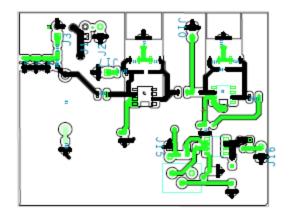
Azimuthal angle source resolution (LYSO Scintillator+Teflon Reflector)

Candidate Profile: 1)Physicist/Engineer 2) Programming skills-> C++/Geant4/ROOT/p ython

4) Low-Cost Radiation detectors

Main Task: Design Low noise board + evaluation software





Candidate Profile: 1)Physicist/Engineer 2) Programming skills-> Cadence/Orcad/Pspice simulation packages