

Session Program

3-7 Jun 2018



2018 IEEE International Power Modulator and High Voltage Conference

Oral 7 - High Voltage Design

Jackson Lake Lodge, Jackson Lake Lodge
101 Jackson Lake Lodge Road, Jackson Lake Lodge, Moran, WY 83013 Phone: (307) 543-3100

Tuesday 5 June

15:30

Oral 7 - High Voltage Design

Session | **Location:** Explorers Room

15:30–16:00

Redesign of a High Voltage Test Bed for Marxes on Z

Speaker

Dr William White

16:00–16:15

Charge Plasma High Voltage PIN Diode Investigation

Speaker

Mrs Sara Hahmady

16:15–16:30

Pulse-Sharpening Circuit for Explosive Emission Cathode

Speaker

Nicholas Kallas

16:30–16:45

80kV Coaxial Feedthrough for CPP20 Pulse-Forming Line Kicker Applications

Speaker

Mr Michael Osemann

17:00–17:15

Study on the Nonlinear Vibration Characteristic of Dry-Type Air-Core Reactor

Speaker

Mr Zhitong Jiang

17:15–17:30

Diagnosis on Winding Failure Through Impulsive Sound of Power Transformer

Speaker

Dr Yuhang Shi

17:30