

Session Program

5-9 Jul 2016



2016 IEEE Power Modulator and High Voltage Conference

Oral 6

<http://www.sfpalace.com/>Palace Hotel San Francisco
2 New Montgomery St. San Francisco, CA 94105United States

Thursday 7 July

10:00

Oral 6: Power Electronics and High Voltage

Session | **Location:** Palace Hotel San Francisco, Gold Ballroom

10:00-10:15

DEVELOPMENT OF 3 MW DUAL OUTPUT HIGH VOLTAGE POWER SUPPLY FOR IC RF SYSTEM

Speaker

Mr AMIT PATEL

10:15-10:30

High precision, low ripple 3kV capacitor charger for future linear colliders

Speaker

Sebastian Blume

10:30-10:45

HV Energy Dosing dc-to-dc Converter in PWM Mode

Speaker

Alexander Pokryvailo

10:45-11:00

FILTERING OF NOISES ASSOCIATED TO THE COMMUTATION OF THE DC RECTIFIERS IN PATTERNS φ , q , n OF PARTIAL DISCHARGES

11:00-11:15

Vibration and Audible Noise Analysis of Power Capacitors with Constrained Damping Structure

Speaker

Mr Jinyu Li

11:15-11:45

High power, high repetition rate, fast capacitor charging circuit

Speaker

Andras Kuthi

11:45-12:00

The Distribution Of Lightning Striking Points On Aircraft Using The Leader Progress Method

12:00