

## Session Program

1-5 Aug 2022



## **23rd Virtual IEEE Real Time Conference** ***Architectures, Intelligent Signal Processing & Simulation***

# Thursday 4 August

11:00

## Architectures, Intelligent Signal Processing & Simulation

### Session

11:00–11:20

#### **PulseDL-II: a system-on-chip neural network accelerator for timing and energy extraction of nuclear detector signals**

##### **Speaker**

Dr AI, Pengcheng

11:20–11:40

#### **The LHCb HLT2 storage system: a 40 GB/s system made from commercial off-the-shelf components and open-source software**

##### **Speaker**

Mr CIFRA, Pierfrancesco

11:40–12:00

#### **The Real-Time System for Distribution of Clock, Control and Monitoring Commands with Fixed Latency of the LHCb experiment at CERN**

##### **Speaker**

FEO, Mauricio

12:00–12:20

#### **Using FPGA-based AMC Carrier Boards for FMC to Implement Intelligent Data Acquisition Applications in MTCA.4 Systems using OpenCL**

##### **Speakers**

Mr RIVILLA, Daniel, RIVILLA, Daniel

12:20–12:40

#### **ERSAP: Towards better HEP/NP data-stream analytics with flow-based programming**

##### **Speaker**

Dr GYURJYAN, Vardan

12:40