

RT2022 – Aug 4th, 2022, - Thursday – Mini Oral Session IV

16	haibo yang	Design of the readout electronics for the CEE-ZDC mission #16
17	Cong Zhao	A 25 Gbps VCSEL Driving ASIC for Detector Front-end Readout #17
18	Qiangjun Chen, Di Guo	A Low power 4 × 14-Gbps VCSEL Driver in 65 nm CMOS for Particle Physics Experiments #18
26	Siyu Huang	The frontend electronics design for broadband high-sensitive light pulse detection #26
28	Yubo Ma	An Efficient Waveform Reconstruction Method for Digital Bandwidth Interleaving Sampling System #28
32	Peng Hu	Preliminary Study on Timing Characteristics of Fast SiPMs #32
74	Giuseppe Avon	Design, integration and testing of the ITER magnetics diagnostics embedded processing system #74
78	Rui He	Design of a Pipeline Regional ADC for Monolithic Active Pixel Sensor #78
79	Rui Yin	Design of a 12-bit column-parallel ADC for Monolithic Active Pixel Sensor #79
91	Shun Liao	The FWD, a Full-Waveform Digitizer for the readout of the plastic scintillator detectors at HIRFL and HIAF #91
101	Lin Jiang	Development of the Test-bench “Wukong” for Readout Electronics and Pulse Digitizer #101
107	João Oliveira	Conceptual design of a magnetic data acquisition system in fusion experiments #107
109	Marvin Fuchs	Split Boot - True network-based booting on heterogeneous MPSoCs #109
112	Ed Jastrzembki	VMM Based Readout Prototype for the SoLID Detector at Jefferson Lab #112
117	Ming Xiong Liu	Intelligent experiments through real-time AI: Fast Data Processing and Autonomous Detector Control for the sPHENIX and future EIC detectors #117
123	Yunqi Deng	Fast-Settling high input dynamic range Automatic Gain Control Front-end circuit for particle detect #123
124	Binqiang Xiong	SiPM readout chip design for Heavy-ion Physics #124
126	Jana Faltova	A 10-bit Resistor-Floating-Resistor-String DAC for Multi-Voltage Threshold Digitizer in PET #126