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# USB 3.0 realization based on FPGA

USB are widely used in FPGA boards, and in the most cases a USB chip such as Cypress CY3014 are used for transferring or an external USB 3.0 PHY is used coupled with USB controllers realized by software running on arm. The paper presents a new USB realization based on FPGA fabrics for everything including controllers and PHY. The result shows that the nearly 320 MBps for downstream and upstream transferring.

### Minioral

No

### IEEE Member

No

### Are you a student?

No

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