

Session Program

22-26 Apr 2024



24th IEEE Real Time Conference - ICISE, Quy Nhon, Vietnam

Invited Talk, Oral presentations, Mini-Orals

Wednesday 24 April

09:00

Invited Talk, Oral presentations, Mini-Orals

Session | Location:

09:00–09:40

End-to-end Codesign of Hessian-aware Quantized Neural Networks for FPGAs

Speaker

Vladimir Loncar

09:40–10:00

Science Rate Acceleration with ESnet-JLab FPGA Accelerated Transport Load Balancer

Speaker

michael goodrich

10:00–10:20

Development of the Proton Computed Tomography (pCT) prototype

Speaker

Arnon Songmoolnak

10:20–10:50

Coffee Break

10:50–11:35

Mini Orals Session III

11:35–11:55

Enhancing Real-time Data Monitoring Display through Video Streaming Technology

Speaker

Nicholas Tan Jerome

11:55–12:15

FPGA Tracking with oneAPI

Speaker

Karol Hennessy

12:15–12:35

Studies on track finding algorithms based on machine learning with GPU and FPGA

Speaker

Francesco Armando Di Bello

12:35