ID#	Mini Oral III	Speaker
10	The Software Platform of LabVIEW-FPGA-Based Real-time Processing System in Keda Torus eXperiment	Jiahong Jiang
14	An FPGA-based ADC for PET module applications	songqing liu
22	Advances in Readout Electronics for STCF ECAL	Hanlin YU
23	A 25 Gbps VCSEL Driving ASIC for Detector Front-end Readout	Di Guo; Cong Zhao
26	Real Time Data Acquisition for PET Detector Evaluation based on dual-polarity Charge-to-Digital Converter	Bo Wang
28	Front-end Electronics for the Prototype of HERD Transition Radiation Detector	Jieyu Zhu; Haibo Yang; Yangzhou Su
36	A fast front-end readout design for NICA-MPD shashlik electromagnetic calorimeter	Xinchi Ran
42	A radiation trace recognition framework for the Timepix event data	Quốc Trình Nguyễn Ngọc
46	High-throughput Custom Monitoring for the Mu2e TDAQ System	Michael H L Wang
59	Design of a 0.8 V Low-voltage High-rate Prototype Readout ASIC for the µRWELL-based Inner Tracker Detector	Jiaming Li
69	A 4×6.25-Gbs Serial Link Transmitter Core in 0.18-µm CMOS for high-speed front-end ASICs	Jiacheng Guo
70	High speed readout electronics for new generation Pulsed Muon Spectrometers	Francesco Caponio
74	A Frequency Division Multiplexing Room-temperature Electronics Readout Scheme for TES Calorimeter Arrays	Jianguo Liu
75	Nupix-H2: a Monolithic Active Pixel Sensor for Multidimensional Measurement	Rui He
76	Hi'BT : a pixel sensor-based heavy-ion beam telescope for ion-track localization at HIAF	Honglin Zhang
77	Development of a High-Bandwidth Waveform Processing System using RFSoC for RI Beam Experiments	Shoko Takeshige
85	A digital LLRF system based on phase tracking for HALF linear accelerator system	Zhenyan Li
86	A Compact Readout Electronics based on Current Amplifier for Micromegas Detector	Ting Wang
96	Development of warm readout electronics for Time-division Multiplexing SQUID	Nan Li
98	The front-end electronics of the Hyper-Kamiokande far detector	Alessandro Di Nola