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Development of FPGA-Based Nuclear Electronics using NI MyRIO Hardware for Small-Scale Radiation Detector Systems

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Coincidence electronics and pulse height analyzers stand as pivotal techniques in radiation detection systems. The development of FPGA-based nuclear electronics has garnered attention due to their programmability, simplicity, testability, compact size, and low power consumption. This technology is increasingly favored worldwide for nuclear electronic systems over traditional analog counterparts. This study presents advancements in coincidence electronics and a Pulse Height Analyzer (PHA) utilizing commercial FPGA-based (Field-Programmable Gate Array) hardware for radiation scintillation detectors. The hardware, based on a cost-effective NI myRIO device, integrates a Field-Programmable Gate Array (FPGA), ARM Cortex-A9 processor, analog input (AI), digital input and output (DIO), and USB/wireless connectivity with a host computer. LabVIEW codes, developed on the LabVIEWTM platform, are implemented in NI myRIO hardware for seamless integration and computer interface. The FPGA-based coincidence electronics performance is assessed through an experimental setup for the gamma-gamma angular distribution of a Na-22 radioisotope source. Similarly, the FPGA-based PHA undergoes testing with a NaI(TI) detector, with a subsequent comparison of energy resolution against a commercial EASY-MCA 2K from AMETEK Inc.

Minioral

Yes

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Yes

Are you a student?

No

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