24th IEEE Real Time Conference - ICISE, Quy Nhon, Vietnam



Contribution ID: 80

Type: Mini Oral and Poster

The Design of an 8-channel, 41.7-ps Resolution Time-to-Digital Converter for STCF ECAL

Tuesday 23 April 2024 12:35 (20 minutes)

The Super Tau-Charm Facility (STCF) under construction in China demands for a new Electromagnetic Calorimeter (ECAL) with good time measuring capability to realize background suppression and gamma-neutron discrimination. The time measurement requirements include high resolution, large dynamic range and multichannel integration. Therefore, we adopted a 3-level-structure based Time-to-Digital Converter (TDC) which can realize large dynamic range with a coarse counter as the first level and high resolution with a modified Vernier Delay Loop (VDL) as the finest level. The proposed VDL can get a stable and sub-gate resolution utilizing differentials between two kinds of unit delay locked by different Delay Locked Loops (DLLs). The proposed VDL is also characterized with the cyclic structure for linearity improvement and the automatic restoration for continuous measurement. The proposed 8-channel TDC was fabricated with standard 180-nm CMOS process and it can reach a resolution of 41.7-ps and dynamic range of 2.56-µs under the reference clock frequency of 100-MHz. According to test results, the precision of asynchronous measurement is 47.0-ps in the best situation and all channels have good linearity with DNL < 1 LSB and INL < 1.5 LSB.

Minioral

Yes

IEEE Member

Nο

Are you a student?

Yes

Authors: Dr ZHAO, Ziwei (School of Computer Science and Technology, Northwestern Polytechnical University); Dr ZHENG, Ran (School of Computer Science and Technology, Northwestern Polytechnical University); Dr WANG, Jia (School of Computer Science and Technology, Northwestern Polytechnical University); Dr ZHAO, Ruiguang (School of Computer Science and Technology, Northwestern Polytechnical University); Dr WEI, Xiaomin (School of Computer Science and Technology, Northwestern Polytechnical University); Dr XUE, Feifei (School of Computer Science and Technology, Northwestern Polytechnical University); Mr LIU, Chao (School of Computer Science and Technology, Northwestern Polytechnical University); Prof. HU, Yann (School of Computer Science and Technology, Northwestern Polytechnical University)

Presenter: Dr ZHAO, Ziwei (School of Computer Science and Technology, Northwestern Polytechnical University)

Session Classification: Poster A

 $\textbf{Track Classification:} \ \ \textbf{Front-End Electronics, Fast Digitizers, Fast Transfer Links \& Networks}$