Session Program

12-23 Oct 2020

22nd Virtual IEEE Real Time Conference

Poster session B-01

Tuesday 13 October

16:05

Poster session B-01
Session | **Location:**

16:05–16:06

A reconfigurable neural network ASIC for detector front-end data compression

Speaker

TRAN, Nhan Viet

16:06–16:07

System Design and Prototyping for the CMS Level-1 Trigger at the High-Luminosity LHC

Speaker

TAPPER, Alex

16:07–16:08

A 32 Channel Time-Tagging and Coincidence Detector Unit with High Data Throughput

Speaker

HIDVEGI, Attila

16:08–16:09

Study on the time characteristics of fast MPPC based on 40GS/s real-time oscilloscope

Speakers

YAN, Min, Mr QIAN, Sen

16:09–16:10

The Study of Test Method on Time Characteristic for ultra-fast Photodetectors

Speakers

QIANYU, Hu, Mr QIAN, sen

16:10–16:11   Applications of Triggered Scaler Module for Accelerator Timing

Speaker

Ms YANG, Min

16:11–16:12

The influence of the dead layer increase on total gamma spectrum response for a coaxial HPGe p-type detector

Speaker

THỊ HỒNG LOAN, Trương

16:12–16:13

Development of a real-time simulation RELAP/SUNDIALS code for the Dalat Nuclear Research Reactor

Speaker

Mr TRUONG, Tuan

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16:13–16:14

Response function of a neutron dose-rate meter: unfolding evaluation and verification

Speaker

Dr LE, Ngoc Thiem

16:14–16:15

Optimization of PSD technique on charge integration ratio to improve neutron/ gamma discrimination for EJ-276 plastic scintillation detector

Speaker

Dr VO, Hong Hai

16:15–16:16

Study on impact of ENDF/B-VII.0 nuclear library uncertainty on the CERMET fueled ADS reactivity calculation using MCNP6 and WHISPER

Speaker

Dr VU, Thanh Mai

16:16–16:17   **Safety Interlock System Design for Proton Therapy System**

Speaker

FENG, Hansheng

16:17–16:18   **Architecture Design of JUNO DAQ**

Speaker

LI, Fei

16:18–16:19   **Real-time monitoring of operational data in the Belle II experiment**

Speaker

KUNIGO, Takuto

16:19–16:20   **Preliminary design of real-time plasma control system for CFETR**

Speaker

Dr YUAN, Qiping

16:20–16:21

Development of the second version of a 64-ch SiPM readout ASIC for TOP-PET with individual energy and timing digitizer

Speaker

Mr HAO, Jiajun

16:21–16:22

Clock Trigger Node based on White Rabbit Technology for Distributed Trigger for Large Scale Neutron Detectors

Speaker

Ms LI, Jiawen

16:22–16:23

A Low-power Large-capacity Storage Method for Deep-sea In-situ Radiation Measurement

Speaker

JIANG, Wei

16:23–16:24

Monitoring and control systems for a new branched proton beam line in Hadron Experimental Facility at J-PARC.

Speaker

Dr MORINO, Yuhei

16:24–16:25   **A Linear Digital Timing Measure Method for In-Beam PET**

Speaker

Dr KE, Lingyun

16:25–16:26

Characterization of an In-Beam PET prototype for Heavy-Ion Cancer Therapy Device

Speaker

Dr YAN, junwei

16:26–16:27

Universal Readout Method for GAEA Gamma Spectrometer at CSNS Back-n White Neutron Source

Speaker

XIE, Likun

16:27–16:28

Development of an 16 channel low power and high intergration readout ASIC for time projection chamber in 65 nm CMOS

Speaker

Mr LIU, wei

16:28–16:29   **The data taking network for COMET Phase-I**

Speaker

Dr IGARASHI, Youichi

16:29–16:30

Pulse Shape Discrimination of Bulk and Very Bulk Events within HPGe Detectors

Speaker

Dr ZHU, Jinfu

16:30–16:31

A Prototype Design of the Readout Electronics of MRPC Detectors in CEE in HIRFL

Speaker

LU, Jiaming

16:31–16:32

Multi-Spectrometer Compatible Data Acquisition System for Deep-sea In-situ Radiation Measurement

Speaker

Mr YUAN, Jianhui

16:32–16:33   **Peak-finding for Longitudinal Beam Halo Readout System**

Speaker

Dr NGUYEN, Minh Truong

16:33–16:34   **Archiver System Management for Belle II Detector Operation**

Speaker

KIM, Yongkyu

16:34–16:35

Prototype Design for Upgrading Central Safety and Interlock System on EAST

Speaker

Mr ZHANG, Zuchao

16:35–16:36

MPV - Parallel Readout Architecture for the VME data acquisition system

Speaker

BABA, Hidetada

16:36–16:37   **Improvement of EAST Data Acquisition Configuration Management**

Speakers

Dr CHEN, Ying, Dr LI, Shi, Dr WANG, Feng

16:37–16:38

Validation 2-D In-Vivo Dosimetry Based on Correlation Ratio Algorithm

Speaker

Mr RAMADHAN, Muhammad Mahdi

16:38–16:39

Particle Tracking with Space Charge Effect using Graphics Processing Unit

Speaker

KURIMOTO, Yoshinori

16:39–16:40   **Data Management and Network Platform for CFETR**

Speaker

Dr SUN, Xiaoyang

16:40–16:41

Gigabit Ethernet Daisy-Chain on FPGA for COMET Read-out Electronics

Speaker

HAMADA, Eitaro

16:41–16:42

Noise Analysis of Current Sensitive Preamplifiers and Influence on Energy Resolution of NaI:Tl Detector System

Speaker

Mr JIANG, Lin

16:42–16:43

Iterative Retina for barrel-shape tracker and high magnetic field and high track multiplicity

Speaker

Mr DENG, Wendi

16:43–16:44

A Low-power VCSEL Driving Structure Implemented in a 4 x 14-Gbps VCSEL Array Driver

Speaker

GUO, Di

16:45