

## **Session Program**

**9-15 Jun 2018**



## **21st IEEE Real Time Conference - Colonial Williamsburg**

### ***Front End Fast Detectors 2***

Woodlands Conference Center  
159 Visitor Center Dr, Williamsburg, VA 23185

## Thursday 14 June

11:10

### Front End Fast Detectors 2

**Session** | **Location:** Woodlands Conference Center, 159 Visitor Center Dr, Williamsburg, VA 23185 | **Convener:** Jinyuan Wu

11:10–11:30

#### A 3.8ps RMS time synchronization implemented in a 20 nm FPGA

**Speaker**

Mr HongBo Xie

11:30–11:50

#### FPGA Based Pico-second Time Measurement System for a DIRC-like TOF Detector

**Speaker**

Mr Qiang Cao

11:50–12:10

#### Preliminary Design of Integrated Digitizer Base for Photomultiplier Tube

**Speaker**

Mr Jinfu Zhu

12:10–12:30

#### Development of a multichannel FPGA based high resolution Time-to-Digital Converter

**Speaker**

Tomonori Takahashi

12:30–12:50

#### A 5.5 ps Time-interval RMS Precision Time-to-Digital Convertor Implemented in Intel Arria 10 FPGA

**Speakers**

Mr Jie Kuang, Prof. Yonggang Wang

12:50