

Session Program

9-15 Jun 2018



21st IEEE Real Time Conference - Colonial Williamsburg

Poster 2

Woodlands Conference Center
159 Visitor Center Dr, Williamsburg, VA 23185

Wednesday 13 June

14:35

Poster 2

Poster Session | **Location:** Woodlands Conference Center, 159 Visitor Center Dr, Williamsburg, VA 23185 | **Convener:** Martin Grossmann

Design of a common verification board for different back-end electronics options of the JUNO experiment

Speaker
yifan yang

General purpose readout board π LUP: overview and results.

Speaker
Mr Nico Giangiacomi

Design and development of the DAQ and Timing Hub for CMS Phase-2

Speaker
Frans Meijers

A new approaching method of PSD technique on charge integration ratio to improve neutron/gamma discrimination in low-energy region for EJ-299-33 plastic scintillation detector

Speaker
Võ Hồng Hải

Quality Evaluation Electronics for CBM-TOF Super Module

Speaker
Mr Chao Li

Development of FEB Configuration Test Board for the ATLAS NSW Upgrade

Speaker
Mr Houbing Lu

T0 Fanout for Back-n White Neutron Facility at CSNS

Speaker
Mr Xuyang Ji

Prototype of Front-end Electronics for PandaX-4ton Experiment

Speaker
Mr Shuwen Wang

Network Time Synchronization of the Readout Electronics for a New Radioactive Gas Detection System

Speaker
Wolfgang Hennig

An FPGA Based Fast Linear Discharge Method for Nuclear Pulse Digitization

Speakers
Prof. Yonggang Wang, Mr Jie Kuang

Real-time State Monitoring System for Motor Based on Web

Speakers

Dr Dan Li, Prof. Xiao Bingjia, Prof. Ji Zhenshan, Dr Wang Yong, Dr Liu Shaoqing, Mrs He Xianting

Development of MicroTCA.4 based remote DAQ system for KSTAR Tokamak**Speaker**

giil kwon

A General Purpose FPGA-based Programmable Digital Patter Generator**Speaker**

Dr stefano russo

Study on timing performance of a readout circuit for SiPM**Speakers**

Prof. Yonggang Wang, Mr Qiang Cao

A Design of FPGA Based Small Animal PET Real Time Digital Signal Processing and Correction Logic**Speaker**

Mr Jiaming Lu

Nuclear Pulse Charge Measurement with a Method of Time over Linear Threshold**Speakers**

Mr Zhengqi Song, Prof. Yonggang Wang, Mr Qiang Cao

An SOA based Design of JUNO DAQ Online Software**Speaker**

Ms Jin Li

Design of readout electronics of scintillators and SiPMs for CEPC ECAL preresearch**Speaker**

Mr Shensen Zhao

A Front-end Signal Digital Acquisition System In Intensive Electromagnetic Field Circumstance**Speaker**

Mr He Zhou

Readout method based on PCIe over optical fiber for CBM-TOF super module quality evaluation**Speaker**

Mr Jianhui YUAN

Timing and clocking scheme in the upgraded LHCb detector**Speaker**

Federico Alessio

JUNO DAQ Readout and Event Building Research**Speaker**

Dr Tingxuan Zeng

A data transmission system for the phase contrast X-ray human computed tomography prototype

Speaker

Dr rongqi sun

The Detector System Design for the Grating-based Phase Contrast Imaging CT Prototype**Speaker**

Dr Rongqi Sun

A new all-digital background calibration technique for time-interleaved ADC using first order approximation FIR filters**Speaker**

Mr Jiadong Hu

Design of Readout Electronics for CEPC Semi-Digital Hadronic Calorimeter Pre-research**Speaker**

Mr Yu Wang

Readout electronics for a boron-coated multi-wire proportional chamber neutron detector**Speaker**

Prof. Ping Cao

Design of Front End Electronics for direct dark matter detection based on LAr**Speaker**

Mr Xing Zhu

An ultra-sensitive balanced detector with low noise for continuous-variable quantum key distribution**Speaker**

Qiming Lu

The Readout Supervisor firmware for controlling the upgraded LHCb detector and readout system.**Speaker**

Federico Alessio

Design of 32-channel TDC Based on Single FPGA for μ SR Spectrometer at CSNS**Speaker**

Mr Fanshui Deng

A Driver ASIC for Scientific CCD Detectors Using 180nm Technology**Speaker**

Mr Yi Feng

Design of a general scientific CCD simulation and test system based on FPGA**Speaker**

Dr Hong-fei Zhang

Detection of Weak Near-Infrared Signal Based on Digital Orthogonal Vector Lock-in Amplifier**Speaker**

Mr Qi-jie Tang

Design of a Non-vacuum-cooling compact scientific CCD camera**Speaker**

Dr Yi Feng

Real time data access log analysis system of EAST tokamak based on spark**Speaker**

Feng WANG

Data Acquisition in Phase II Run of the Belle II Experiment**Speaker**

Satoru Yamada

Design and test of sTGC front-end electronic interface board**Speaker**

Xu Wang

A 14 Gbps low power VCSEL driver for high-energy physics experiments**Speaker**

Wei Zhou

An FPGA-Driven Signal Generator for the Barrier Bucket System at COSY**Speaker**

Dr Peter Wüstner

Java Driver Implementation for the Ethernet Flash ADC**Speaker**

John McKisson

FELIX based readout of the Single-Phase ProtoDUNE detector**Speaker**

Mr Milo Vermeulen

The Fermilab Test Beam Facility Data Acquisition System**Speaker**

Eric Flumerfelt

EAST Real-Time VOD System Based on MDSplus**Speaker**

Ms Jinyao Xia

Framework for High-performance Video Acquisition and Processing in MTCA.4 Form Factor**Speaker**

Mr Aleksander Mielczarek

Reduction Signals Method Preserving Spatial and Temporal Capabilities**Speaker**

Efthymios Lamprou

Final design of the readout system for Triple-GEM detectors for the CMS forward muon upgrade**Speaker**

yifan yang

Development of a 256-channel Time-of-flight Electronics System For Neutron Beam Profiling

Speaker

Mr Haolei Chen

An sTGC Prototype Readout System for ATLAS New-Small-Wheel Upgrade

Speaker

Peng Miao

Design of a Programmable Gain Waveform Digitization Instrument for Detector Calibration

Speaker

Dr Zhe Cao

The Use of Java in Online Event Building and Recording at Jefferson Lab

Speaker

Dr Carl Timmer

Simulation System for the Wendelstein 7-X Safety Control System

Speaker

Mr Jörg Schacht

Design of a general purpose scalable DAQ system

Speaker

Ms Yuyan Huang

Prototype of a multi-host type DAQ front-end system for RI-beam experiments

Speaker

Hidetada Baba

Additive phase-noise in frequency conversion in LLRF systems

Speaker

Mr Igor Rutkowski

Cavity Simulator for European Spallation Source

Speaker

Maciek Grzegorzówka

Trigger Selection System for CBM-TOF Super Module Quality Evaluation

Speaker

Mr Junru Wang

Accurate Synchronization of Multichannel Acquisition for Field Digitizer Modules at CSNS-WNS

Speaker

Dr Xiru Huang

A multi-channel DAQ system based on FPGA for long-distance transmission in nuclear physical experiments

Speaker

Mr Hongwei Yu

A novel real-time radiation detector readout and acquisition system for PET

Speaker

Dr Kun Hu

Upgrade of HADES data acquisition and event building software for FAIR phase 0**Speaker**

Serguei Linev

A High Precision Signals Readout System for Micromegas Detector Based on the VMM**Speaker**

Mr Shuang Zhou

Data Acquisition Software for quality evaluation of CBM-TOF super module detector**Speaker**

Ms Jiawen Li

Design of TDC ASIC based on Temperature Compensation**Speaker**

Ma Yichao

Real-time Data Flow Control for CBM-TOF Super Module Quality Evaluation**Speaker**

Mr Wei Jiang

Data Acquisition System for the CSNS Neutron Beam Monitor**Speaker**

Dr Jian ZHUANG

The Design of Data Acquisition System for EAST Technical Diagnostic System**Speakers**

Dr Ying Chen, Dr Shi Li, Dr Huazhong Wang, Dr Yong Wang, Dr Bingjia Xiao

A Zynq - based flexible ADC architecture combining real-time data streaming and transient recording**Speakers**

Dr Gabriele Manduchi, Andrea Rigoni

The Study of Multi-Layer sTGC Test System for ATLAS Phase-I upgrade**Speaker**

Dr Feng Li

Time of flight Measurement Electronics for Back-n at CSNS**Speaker**

Tao Yu

The customization of White Rabbit for different applications**Speaker**

Dr Guanghua Gong

A low power DAQ system with high-speed storage for submersible buoy**Speaker**

ZhiLei Zhang

Design of Mesh-Signal Readout Electronics for PandaX-III prototype TPC

Speaker

Ms Danyang Zhu

Real-time Data Sharing Comparisons Between NSTX-U, DIII-D, and KSTAR

Speaker

Keith Erickson

Significant acceleration of development by automating quality assurance of a medical particle accelerator safety system using a formal language driven test stand

Speakers

Martin Grossmann, Mr Pablo Fernandez Carmona

16:05