

## **Session Program**

**9-15 Jun 2018**



# **21st IEEE Real Time Conference - Colonial Williamsburg**

## ***Minioral 1***

Woodlands Conference Center  
159 Visitor Center Dr, Williamsburg, VA 23185

# Sunday 10 June

15:00

## Minioral 1

**Session** | **Location:** Woodlands Conference Center, 159 Visitor Center Dr, Williamsburg, VA 23185 | **Conveners:** Rejean Fontaine, Martin Grossmann

15:00-15:02

### 388 Web-based Real Time Monitoring with HTML5

#### Speaker

Stefan Ritt

15:02-15:04

### 387 A programmable clock generator for automatic Quality Assurance of LOCx2

#### Speaker

Mr Yi Feng

15:04-15:06

### 396 Design of Voltage Pulse Control Module for Free Space Measurement-Device-Independent Quantum Key Distribution

#### Speaker

Sijie Zhang

15:06-15:08

### 403 Application of FPGA Acceleration in ADC Performance Calibration

#### Speaker

guangyuan yuan

15:08-15:10

### 407 The Electronics Design of Error Field Feedback Control System in KTX

#### Speaker

Mr Tianbo Xu

15:10-15:12

### 408 The Proton Beam Realtime Monitor System in CSNS

#### Speaker

Dr Jian ZHUANG

15:12-15:14

### 422 Real-Time Betatron Tune Correction with the Precise Measurement of Magnet Current

#### Speaker

Yoshinori Kurimoto

15:14-15:16

### 425 Overview and performance of the ATLAS Level-1 Topological Trigger

#### Speaker

Johannes Frederic Damp

15:16-15:18

### 455 A new high speed, Ultrascale+ based, board for the ATLAS jet calorimeter trigger system

**Speaker**

Johannes Frederic Damp

15:18-15:20

**442 The Phase-1 Upgrade for the Level-1 Muon Barrel Trigger of the ATLAS Experiment at LHC****Speaker**

Vincenzo Izzo

15:20-15:22

**443 Scanning Test System of p/sFEB for the ATLAS Phase-I sTGC Trigger Upgrade****Speaker**

Ms Xinxin Wang

15:22-15:24

**450 Longitudinal Mode-by-Mode Feedback System for The J-PARC Main Ring****Speaker**

Yasuyuki Sugiyama

15:24-15:26

**453 The design and performance of the ATLAS Inner Detector trigger in high pileup collisions at 13 TeV at the Large Hadron Collider****Speaker**

Pierre-Hugues Beauchemin

15:26-15:28

**473 Real time data analysis with the ATLAS trigger at the LHC in Run-2****Speaker**

Pierre-Hugues Beauchemin

15:28-15:30

**467 A readout method based on 10 gigabit Ethernet for silicon pixel detector****Speaker**

HangXu Li

15:30-15:32

**468 Technique of active phase stabilization for the interferometer with 128 actively selectable paths****Speaker**

Yu Xu

15:32-15:34

**470 Single photon source driver designed in ASIC****Speaker**

Bo Feng

15:34-15:36

**458 The Design and Testing of the Address in Real Time Data Driver Card for the Micromegas Detector of the ATLAS New Small Wheel Upgrade****Speaker**

Lin Yao

15:36-15:38

**477 Control and Readout Software in Superconducting Quantum Computing**

**Speaker**

Dr Jin Lin

15:38-15:40

**496 Scalable Self-Adaptive Synchronous Triggering System in Superconducting Quantum Computing**

**Speaker**

Dr Jin Lin

15:40-15:42

**478 Clock Distribution and Readout Architecture for the ATLAS Tile Calorimeter at the HL-LHC**

**Speaker**

Fernando Carrio Argos

15:42-15:44

**492 OpenCL implementation of an adaptive disruption predictor based on a probabilistic Venn classifier**

**Speaker**

Mr Enrique Bernal

15:44-15:46

**504 Real-time data compression for data acquisition systems applied to the ITER Radial Neutron Camera**

**Speaker**

Bruno Santos

16:10