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Java Driver Implementation for the Ethernet Flash ADC

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This paper describes the software developed for control configuration and data access of the Ethernet Flash ADC (EFADC) and the paired module known as the Ethernet Trigger Supervisor (ETS). The EFADC and ETS are a multiple-unit system designed to provide coincidence data acquisition in a modular form with 4 ns timing resolution. The paper briefly describes the EFADC and ETS hardware at the top level and delves into the detail of the control driver and the register architecture of each unit. Controllable functions of each module are discussed briefly while the methodology of configuration of the functions is describe in more detail. An overview of the API and its architecture is presented with some detail in the implementation of several of the objects detailed to clarify their functions. A brief review of the graphical user interface is also presented which has allowed the EFADC systems to be deployed in the field for plant biology applications and adopted for other data acquisition tasks.

Minioral

Yes

Description

Ethernet ADC

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