21st IEEE Real Time Conference - Colonial Williamsburg



Contribution ID: 440

Type: Poster presentation

Study on timing performance of a readout circuit for SiPM

Thursday 14 June 2018 15:50 (1 minute)

In recent years, SiPM photoelectric devices have drawn much attention in the domain of time-of-flight-based Positron emission tomography (TOF-PET). Using them to construct PET detectors with excellent coincidence time resolution (CTR) is always one of research focus. In this paper, a SiPM readout pre-amplifier based on common-base current amplifier structure followed by a Pole-Zero (PZ) compensation network is constructed, and the main factors that affect the timing performance of the PET detector are investigated. By experimental measurement, we found that the CTR is heavily related to the bandwidth of the amplifier, bias voltage of SiPM, comparator threshold, and PZ network parameter. The test setup has two detectors, one with LYSO crystal (3 mm x 3 mm x 10 mm) coupled with a Hamamatsu SiPM (S12642-0404), and the other with LaBr3 coupled to a PMT-R9800. After the optimization of the readout circuit with related factors, the CTR between the two detectors is measured as 266ps FWHM. The test result is a helpful guideline for the readout ASIC chip design in our next step.

Minioral

Yes

Description

SiPM timing

Speaker

Liwei Wang

Institute

USTC

Country

China

Author: Mr WANG, Liwei (Department of Modern Physics, University of Science and Technology of China)

Co-authors: Prof. WANG, Yonggang (Department of Modern Physics, University of Science and Technology of China); Mr CAO, Qiang (University of Science and Technology of China); Mr XIAO, Yong (Department of

Modern Physics, University of Science and Technology of China); Dr LIU, Chong (Department of Modern Physics, University of Science and Technology of China)

Presenters: Prof. WANG, Yonggang (Department of Modern Physics, University of Science and Technology of China); Mr CAO, Qiang (University of Science and Technology of China)

Session Classification: Poster 2

Track Classification: Front End Electronics and Fast Digitizers