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VIVADO High Level Synthesis in CLAS12 Trigger System Design

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VIVADO High Level Synthesis was used to design the first stage of the 3-staged CLAS12 Trigger System. It was implemented using Xilinx Virtex7-based VXS trigger processor board specifically designed to be used in the system. Trigger System was successfully complete and used during physics run in Jefferson Lab. Our experience with HLS is described.

Minioral

Yes

Description

Trigger

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