

## Session Program

5-10 Jun 2016



## 20th Real Time Conference

### *Mini Oral 1*

Padova, Italy

<https://goo.gl/maps/vWFxL> Centro Congressi A. Luciani Via Forcellini, 170/A Padova ITALY

# Tuesday 7 June

13:25

## Mini Oral 1

**Session** | **Location:** Padova, Centro Congressi | **Conveners:** Rejean Fontaine, Christian Boehm

13:25-13:27

### **085 TAWARA\_RT: A complete platform for a real time monitoring of contamination events of drinking water**

**Speaker**

Sandra Moretto

13:27-13:29

### **060 Development of ATLAS Liquid Argon Calorimeters Readout Electronics for HL-LHC**

**Speaker**

Kai Chen

13:29-13:31

### **084 Software Integrity Analysis Applied to IRIO EPICS Device Support Based On FPGA Real-Time DAQ Systems**

**Speaker**

Diego Sanz

13:31-13:33

### **018 Fuzzy-PID based heating control system**

**Speaker**

Jian Wang

13:33-13:35

### **012 High Speed Ethernet Application for the Trigger Electronics of the New Small Wheel**

**Speaker**

Kun Hu

13:35-13:37

### **232 NaNet: FPGA-based Network Interface Cards Implementing Real-time Data Transport for HEP Experiments**

**Speaker**

Michele Martinelli

13:37-13:39

### **004 A hardware implementation of the Levinson routine in a radio detector of cosmic rays to improve a suppression of the non-stationary RFI**

**Speaker**

Zbigniew Szadkowski

13:39-13:41

### **021 Adaptive IIR-notch filter for RFI suppression in a radio detection of cosmic rays**

**Speaker**

Zbigniew Szadkowski

13:41-13:43

**119 DCT trigger in a high-resolution test platform for a detection of very inclined showers in the Pierre Auger surface detectors****Speaker**

Zbigniew Szadkowski

13:43-13:45

**040 Implementation of ITER Fast Plant Interlock System using FPGAs with cRIO****Speaker**

Mariano Ruiz

13:45-13:47

**150 FPGA online tracking algorithm for the PANDA straw tube tracker****Speaker**

Yutie Liang

13:47-13:49

**095 Design and test of a GBTx based board for the upgrade of the ALICE TOF readout electronics****Speaker**

Davide Falchieri

13:49-13:51

**104 Exploring RapidIO technology within a DAQ system event building network****Speaker**

Simaolhoda Baymani

13:51-13:53

**076 Online calibration of the TRB3 FPGA TDC with DABC software****Speaker**

Joern Adamczewski-Musch

13:53-13:55

**039 FPGA Implementation of Toeplitz Hashing Extractor for Real Time Post-processing of Raw Random Numbers****Speaker**

xiaoguang zhang

13:55-13:57

**062 New LLRF control system at LNL****Speaker**

Stefano Pavinato

13:57-13:59

**011 An Extensible Induced Position Encoding Readout Method for Micro-pattern Gas Detectors****Speaker**

Binxiang Qi

13:59-14:01

**066 An I/O Controller for Real Time Distributed Tasks in Particle Accelerators****Speaker**

Davide Pedretti

14:01-14:03

**005 Design and Testing of the Bunch-by-Bunch Beam Transverse Feedback Electronics for SSRF****Speaker**

Mr Jinxin Liu

14:03-14:05

**054 Control system optimization techniques for real-time applications in fusion plasmas: the RFX-mod experience****Speaker**

Leonardo Pigatto

14:05-14:07

**061 High-speed continuous DAQ system for reading out the ALICE SAMPA ASIC****Speaker**

Ganesh Jagannath Tambave

14:07-14:09

**046 Brain Emulation for Image Processing****Speaker**

Pierluigi Luciano

14:09-14:11

**176 Readout electronics for Belle II imaging Time of Propagation detector****Speaker**

Dr Dmitri Kotchetkov

14:11-14:13

**010 An  $\alpha/\gamma$  Discrimination Method for BaF2 Detector by FPGA-based Linear Neural Network****Speaker**

Chenfei Yang

14:13-14:15

**007 Design of the Readout Electronics Prototype for LHAASO WCDA****Speaker**

Ma Cong

14:15-14:17

**053 A Calculation Software Based on Pipe-and-Filter Architecture for the  $4\pi\beta\text{-}\gamma$  Digital Coincidence Counting Equipment****Speaker**

Mr Zhiguo Ding

14:17-14:19

**229 Evaluation of 100 Gb/s LAN networks for the LHCb DAQ upgrade****Speaker**

Balazs Voneki

14:19-14:21

**031 TaskRouter: A newly designed online data processing framework****Speaker**

Minhao Gu

14:21-14:23

**114 A High Frame Rate Test System for The HEPS-BPIX based on NI-sbRIO Board**

**Speaker**

Jingzi Gu

14:23-14:25

**192 Particle identification on an FPGA accelerated compute platform for the LHCb Upgrade.****Speaker**

Christian Faerber

14:25-14:27

**074 Field Waveform Digitizer for BaF2 Detector Array at CSNS-WNS****Speaker**

qi wang

14:27-14:29

**109 Signal Processing Scheme for a Low Cost LiF:ZnS(Ag) Neutron Detector with Silicon Photomultiplier****Speaker**

Mr Kevin Pritchard

14:29-14:31

**213 Design of a Compact Hough Transform for a new L1 Trigger Primitives Generator for the upgrade of the CMS Drift Tubes muon detector at the HL-LHC****Speaker**

Nicola Pozzobon

14:31-14:33

**248 White Rabbit based sub-nsec time synchronization, time stamping and triggering in distributed large scale astroparticle physics experiments****Speaker**

Martin Brückner

14:33-14:35

**086 Enabling Real Time Reconstruction for High Resolution SPECT Systems****Speaker**

Mélanie Bernard

14:35-14:37

**135 Modular Software for MicroTCA.4 Based Control Applications****Speaker**

Nadeem Shehzad

14:45