Session Program

5-10 Jun 2016

20th Real Time Conference

Mini Oral 3

Padova, Italy
Centro Congressi A. Luciani Via Forcellini, 170/A Padova ITALY

Thursday 9 June

14:15

Mini Oral 3

Session | **Location:** Padova, Centro Congressi | **Conveners:** Jinyuan Wu, Mr Pierre-Andre Amaudruz

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14:15–14:17   **051 Design of a 10-Gbps Random Number Recorder**

Speaker
Xinzhe Wang

14:17–14:19   **016 10-Gbps True Random Number Generator Accomplished in ASIC**

Speaker
Xinzhe Wang

14:19–14:21

195 20-Channel_14MeV_Neutron_Detector_Electronics_Readout_System

Speaker
Shengquan Liu

14:21–14:23

097 Real-time vertical plasma position control using the heavy ion beam diagnostic

Speaker
Rafael Henriques

14:23–14:25   **199 Data acquisition system for the J-PARC E36 experiment**

Speaker
Dr Youichi IGARASHI

14:25–14:27

034 A Reconfigurable Ethernet-Based Data Acquisition and Processing System for Particle Physics Experiments

Speaker
Mr Zhaoqi Wang

14:27–14:29

165 A cosmic ray readout system for qualifications of small-strip Thin Gap Chambers of the ATLAS Muon Spectrometer Phase-I upgrade

Speaker
Xu Wang

14:29–14:31

032 High Counting-rate Data Acquisition System for the Applications of PGNAA

Speaker
yuzhe liu

14:31–14:33

001 An Energy Measurement Method of High-frequency Narrow Laser Pulse based on FPGA

Speaker

Jian Wang

14:33–14:35

003 Design of ultra-low noise power system for high-precision detectors

Speaker

Jian Wang

14:35–14:37

002 A DAQ Prototype for the ATLAS small-strip Thin Gap Chamber Phase-I Trigger Upgrade

Speaker

Kun Hu

14:37–14:39 **143 Intelligent FPGA Data Acquisition Framework**

Speaker

Dmytro Levit

14:39–14:41

221 Multi-Channel FPGA-Based Data-Acquisition-System for Time-Resolved Single-Photon Counting in Synchrotron Radiation Experiments.

Speaker

Michael Ziolkowski

14:41–14:43 **204 Portable calibration node for LHAASO KM2A detector array**

Speaker

hongming Li

14:43–14:45

013 Preliminary Performance of a Continuous Crystal PET Detector with TODT Readout Scheme

Speaker

Yong Xiao

14:45–14:47

260 Analog Data Acquisition and Processing FPGA-based Solutions Integrated in AreaDetector using FlexRIO technology

Speaker

Sergio Esquembri Martínez

14:47–14:49 **068 Complete Parallel Readout VME DAQ System**

Speaker

Hidetada Baba

14:49–14:51

159 Intelligent FPGA based Event Builder and Data Acquisition System for the COMPASS experiment.

Speaker

Igor Konorov

14:51–14:53

056 mbspex and pexornet - linux device drivers for PCIe optical receiver data acquisition and contro

Speaker

Joern Adamczewski-Musch

14:53–14:55

206 An Ultra-fast Linear Array Detector for MHz Line Repetition Rate Spectroscopy

Speaker

Michele Caselle

14:55–14:57

047 ELECTRONICS FOR HARPO - Design, development and validation of electronics for a high performance polarized γ-ray detector.

Speaker

Mr Yannick Geerebaert

14:57–14:59   **073 The Implementation of KTX Central Control System**

Speaker

Zuchao Zhang

14:59–15:01

026 A readout system for the Ground Tests of the DAMPE BGO Calorimeter

Speaker

Junbin Zhang

15:01–15:03

269 Development of a high dynamic range and wide bandwidth amplifier electronics system

Speaker

Cheng Li

15:03–15:05

009 Two-dimensional encoded multiplexing readout with a 5x5cm2 THGEM

Speaker

guangyuan yuan

15:05–15:07

042 PLAS: A 32-channel, dead time-less analog memory ASIC for the TRACE detector

Speaker

Ramon J. Aliaga

15:07–15:09

249 New updates on the ATLAS ROD board implementation for Pixel Layer 1 and Layer 2

Speaker

Nico Giangiacomi

15:09–15:11   **227 The laser control system for a calibration facility of light detector**

Speaker

Stefano Mastroianni

15:11–15:13

228 The monitoring board for the calibration system of the g-2 experiment

Speaker
Stefano Mastroianni

15:20