Session Program

Jun 5 – 10, 2016

20th Real Time Conference

Mini Oral 3

Padova, Italy
Centro Congressi A. Luciani Via Forcellini, 170/A Padova ITALY

Thu, June 9

2:15 PM

Mini Oral 3

Session | **Location:** Padova, Centro Congressi | **Conveners:** Mr Pierre-Andre Amaudruz, Jinyuan Wu

2:15 – 2:17 PM **051 Design of a 10-Gbps Random Number Recorder**

Speaker
Xinzhe Wang

2:17 – 2:19 PM **016 10-Gbps True Random Number Generator Accomplished in ASIC**

Speaker
Xinzhe Wang

2:19 – 2:21 PM
195 20-Channel_14MeV_Neutron_Detector_Electronics_Readout_System

Speaker
Shengquan Liu

2:21 – 2:23 PM
097 Real-time vertical plasma position control using the heavy ion beam diagnostic

Speaker
Rafael Henriques

2:23 – 2:25 PM **199 Data acquisition system for the J-PARC E36 experiment**

Speaker
Dr Youichi IGARASHI

2:25 – 2:27 PM
034 A Reconfigurable Ethernet-Based Data Acquisition and Processing System for Particle Physics Experiments

Speaker
Mr Zhaoqi Wang

2:27 – 2:29 PM
165 A cosmic ray readout system for qualifications of small-strip Thin Gap Chambers of the ATLAS Muon Spectrometer Phase-I upgrade

Speaker
Xu Wang

2:29 – 2:31 PM
032 High Counting-rate Data Acquisition System for the Applications of PGNAA

Speaker
yuzhe liu

2:31 – 2:33 PM
001 An Energy Measurement Method of High-frequency Narrow Laser Pulse based on FPGA

Speaker
Jian Wang

2:33 – 2:35 PM
003 Design of ultra-low noise power system for high-precision detectors

Speaker
Jian Wang

2:35 – 2:37 PM
002 A DAQ Prototype for the ATLAS small-strip Thin Gap Chamber Phase-I Trigger Upgrade

Speaker
Kun Hu

2:37 – 2:39 PM **143 Intelligent FPGA Data Acquisition Framework**

Speaker
Dmytro Levit

2:39 – 2:41 PM
221 Multi-Channel FPGA-Based Data-Acquisition-System for Time-Resolved Single-Photon Counting in Synchrotron Radiation Experiments.

Speaker
Michael Ziolkowski

2:41 – 2:43 PM **204 Portable calibration node for LHAASO KM2A detector array**

Speaker
hongming Li

2:43 – 2:45 PM
013 Preliminary Performance of a Continuous Crystal PET Detector with TODT Readout Scheme

Speaker
Yong Xiao

2:45 – 2:47 PM
260 Analog Data Acquisition and Processing FPGA-based Solutions Integrated in AreaDetector using FlexRIO technology

Speaker
Sergio Esquembri Martínez

2:47 – 2:49 PM **068 Complete Parallel Readout VME DAQ System**

Speaker
Hidetada Baba

2:49 – 2:51 PM
159 Intelligent FPGA based Event Builder and Data Acquisition System for the COMPASS experiment.

Speaker
Igor Konorov

2:51 – 2:53 PM
056 mbspex and pexornet - linux device drivers for PCIe optical receiver data acquisition and contro

Speaker
Joern Adamczewski-Musch

2:53 – 2:55 PM
206 An Ultra-fast Linear Array Detector for MHz Line Repetition Rate Spectroscopy

Speaker
Michele Caselle

2:55 – 2:57 PM
047 ELECTRONICS FOR HARPO - Design, development and validation of electronics for a high performance polarized γ-ray detector.

Speaker
Mr Yannick Geerebaert

2:57 – 2:59 PM **073 The Implementation of KTX Central Control System**

Speaker
Zuchao Zhang

2:59 – 3:01 PM
026 A readout system for the Ground Tests of the DAMPE BGO Calorimeter

Speaker
Junbin Zhang

3:01 – 3:03 PM
269 Development of a high dynamic range and wide bandwidth amplifier electronics system

Speaker
Cheng Li

3:03 – 3:05 PM
009 Two-dimensional encoded multiplexing readout with a 5x5cm2 THGEM

Speaker
guangyuan yuan

3:05 – 3:07 PM
042 PLAS: A 32-channel, dead time-less analog memory ASIC for the TRACE detector

Speaker
Ramon J. Aliaga

3:07 – 3:09 PM
249 New updates on the ATLAS ROD board implementation for Pixel Layer 1 and Layer 2

Speaker
Nico Giangiacomi

3:09 – 3:11 PM
227 The laser control system for a calibration facility of light detector

Speaker
Stefano Mastroianni

3:11 – 3:13 PM
228 The monitoring board for the calibration system of the g-2 experiment

3:20 PM

Speaker
Stefano Mastroianni