

## Session Program

5-10 Jun 2016



## 20th Real Time Conference

### ***RTA 1***

Padova, Italy

<https://goo.gl/maps/vWFxL>>Centro Congressi A. Luciani Via Forcellini, 170/A Padova ITALY</a>

## Thursday 9 June

10:40

### RTA 1

**Session** | **Location:** Padova, Centro Congressi | **Conveners:** Kay Rehlich, Jinyuan Wu

10:40–11:00

#### Control System of the European XFEL Accelerator

**Speaker**

Kay Rehlich

11:00–11:20

#### A JESD204B-compliant Architecture for Remote and Deterministic-Latency Operation

**Speaker**

Dr Raffaele Giordano

11:20–11:40

#### IRIO Technology: developing applications for Advanced DAQ systems using FPGAs

**Speaker**

Mariano Ruiz

11:40–12:00

#### An FPGA-Based Track Finder for the L1 Trigger of the CMS Experiment at the High Luminosity LHC

**Speaker**

Thomas Schuh

12:00