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An FPGA-Based Track Finder for the L1 Trigger of the CMS Experiment at the High Luminosity LHC

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A new tracking system is under development for operation in the CMS experiment at the High Luminosity LHC. It includes an outer tracker which will construct stubs, built by correlating clusters in two closely spaced sensor layers for the rejection of hits from low transverse momentum tracks, and transmit them off-detector at 40 MHz. If tracker data is to contribute to keeping the Level-1 trigger rate at around 750 kHz under increased luminosity, a crucial component of the upgrade will be the ability to identify tracks with transverse momentum above 3 GeV/c by building tracks out of stubs. A concept for an FPGA-based track finder using a fully time-multiplexed architecture is presented, where track candidates are identified using a projective binning algorithm based on the Hough Transform. A hardware system based on the MP7 MicroTCA processing card has been assembled, demonstrating a realistic slice of the track finder in order to help gauge the performance and requirements for a full system. This paper outlines the system architecture and algorithms employed, highlighting some of the first results from the hardware demonstrator and discusses the prospects and performance of the completed track finder

Authors: JAMES, Thomas Owen (Imperial College Sci., Tech. & Med. (GB)); SCHUH, Thomas (KIT - Karlsruhe Institute of Technology (DE))

Co-authors: TAPPER, Alex (Imperial College Sci., Tech. & Med. (GB)); MORTON, Alexander (Brunel University (GB)); ROSE, Andrew William (Imperial College Sci., Tech. & Med. (GB)); SHTIPLIYSKI, Antoni (Imperial College Sci., Tech. & Med. (GB)); AMSTUTZ, Christian (Karlsruhe Institute of Technology); SHEPHERD-THEMISTOCLEOUS, Claire (STFC - Rutherford Appleton Lab. (GB)); Prof. NEWBOLD, Dave (University of Bristol (GB) / Rutherford Appleton Laboratory (GB)); CIERI, Davide (STFC - Rutherford Appleton Lab. (GB)); CLEMENT, Emyr (University of Bristol (GB)); BALL, Fionn Amhairghen (University of Bristol (GB)); HALL, Geoff (Imperial College Sci., Tech. & Med. (GB)); ILES, Gregory Michiel (Imperial College Sci., Tech. & Med. (GB)); TOMALIN, Ian (STFC - Rutherford Appleton Lab. (GB)); Dr REID, Ivan (Brunel University London (GB)); BROOKE, Jim (University of Bristol (GB)); UCHIDA, Kirika (Imperial College Sci., Tech. & Med. (GB)); MANOLOPOULOS, Konstantinos (STFC - Rutherford Appleton Lab. (GB)); HARDER, Kristian (STFC - Rutherford Appleton Lab. (GB)); CALLIGARIS, Luigi (STFC - Rutherford Appleton Lab. (GB)); WEBER, Marc (KIT - Karlsruhe Institute of Technology (DE)); PESARESI, Mark (Imperial College Sci., Tech. & Med. (GB)); BALZER, Matthias Norbert (KIT - Karlsruhe Institute of Technology (DE)); SANDER, Oliver (KIT - Karlsruhe Institute of Technology (DE)); VICHODIS, Paschalis (CERN); HOBSON, Peter (Brunel University (GB)); SUMMERS, Sioni Paris (Imperial College Sci., Tech. & Med. (GB)); PARAMESVARAN, Sudarshan (University of Bristol (GB)); MATSUSHITA, Takashi (Imperial College); HARBAUM, Tanja Renate (KIT - Karlsruhe Institute of Technology (DE))

Presenter: SCHUH, Thomas (KIT - Karlsruhe Institute of Technology (DE))

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