

Fast Intra Bunch Train Charge Feedback for FELs based on Photo Injector Laser **Pulse Modulation**



Motivation



Modern high repetition rate Free Electron Lasers (FELs) are driven by a high energy electron beam generated by a superconducting accelerator operated in pulsed mode. Electron bunches are produced by a complex photoinjector system which can generate long electron bunch trains.

Intra bunch train charge instabilities disturb the longitudinal phase space of the electron bunches introducing instabilities in bunch peak current, pulse duration and shape.

Noticeable charge oscillations and a charge slope along the bunch trains in the range from 0.5 % to 3 % are observed. They are caused mainly by the photo-injector laser, but also by instabilities in the RF-gun accelerating field and time dependent effects in the photo emission process.



Intra train charge variation over a train with 200 bunches observed in FLASH (charge slope artificially amplified).

Intra Bunch Train Charge Stabilization System

In order to stabilize charge in the bunch trains generated by Photo-Injector Laser System dedicated system has been designed. Operation principle: the electron bunch charge is read by a toroid, the feedback system acts on a Pockels cell modulating the laser polarization and thus, with a subsequent polarizer the laser energy.

The feedback loop delay must be smaller than 1 μ s, the distance between bunches in the train.

Main components of the system are:

- Real time charge detection module
- Fast Pockels cell controller module
- BBO Pockels cell and amplifier

Modern electronic components built according to Micro Telecommunication Computing Architecture (MTCA.4) standard has been employed in the system.

Detector module: SIS8300+SIS8900 with LLL connection

- Real-time charge detection module based on MTCA.4 AMC board - SIS8300 (Struck Innovative Systeme GmbH)
- Detection algorithm implemented in Virtex 5 FPGA Dedicated high level control software (DOOCS server) for detection parameters setting and system monitoring Charge data send to Pockels cell Controller module with fast (3,25 GB/s) fiber low latency link

Pockels cell Controller module: SIS8300L with LLL connection

- Pockels cell controller based on MTCA.4 AMC board - SIS8300L (Struck Innovative Systeme GmbH)
- Control algorithm implemented in Virtex 6 FPGA

L1 b1 Q=0.261 nC ms=4.097 pC (1.57%) bg=3.777 pC ms=0.202 pC

Time (sec)

Example of a charge stability measurement.

System OFF

ain <Q> =0.262 nC rms=0.993 pC (0.378%)

b6 <Q>=0.255 nC rms=1.771 pC (0.695%)

ntra-train x10 <rms>=0.004 nC

System ON

τ^ο 0.15

0.1

- 0.05 -

Dedicated high level control software (DOOCS server) for data acquisition and monitoring, Learning Feed Forward (LFF) and Setpoint Correction (SC) calculations



Block diagram of the FLASH Photoinjector and the intra bunch train charge stabilization system.

Actuator module: Pockels cell with the amplifier

- 5 mm aperture BBO Pockels cell for 262 nm (LEYSOP LTD, England) installed.
- Bandwidth: about 7 MHz.
- High power amplifier (LEYSOP LTD, England) •



First results and plans

Results

- in installed FLASH the System has been photoinjector laser.
- First system tests have been performed in March and April 2016. During tests in March laser system was less stable than during the April tests. A charge oscillations of unknown source were • observed. In both cases, the charge stability and train flatness \bullet could be improved by the feedback system.



providing correct operation voltages for Pockels cell



System installed in PIL 2 of FLASH



Pockels cell setup in UV laser beam optical path



Pockels cell high voltage amplifier



email: tomasz.kozak@desy.de

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0.315

0.31-

0.305-

0.3-

Bunch train (200 pulses) with visible oscillations.

1000 1100 1200 Oscillations minimalized by Intra-Bunch-Train Feedback System

Plans

- Currently the main limitation of the system seems to the charge detection: high noise level, detection algorithm.
- In June new toroid electronics with less noise will be installed.
- New detection algorithms are planned to be developed and tested.
- More tests of the system are required.

