## 20th Real Time Conference



Contribution ID: 269

Type: Poster presentation

## Development of a high dynamic range and wide bandwidth amplifier electronics system

Friday 10 June 2016 10:30 (1h 35m)

In modern physics experiment, data acquisition is required to readout high dynamic range and wide bandwidth detector signal, especially from direct current (DC) or ultra-low frequency (ULF). In this paper, a development of amplifier electronics system is presented to comply with the requirements of analog signal with DC or ULF to 250MHz signal bandwidth and 60dB dynamic range.

The core of the system is based on a programmable gain amplifier (PGA), whose gain is programmable and variable according to its application scenarios. The prototype amplifier electronics include programmable attenuator, programmable gain amplifier, differential amplifier and low-pass filter (LPF). This system also includes a specifically designed remote control board, which is composed by a microcontroller STM32 chip, control interface and necessary communication ports. In order to achieve a high performance, several boards of different schemes based on the structure are designed and compared.

Test results show that the amplifier electronics system has been proven to perform well. It can be of great analog signal processing performance in Ultra-fast ADC system and Switched Capacitor Arrays (SCA).

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**Session Classification:** Poster Session 2

Track Classification: Front End Electronics and Fast Digitizers