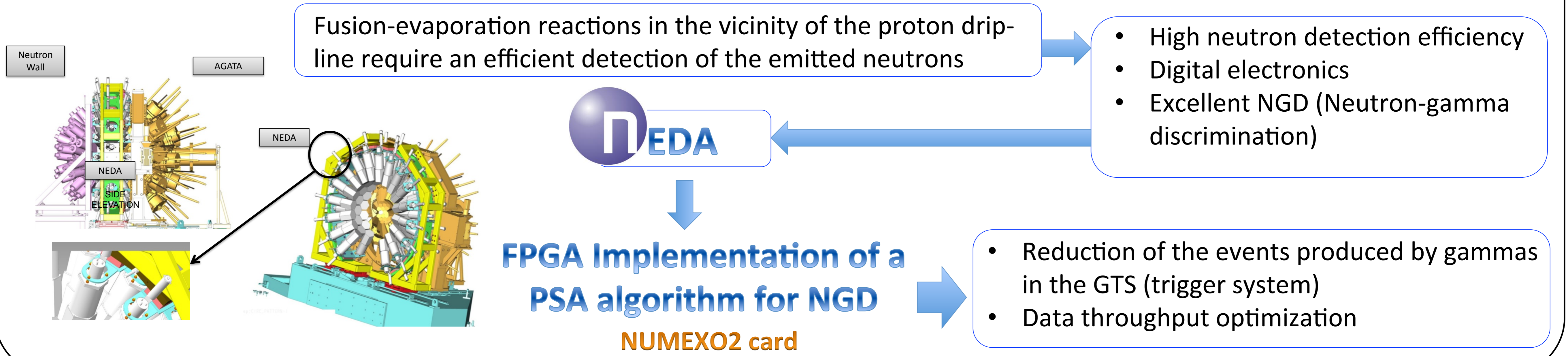


An FPGA Implementation of a Neutron-Gamma Discrimination Algorithm for the NEDA Neutron Detector

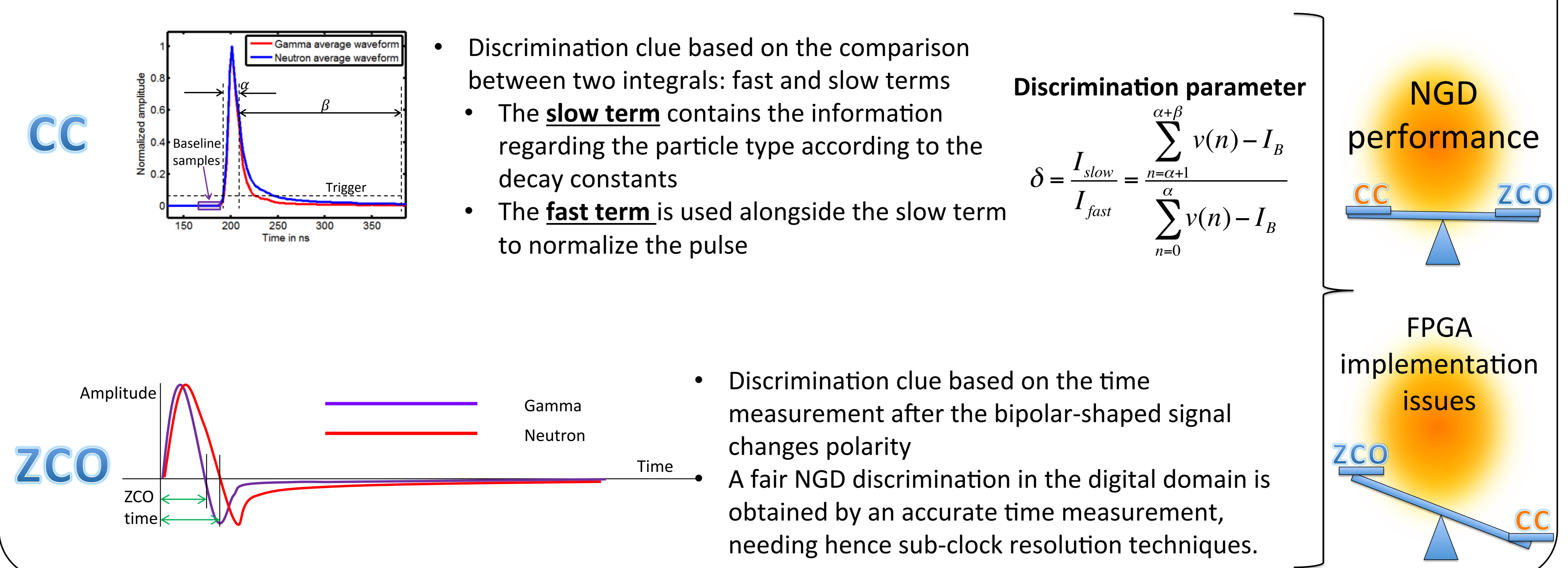
F.J. Egea, C. Houarner, V. González, P-A Söderström, J. Nyberg, M. Tripon, A. Boujrad, A. Gadea, M. Jastrzab, J.J. Valiente-Dobón, G. de France, I. Lazarus, G. Jaworski, T. Hüyük, A. Triossi, X.L. Luo, M.N. Erduran, S. Ertürk, M. Moszyński, V. Modamio, M. Palacz, E. Sanchis, R. Wadsworth



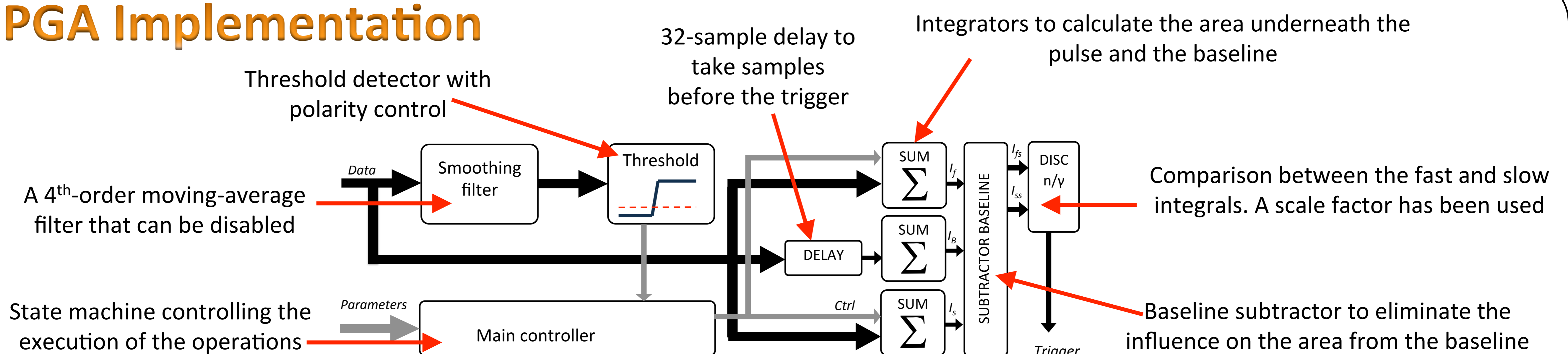
Background: The NEDA neutron detector



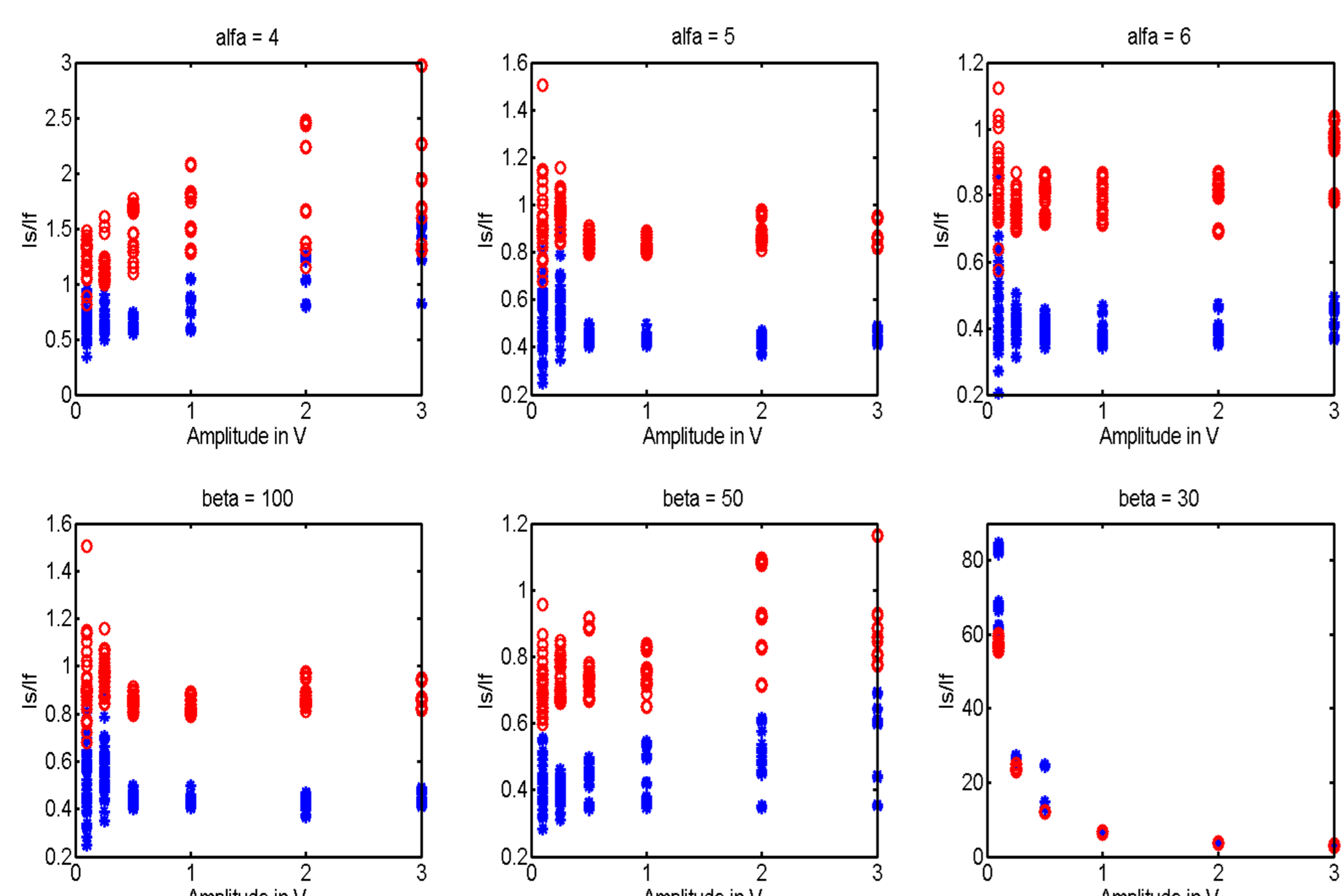
Algorithms: Zero Crossover (ZCO) vs charge comparison (CC)



FPGA Implementation



Preliminary results and future overview



The first algorithm verification tests were carried out using real samples from a PMT XP4512 and an arbitrary waveform generator 33522A able to reproduce them.

Besides, the algorithm was tested at high counting (but periodic) rates between 10 kHz and 50 kHz. Some of the patterns used involved only-gammas, only-neutrons and alternating patterns

For values of $\alpha = 20$ and 25 ns, and $\beta = 500$ ns and 250 ns we observed the algorithm best behavior in terms of discrimination

Next future tests foresee a ^{252}Cf with a NEDA detector. Those will be performed along the 2016.