Session Program

28 April 2016

2nd FTEC Workshop

Electronics Engineering

CERN, 40/2-A01 40-2-A01

Thursday 28 April

10:25 Electronics Engineering

Session | Location: CERN, 40/2-A01, 40-2-A01 | Convener: Luis Garcia-Tabares

10:25-10:35

Design, production and test of readout electronics for the assessment of the radiation hardness of pixel sensor based detector components for the ALICE ITS Upgrade project (PH3462)

Speaker

Andres Sanchez Gonzalez

10:35-10:45

Modelling and testing of circuit protection of new superconducting magnets for the HL-LHC project (TE3544)

Speaker Alejandro Manuel Fernandez Navarro

10:45-10:55

Development of electronics for Radiation Monitoring and Solid State Detector R&D (PH3472)

Speaker

Isidro Mateu Suau

10:55-11:05

Radiation hard electronics for ATLAS and CMS pixel detector upgrades (PH3468)

Speaker

Luis Miguel Jara Casas

11:05-11:15 Versatile Link Demonstrator Board (PH3464)

Speaker

Raul Martin Lesma

11:15-11:25 **Construction and commissioning of electronic test benches (BE3526)**

Speaker

Mr Josue Rodriguez Diez

11:30