

Session Program

27 October 2015

1st FTEC Workshop

Electronics Engineering

CERN, 40/S2-D01 - Salle Dirac
40-S2-D01

Tuesday 27 October

09:15

Electronics Engineering

Session | **Location:** CERN, 40/S2-D01 - Salle Dirac, 40-S2-D01 | **Convener:** Luis Garcia-Tabares

09:15–09:25

Construction and commissioning of electronic test benches (BE3526)

Speaker

Mr Josue Rodriguez Diez

09:25–09:35

Versatile Link Demonstrator Board (PH3464)

Speaker

Raul Martin Lesma

09:35–09:45

Radiation hard electronics for ATLAS and CMS pixel detector upgrades (PH3468)

Speaker

Luis Miguel Jara Casas

09:45–09:55

Development of electronics for Radiation Monitoring and Solid State Detector R&D (PH3472)

Speaker

Isidro Mateu Suau

09:55–10:05

Modelling and testing of circuit protection of new superconducting magnets for the HL-LHC project (TE3544)

Speaker

Alejandro Manuel Fernandez Navarro

10:05–10:15

Design, production and test of readout electronics for the assessment of the radiation hardness of pixel sensor based detector components for the ALICE ITS Upgrade project (PH3462)

Speaker

Andres Sanchez Gonzalez

10:30