

Tuesday 2 May



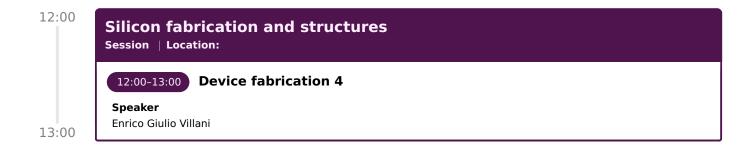
Friday 5 May



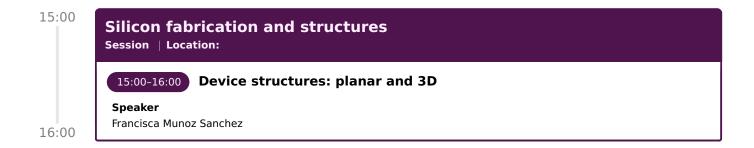
Tuesday 9 May



Friday 12 May



Tuesday 16 May



Friday 19 May

Silicon fabrication and structures
Session | Location:

11:00-12:00 Device structures: Gain Layers (LGAD, SPAD, SiPM)

Speaker
Francisca Munoz Sanchez

Tuesday 23 May



Friday 26 May

Silicon fabrication and structures
Session | Location:

12:00-13:00 Transistor layout: 250 - 28 nm, FinFETs
Speaker
Eva Vilella Figueras