

The ATLAS HGTD - Status and Updates and Exploring the response of LGADs for time resolved synchrotron applications



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IX Reunião Geral - Projeto Especial FAPESP "Física e Instrumentação de Altas Energias com o LHC-CERN"

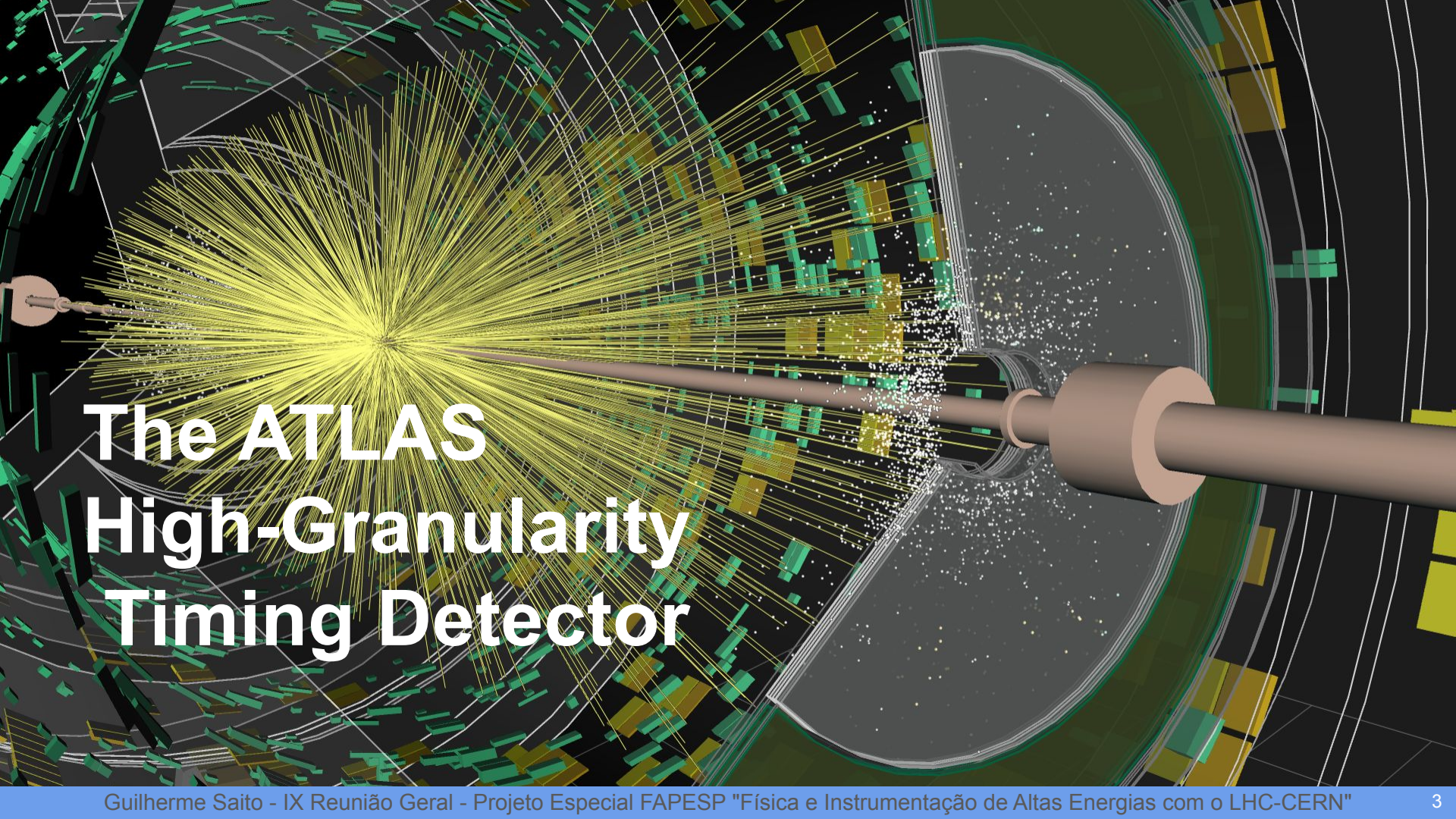


27 May 2026



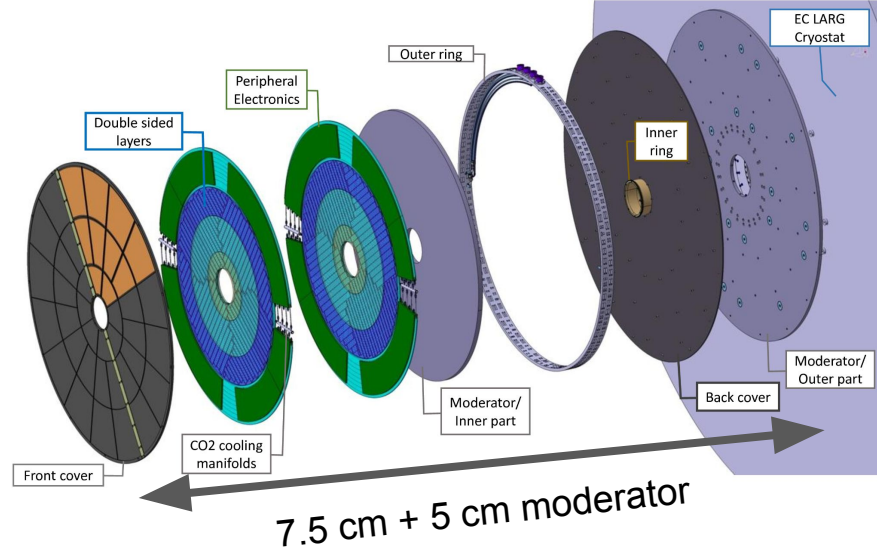
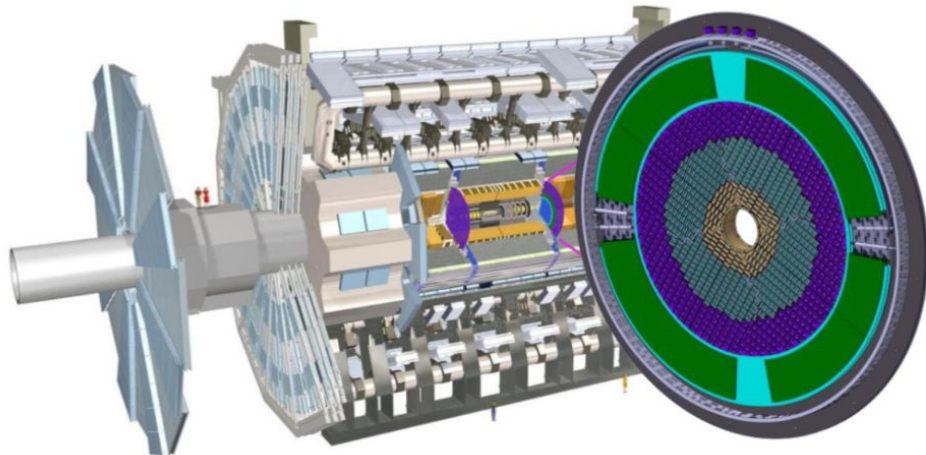
On this presentation

- The ATLAS High-Granularity Timing Detector
 - Project status
 - Sensor delivery
 - Sensors and wafers quality control at USP and CERN
- LGADs for time resolved Synchrotron X-ray applications
 - Beam testing campaigns in Sirius
 - New results

A 3D visualization of the ATLAS High-Granularity Timing Detector. The image shows a central beam pipe with a dense spray of yellow particle tracks radiating outwards. The detector is composed of various layers, including a central silicon pixel detector and an outer layer of calorimeters. The background is dark, highlighting the detector's structure and the particle tracks.

The ATLAS High-Granularity Timing Detector

High-Granularity Timing Detector



Mitigate pileup using 4D information on hits/tracks

- See Rodrigo's talk for more on pileup mitigation

- Placed between ITk and LAr Calorimeter
- Active area coverage: $2.4 < |\eta| < 4.0$
- Track-time resolution: **30 ps** (start) - 50 ps (end)
- **6.4 m² of silicon sensors**
- **8032 modules** (2 LGAD sensors arrays + 2 ASICs)

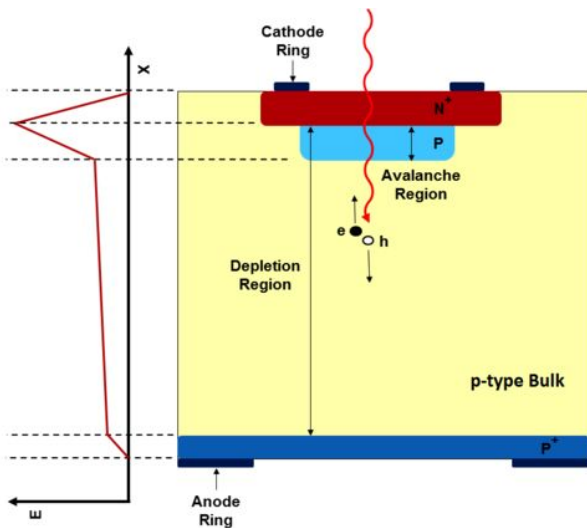
- Radiation hardness requirements (with replacements):
 - ◆ Maximum fluence: $2.5 \times 10^{15} n_{eq}/cm^2$ with SF=1.5
 - ◆ TID: **2 MGy** with SF=2.25
- Two instrumented double layers per side
- Layout optimised to have **~2 hits per track**
- Unique capability for **bunch-by-bunch luminosity determination**

HGTD Sensors

Low Gain Avalanche Detector (LGAD):

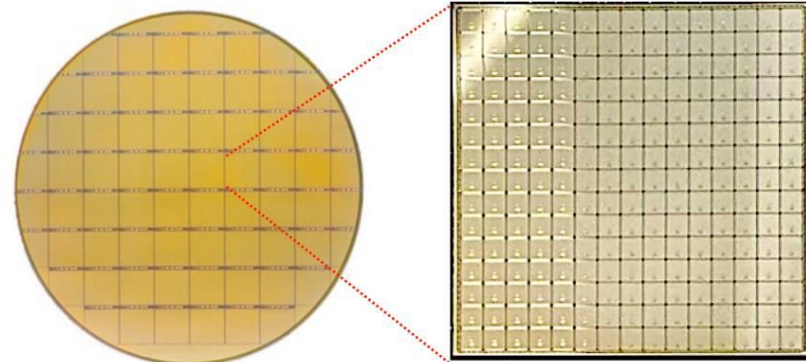
- n-in-p diode structure
 - Extra p-type gain-layer enabling
 - Moderate gain 10-20
- Larger signal-to-noise ratio and fast rise time

= Excellent time resolution



8" sensor wafer

15 x 15 pads



20 mm

HGTD LGAD:

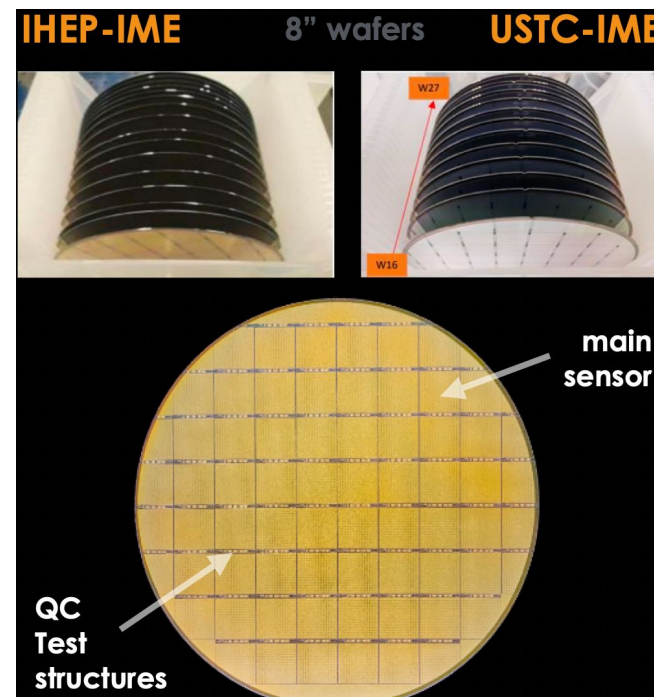
- 15 × 15 pads
- Pad size: 1.3 mm × 1.3 mm
- 3.6 M channels
- Active thickness: 50 μ m
- Carbon implanted gain-layer to withstand radiation

Requirements:

- Hit-time resolution: 40 ps (start) - 70 ps (end)
- Collected charge per hit: 10 fC (start) - 4 fC (end)

HGTD Sensors Production

- 0(600) 8" wafers to be produced for HGTD by IME (Chinese Academy of Science)
- 16000 sensors to be delivered to HGTD:
 - 90% IHEP-IME (China)
 - 10% USTC-IME (China)
- 569 sensors already delivered (Pre-Production)
 - Pre-production of 24 wafers already performed, checked and validated
- Yield on the pre-production wafers was 40-50%
 - But, HGTD buys sensors not wafers



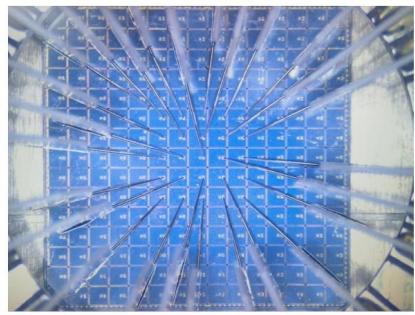
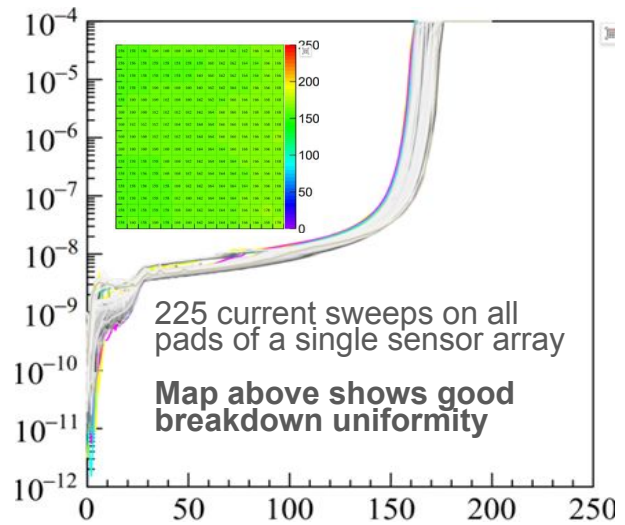
Sensor and Wafer Quality Control

Quality control on **sensor level**:

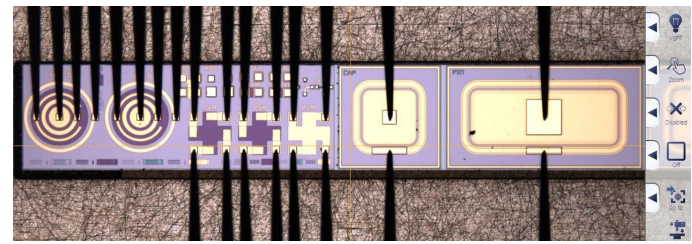
- Only measure Breakdown Voltage and Leakage Current
 - Before company delivery: **on all sensors** with probe card
 - On selected sensors: **pad by pad** (at IHEP and USTC)

Quality control on **wafer level**:

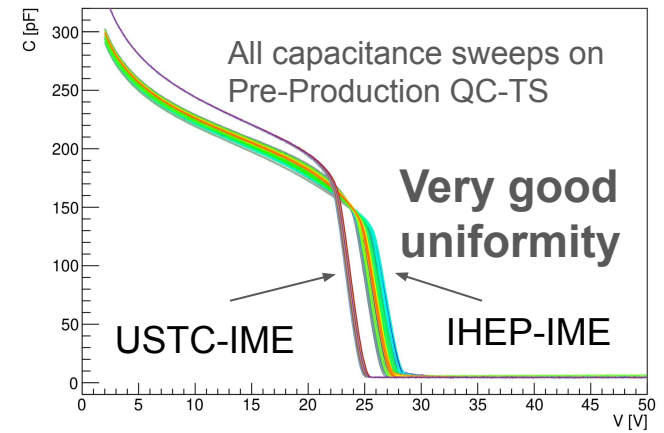
- Dedicated Quality Control Test Structures (QC-TS)
 - **Monitor all available electrical parameters** across the wafer (USP and CERN - 50% each)
 - **Ensures LGAD performance** after irradiation (at JSI)



Probe-card measurement on **main sensor**



Probe-card measurement on **QC-TS**



Sensor and Wafer Production

- As of May 2026:
 - USP and CERN qualified >220 wafers [% of total]
 - Batches of ~100 wafers
 - >2000 Quality Control Test Structures at each site
 - ~6000 good sensors delivered to the HGTD collaboration
- Every batch results from USP, CERN and JSI are fed back to the vendor in dedicated meetings
- Different kinds of excursions:
 - Singular outlier wafers and production trends observed
- No public results yet, all internal until paper publication



3rd Batch of HGTD LGAD Wafer structures sent to USP

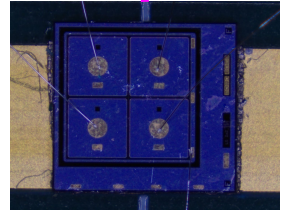
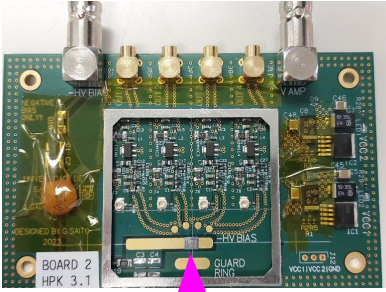
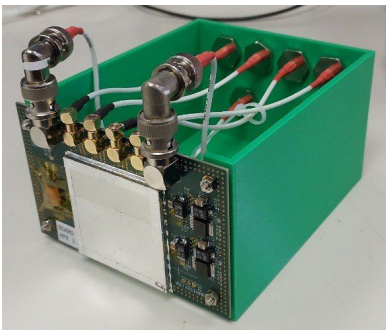
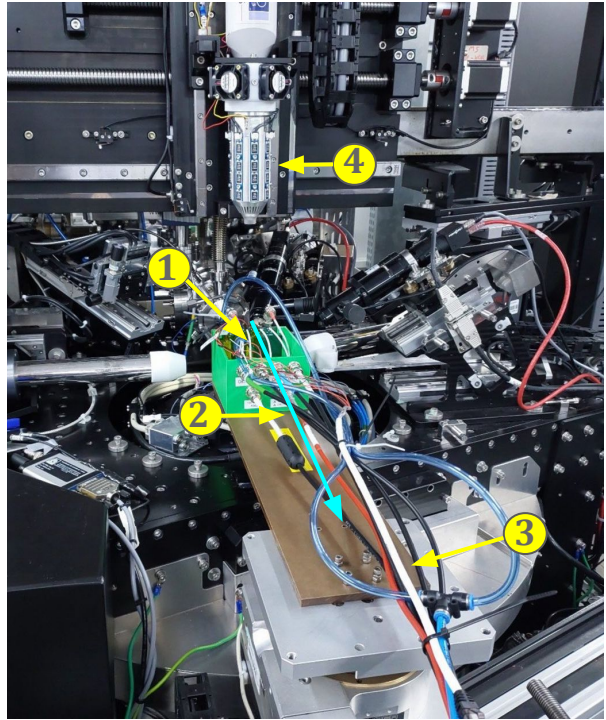
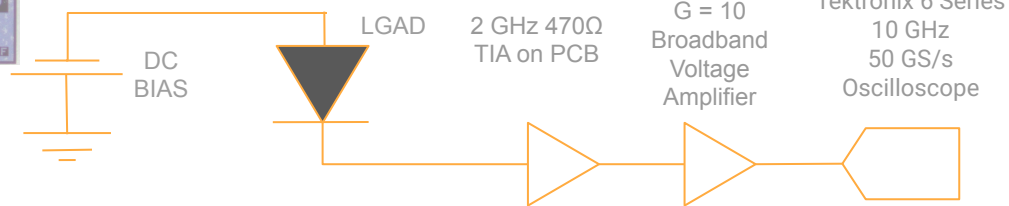
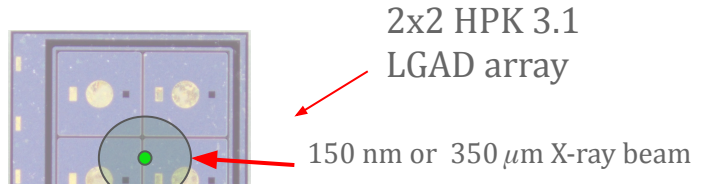


A close-up photograph of a custom electronic board. The board is dark blue and populated with numerous gold-plated pins. A prominent feature is a yellow, rectangular LGAD (Large Area Silicon Detector) sensor mounted on the board. Several gold-plated connectors are visible, with yellow and blue cables plugged into them. The board is housed in a green plastic enclosure. Text on the board includes "UNIVERSIDADE DE SAO PAULO" and "Guilherme SAITO". A date stamp "November 2025" is also visible. The board is mounted on a metal base.

Exploring LGADs for time resolved synchrotron applications

Sirius Carnaúba Beamline

- ATLAS HGTD HPK and IHEP and ① LGAD FBK TI-LGAD sensors tested
 - Beam size 350 μm or 150 nm
 - Detector can move and rotate wrt to the beam
 - Energy/timing resolution wrt X-ray energy, bias and temperature
 - EPIC by Sirius to control/store conditions
- ① LGAD
 - ② Beam direction
 - ③ Linear stage
 - ④ Cooling nozzle



Beam tests

Last 12 months beam tests:

→ **October 2025 - 2 days**

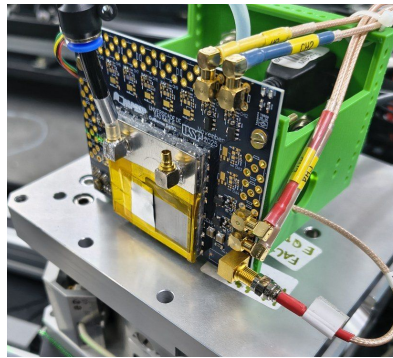
IHEP HGTD LGAD
HPK HGTD LGAD
FBK TI-LGAD

- ◆ Energy resolution and response/linearity as function of bias voltage / X-ray energy
- ◆ Absolute and relative time resolution as function of bias voltage / X-ray energy
- ◆ **Spatial resolution scan** of TI-LGAD
 - new result - first with $<1\mu\text{m}$ beam
- ◆ **Full beam intensity illumination**
 - new result - first LGAD $\sim 500\text{MHz}$ response

→ **May 2026 - 12 hours**

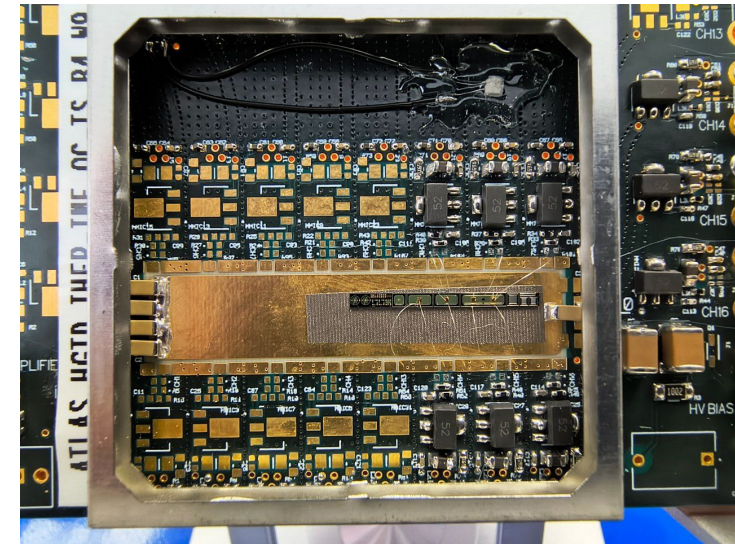
IHEP HGTD LGAD

- ◆ **2.1 keV using a Be window**
- ◆ Non uniform SIRIUS electron bunch filling
 - useful for baseline correction calibration
- ◆ **High intensity large statistics run**
 - new result - very precise non-linearity from $5\text{keV}\sim 30\text{keV}$

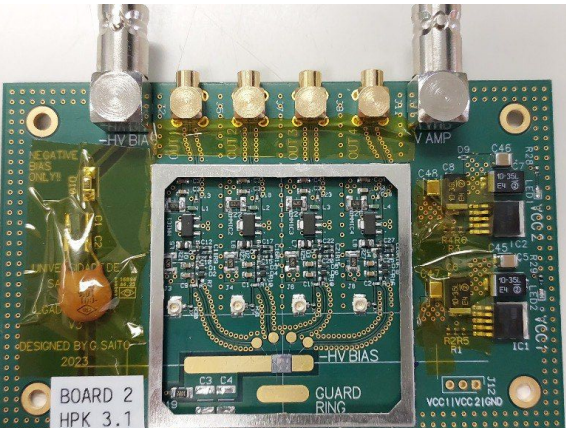


New version of readout board

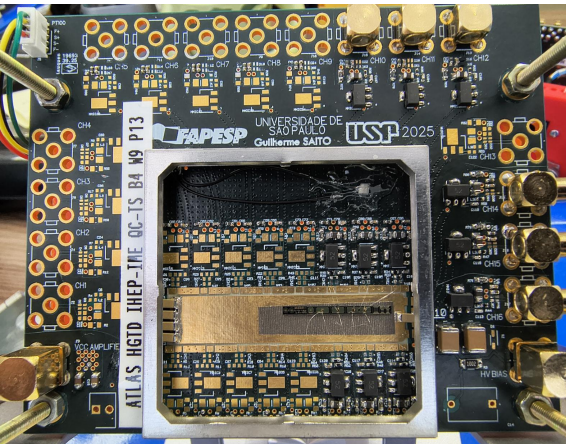
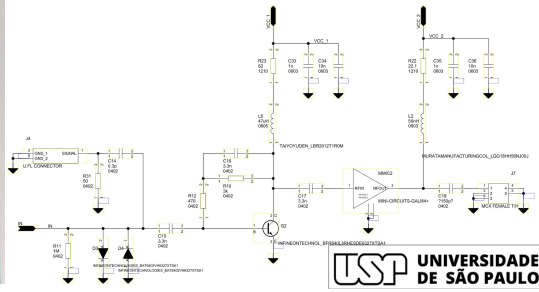
- 16 channels
- Better amplifier design
 - ◆ Better energy estimation
 - ◆ Improved jitter
- Optimal for many pixels sensor



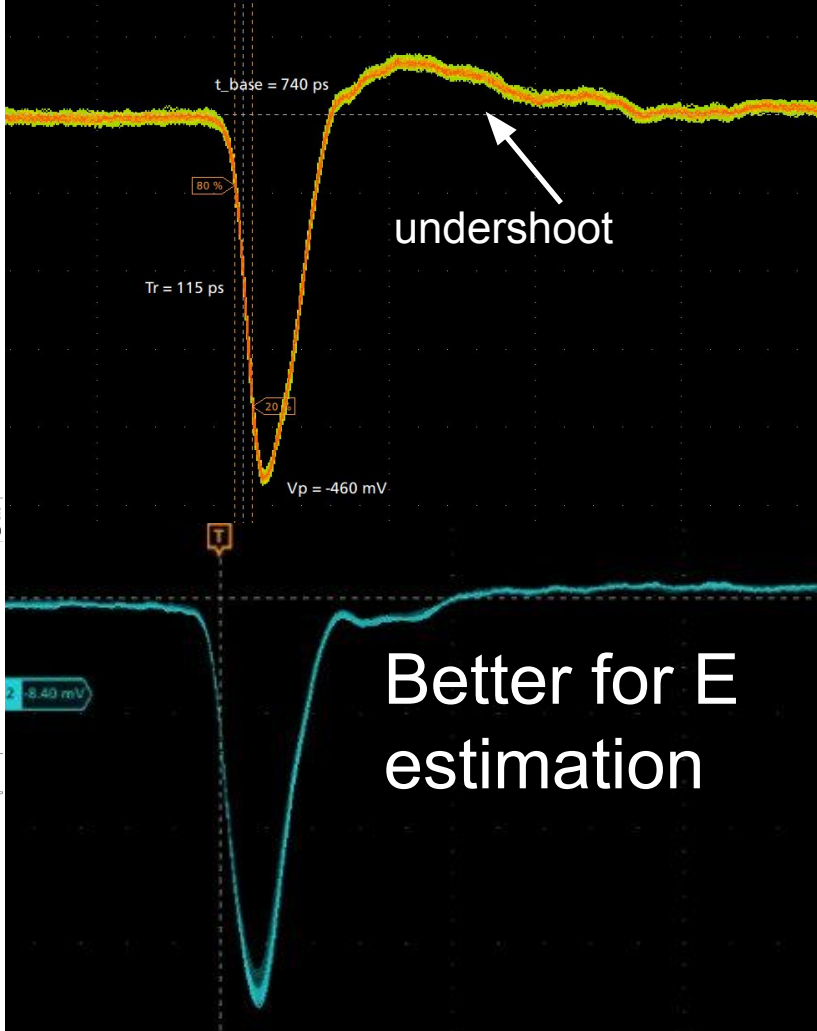
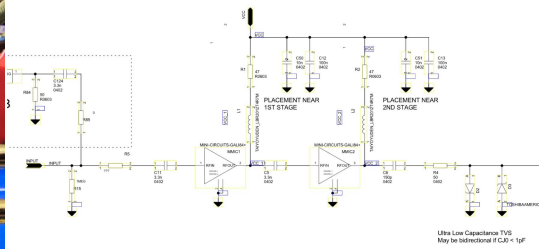
New amplifier design



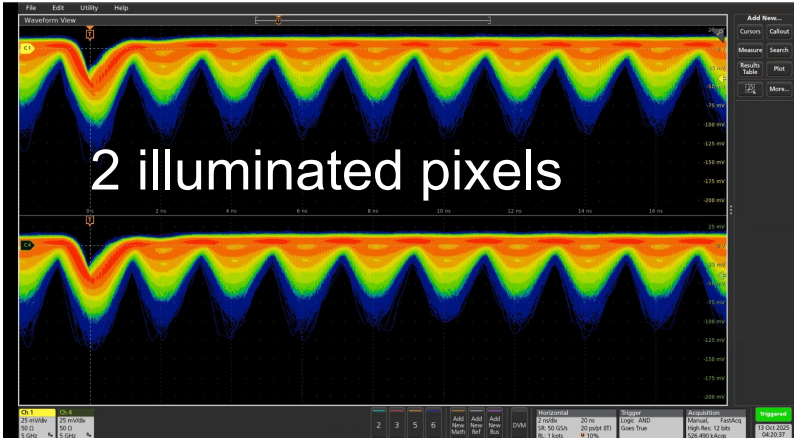
Old design



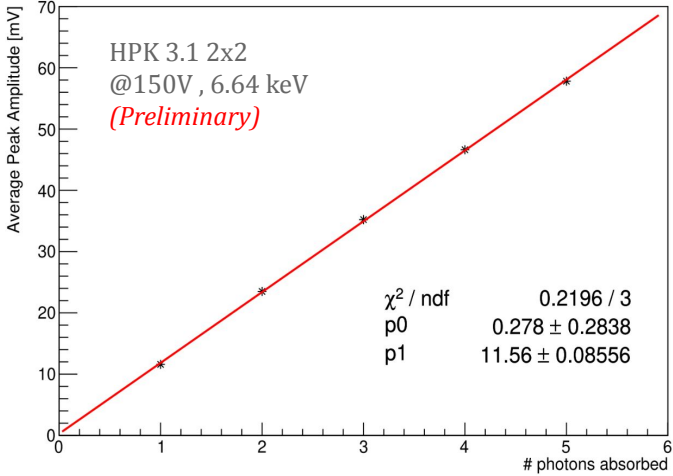
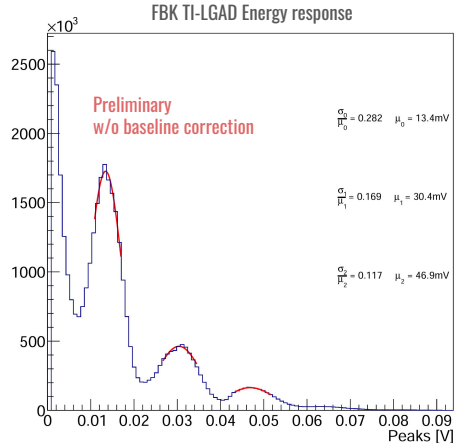
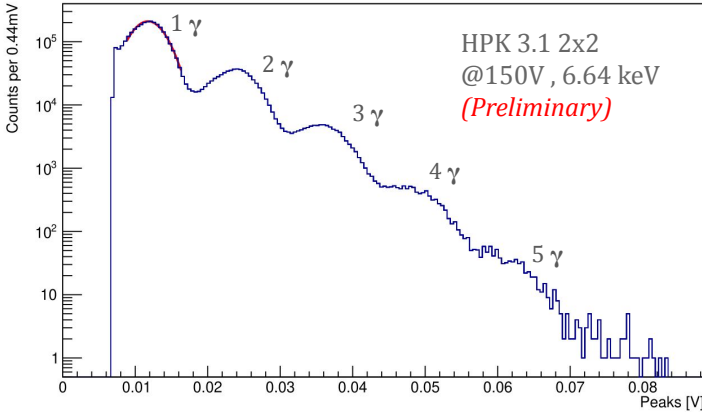
New design



Selected results



High intensity large statistics run - Linearity



Selected results

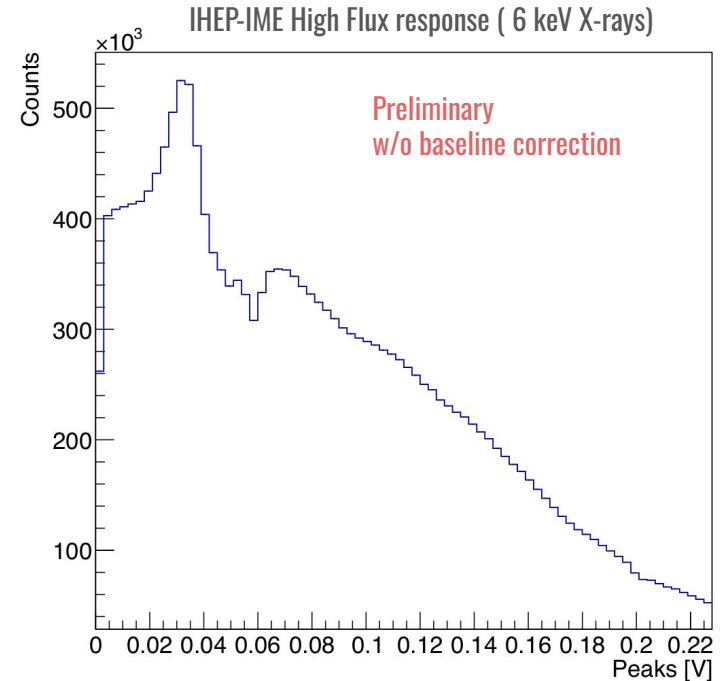
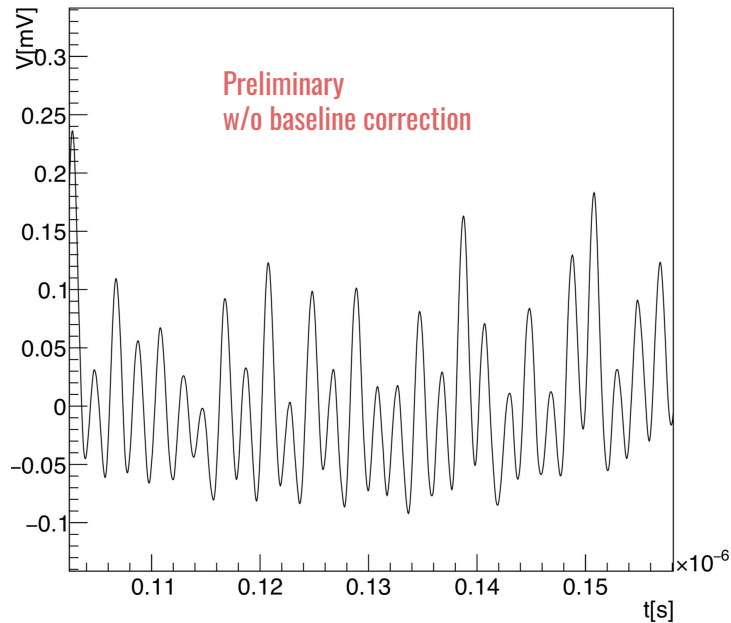
Attempt to kill the sensor

Maximum flux from beam line @ 6 keV, (350 μm spot size)

18 μA current from sensor \Rightarrow no increase in the current in ~ 10 min operation

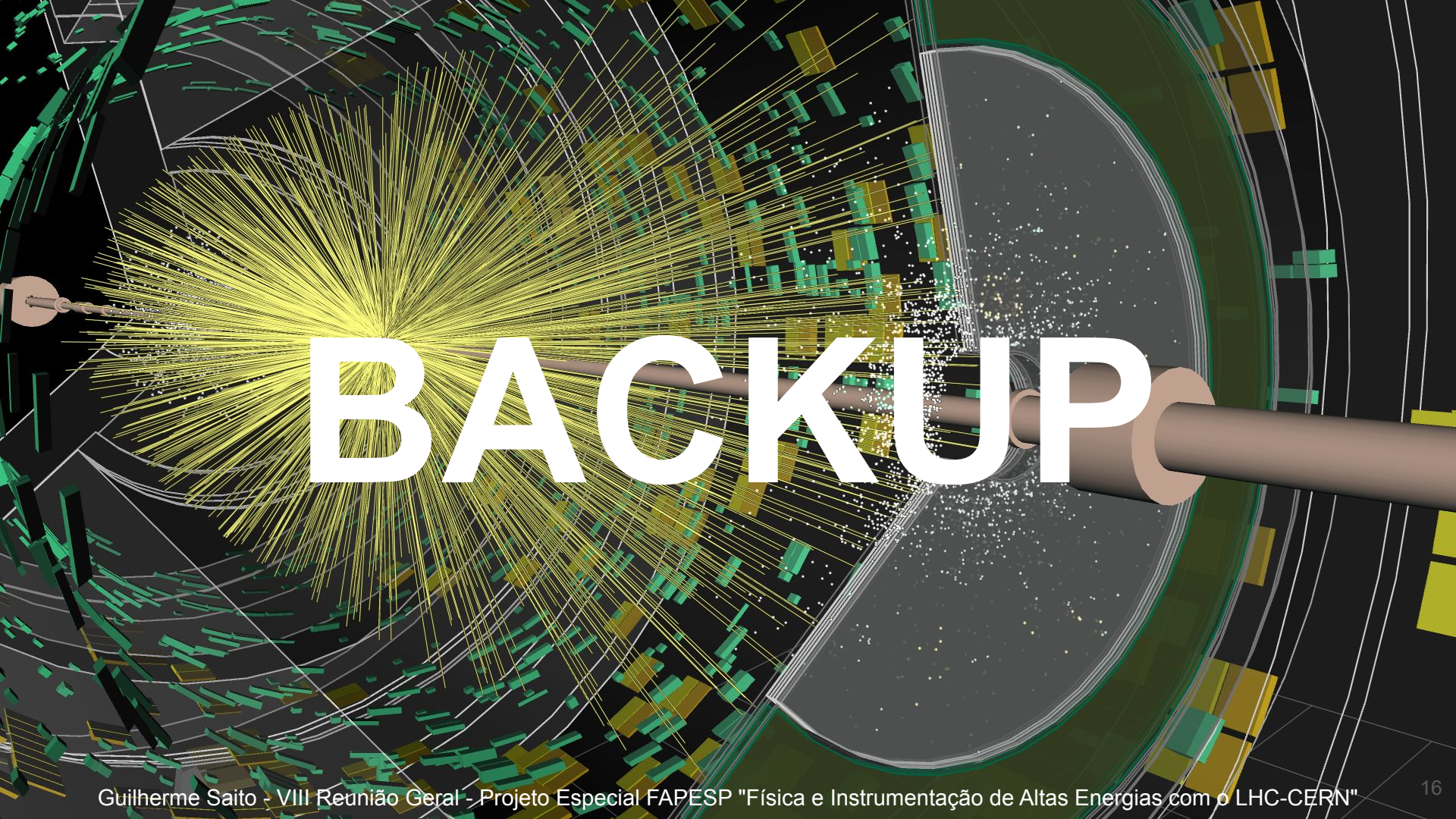
500 MHz rate

Signal recorded at every bunch

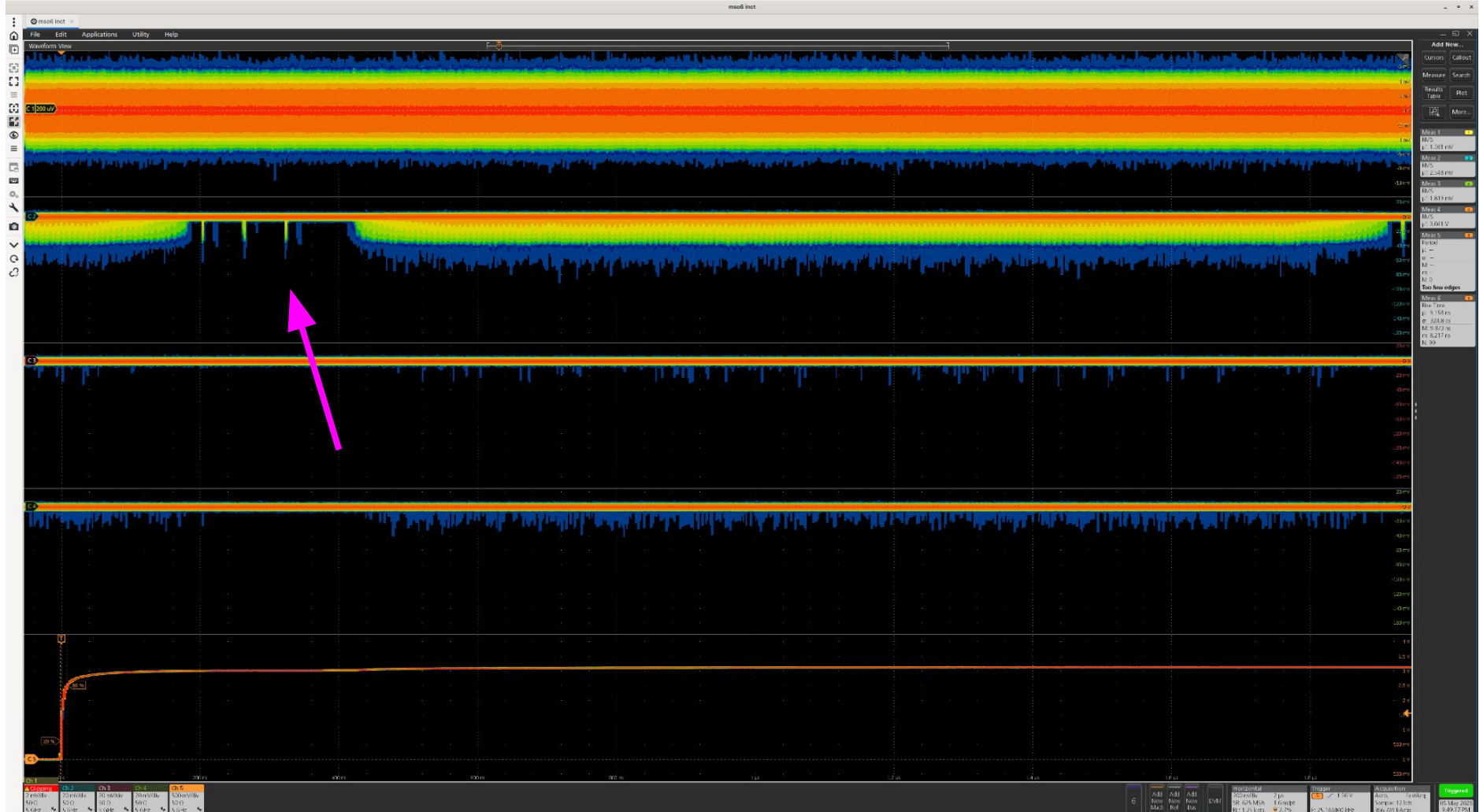


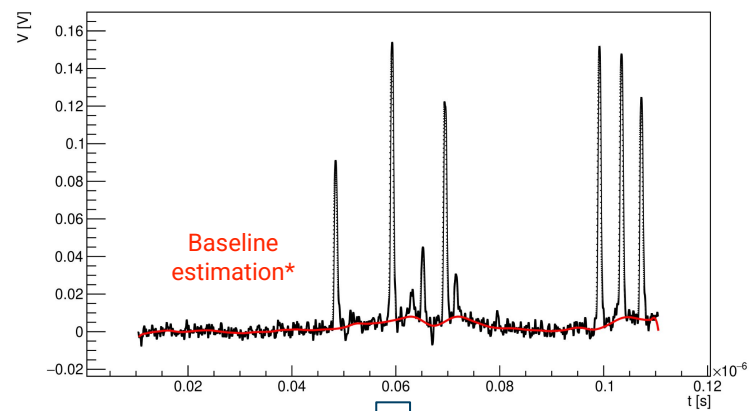
Summary

- Work on ATLAS HGTD Sensor development and qualification presented by G. Saito
 - 2025 IEEE NSS MIC RTSD, 5 November 2025 - Yokohoma, Japan
 - Short paper already published
- Dedicated USP+CERN full paper on wafer production quality control on internal review with HGTD collaboration
- All production testing procedures are being performed at IHEP, CERN, USP and JSI
- **Production is under way!**
 - On 3rd delivery
 - Almost 6000 good sensors already delivered (2/5 of total)
- Work on LGADs for Synchrotron X-ray applications presented by M. Leite
 - 4th Hiroshima Symposium on the Development and Application of Semiconductor Tracking Detectors - 16 November 2025, Taiwan
 - Proceedings on review with journal
- New beam testing campaign on October 2025 and May 2026 @ Sirius with HPK, FBK and IHEP sensors
 - Data still being analysed
- FAPESP SPRINT USP + IHEP ongoing

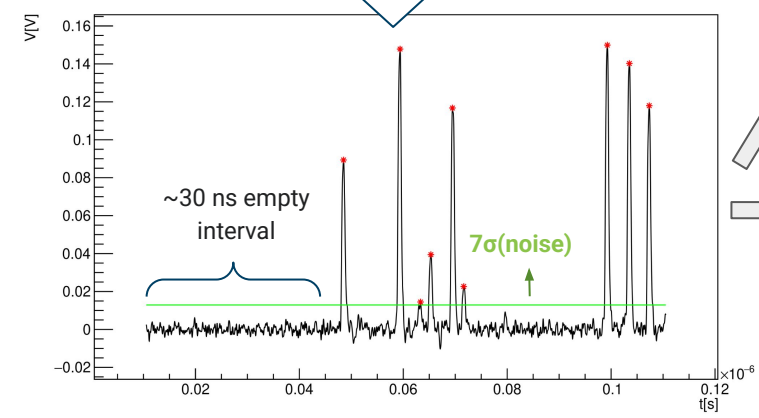
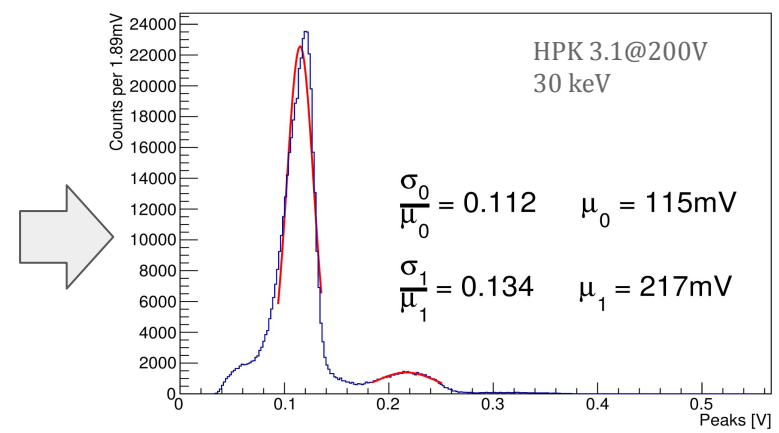


BACKUP

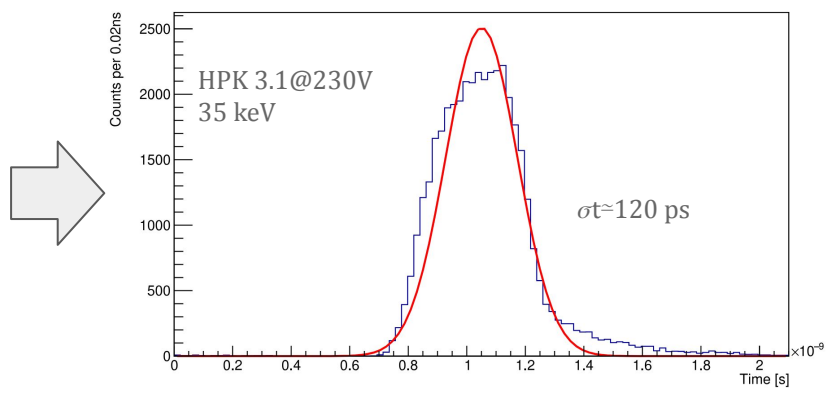




Energy
 $\sigma E/E$ from Amplitude distribution

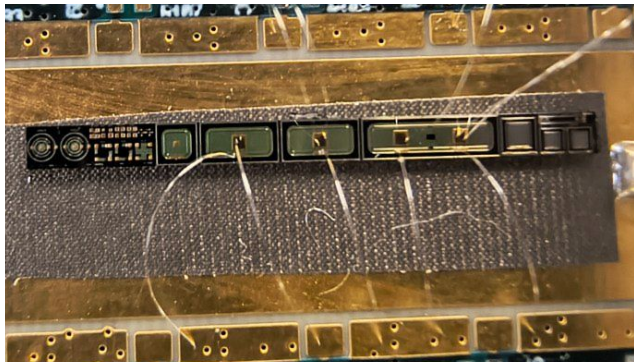
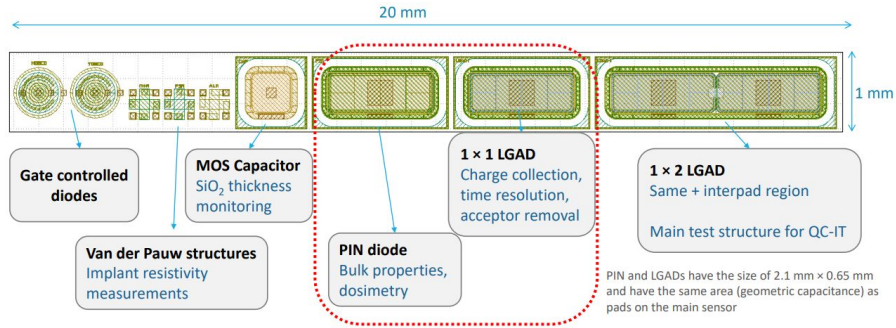


Timing
 σt based on 2.1 ns interval (20% CFD)

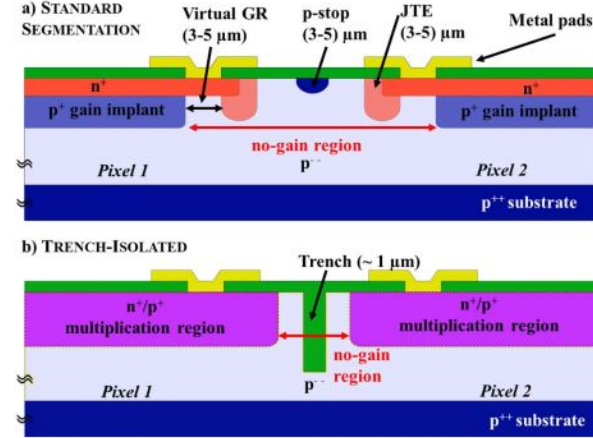


III - New Devices

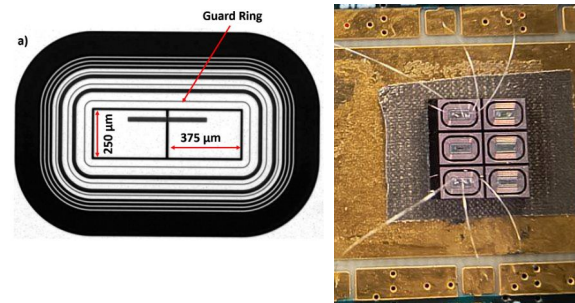
- ATLAS HGTD IHEP-IME QCTS structure with carbon-enriched "standard" DC-LGADs (and PiN)



- FBK TI-LGAD sensor from RD50 production, test structures 2x1 ([Paternoster et al](#))



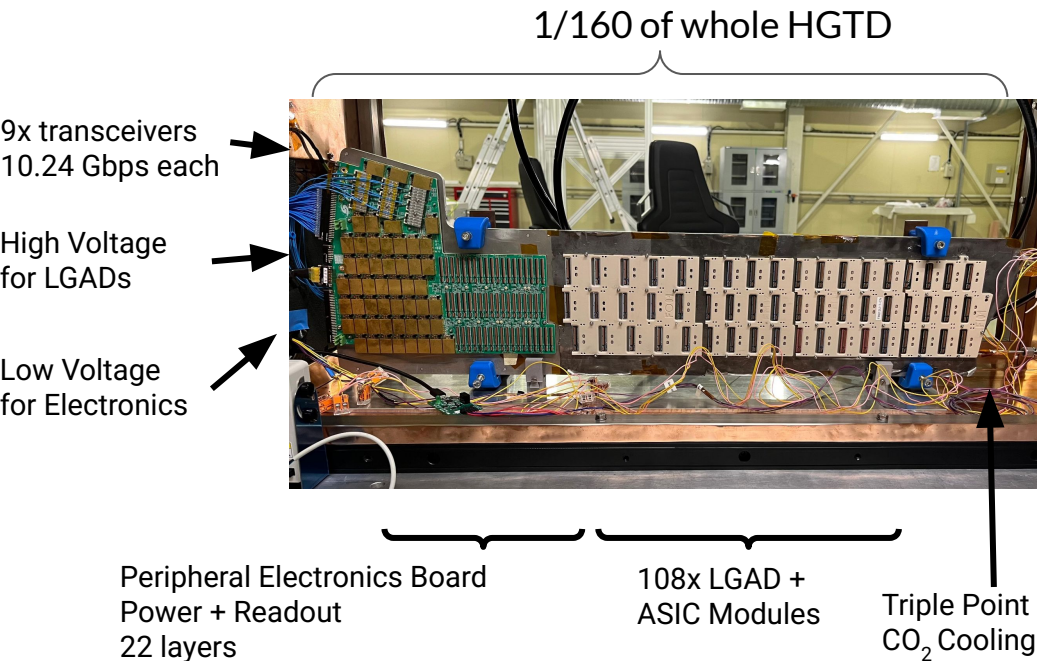
[Paternoster et al](#)



USP Activities at CERN

HGTD Demonstrator

- Exercise module loading and mechanical assembly
- Verify thermal runaway situations
- Full Readout chain

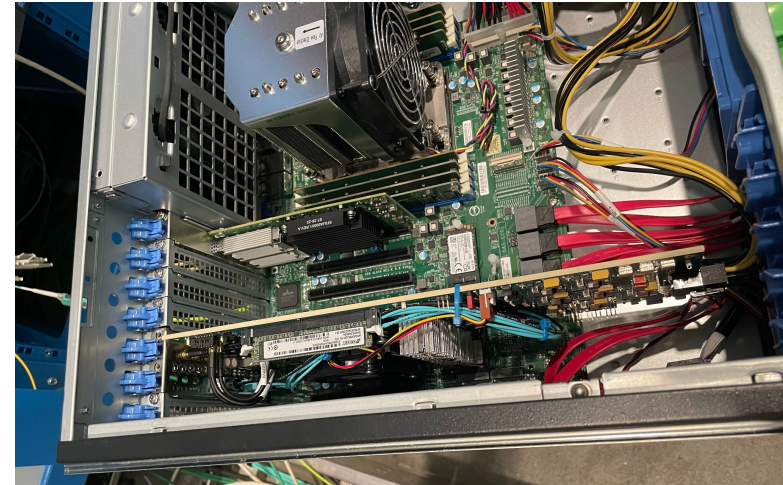


HGTD Readout

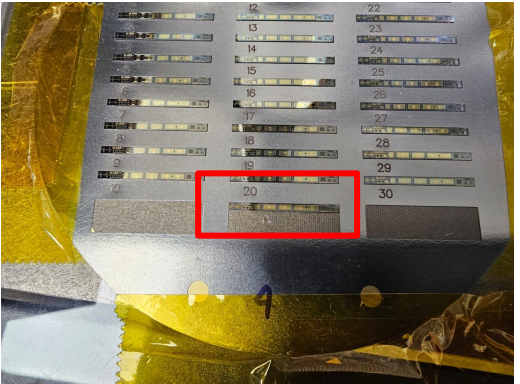
FELIX:

- ATLAS Data Acquisition System
- Custom 24 layer PCB
- 48x 10.24Gbps links

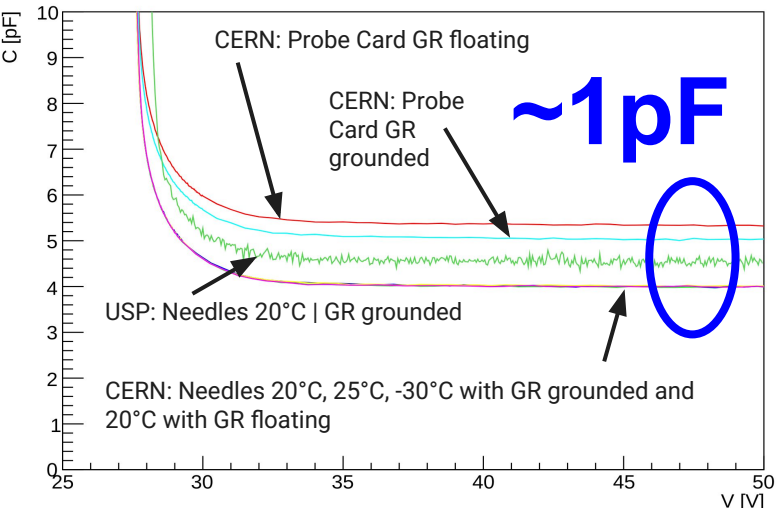
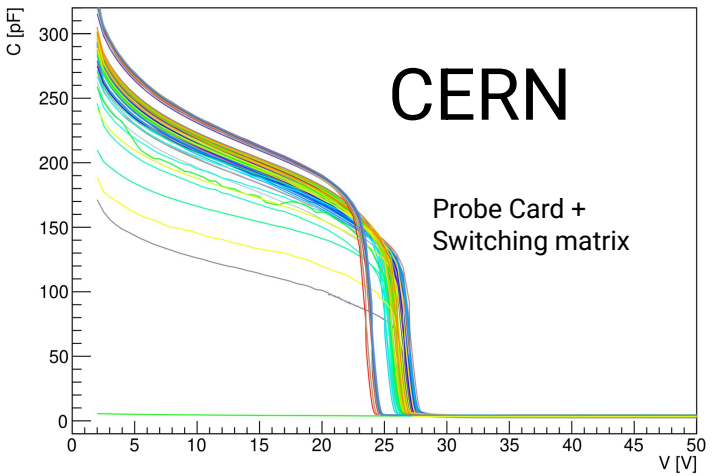
System being replicated @USP



Systematics between USP - CERN

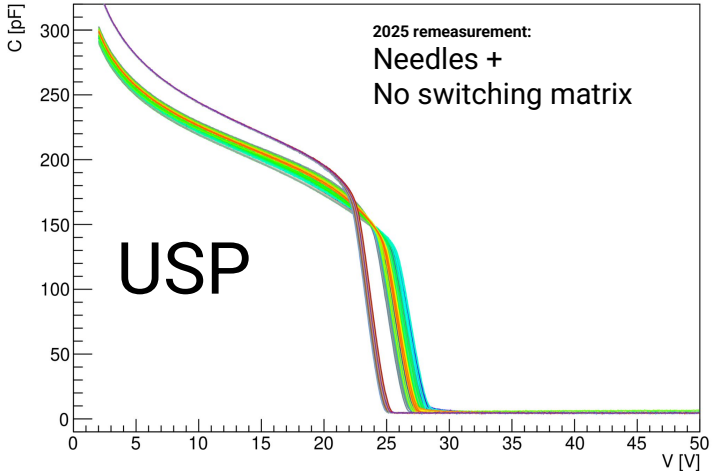


Same device
measured @CERN
and @USP



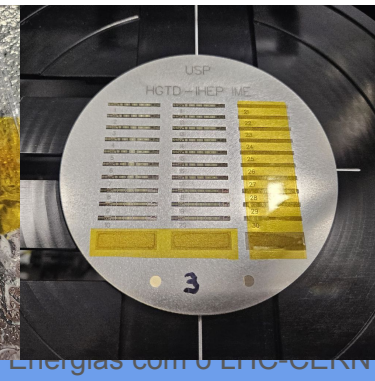
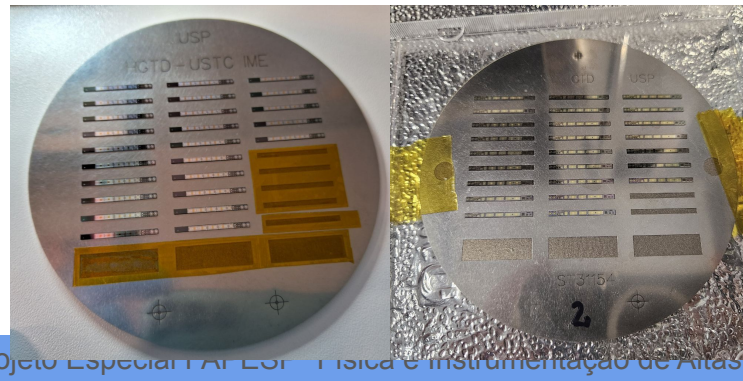
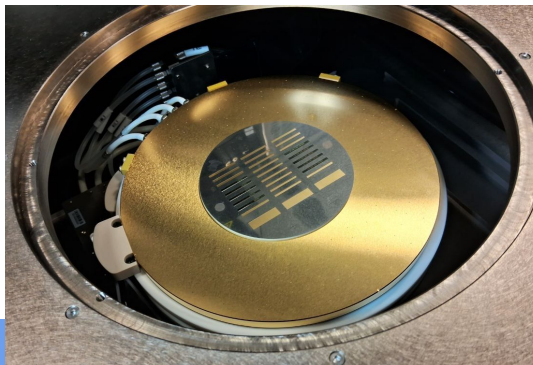
Different
devices

Same
wafers



USP Setup Status

- USP constructed a new facility for the HGTD Sensors PQC
- Probe Station MPI TS2000-HP
 - ◆ Same as CERN
 - ◆ No temperature control, but capable of operation up to 10kV
 - ◆ Installed and Commissioned
- Already measured 120 test structures from 24 pre-production wafers for HGTD
- HGTD sensors production **first delivery now**

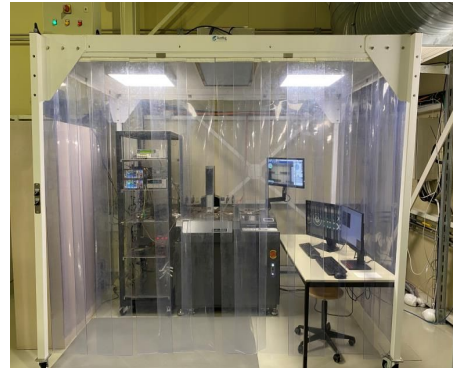


Sensor and Wafer Quality Control

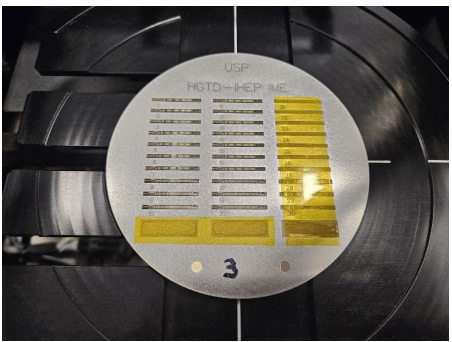
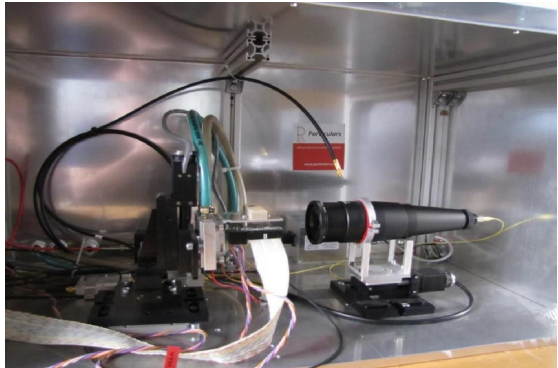
Probe Station setup at USP



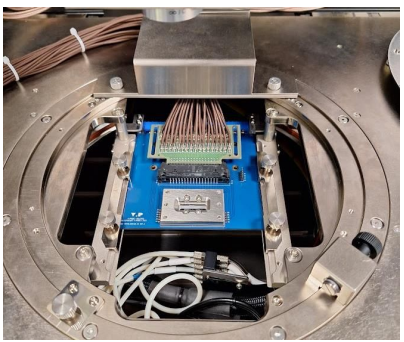
Probe Station setup at CERN



TCT Laser setup at JSI



Many QC-TS loaded on Probe Station



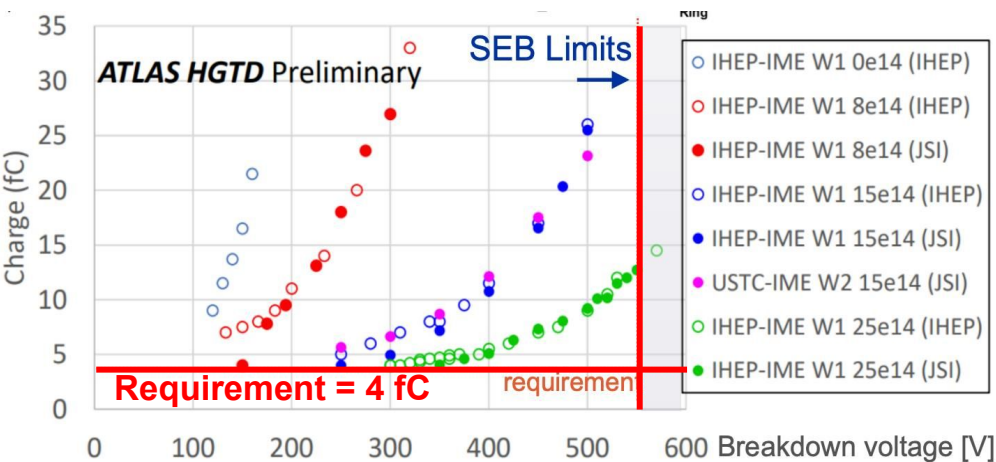
QC-TS Probe Card

Performance

LGAD performance **degrades with radiation** exposure due to a loss of gain

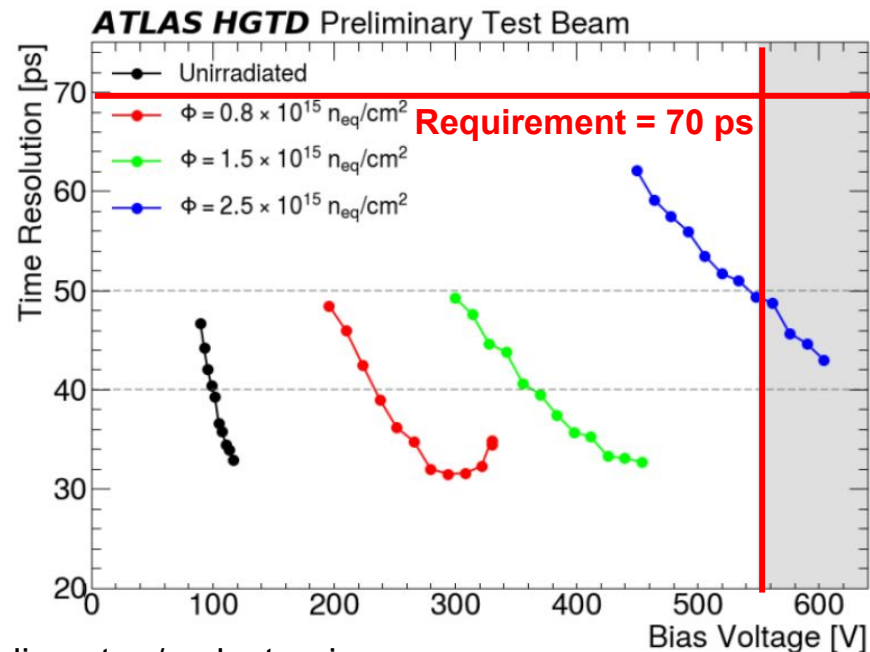
- Radiation induced **removal of Boron** from gain layer
 - Improved by **addition of Carbon** on gain layer
- Recovered by **increasing the bias** during operation
- **Limit imposed by Single Event Burnout (SEB)**
 - Safe operation <550V (more on backup slides)

LGAD Signal vs. Bias Voltage



Both measured with discrete r/o electronics

LGAD Time Resolution vs. Bias Voltage



Single Event Burnout (SEB)

- Irreversible breakdown triggered by a large charge deposition at high operation voltages
- Triggered by a single particle
- Large energy deposits: **electric field collapse** in presence of high concentration of free carriers
- Observed in several test beam campaigns
- Common effort of ATLAS/CMS/RD50 collaborations: determine a safe operating voltage
- Systematically studied at HGTD test beams
- **Safe operating zone: 11V/μm**

For 50μm sensor thickness:
 Maximum safe bias voltage = 550V

