

# **Thursday 7 September**

14:00

## Poster Session III

Session | Location: Oxford, St Catherine's Bernard Sunley Building

14:00-14:10

Towards the construction of the ATLAS ITk Strip Endcap detector for the HL-LHC

Speaker

Laura Franconi

14:10-14:20 Module development with the MALTA monolithic pixel chip

Speaker

Florian Dachs

14:20-14:30

Assembly and Testing of 2S Module Prototypes for CMS Outer Tracker Phase-2 **Upgrade** 

Speaker

Ali Awais

14:30-14:40

Development of the BCM' system for beam abort and luminosity monitoring in ATLAS based on a segmented polycrystalline CVD diamond system and dedicated front-end ASIC

Speaker

Miha Mali

14:40-14:50

A study on the feasibility of CSNS becoming an ATLAS ITk sensor QA irradiation site

**Speakers** 

Hui Li, Zhan Li

14:50-15:00

Results of system qualification and quality control strategies for the production of the ITk Pixel Outer Barrel of the ATLAS experiment

Speaker

Simon Florian Koch

15:00-15:10

Characterization of Ultra-Fast Silicon Detectors for High Energy Physics **Applications** 

Speaker

Fasih Zareef

15:10-15:20

Mapping the effects of ionising and non-ionising radiation on the measurement of inter-strip isolation with ATLAS ITk strip sensors

## Speaker

Andrew Stephen Chisholm

15:20-15:30

Implementation of the annihilation gamma-ray polarizations in Positron Emission **Tomography** 

## Speaker

Ana Marija Kožuljević

15:30-15:40

RD53A Quad Modules Production and QC for the ATLAS Inner Tracker Outer Barrel (OB) Demonstrator.

## Speaker

Mr Yahya A R Khwaira

15:40-15:50

Serial Powering Scheme and Performance Analysis for Innermost Layer (L0) for **ATLAS ITk modules** 

## Speaker

Md Arif Abdulla Samy

15:50-16:00

A 4-Gbps serializer circuit in a 180 nm technology for monolithic pixel sensor prototypes developed for the CEPC vertex detector

## **Speakers**

Mr Tianya Wu, Xiaoting Li

16:00-16:10 High Rate Camera Systems - our approach for control and data

### Speaker

Matthew Hart

18:00