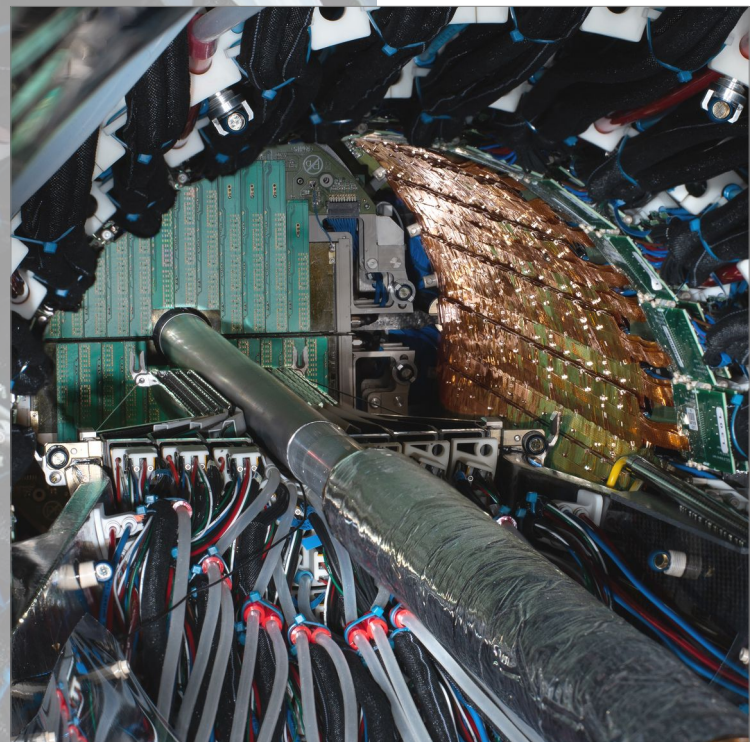


Results of the new MAPS-based ALICE inner tracker operation in the LHC Run 3

Ivan Ravasenga¹
for the ALICE Collaboration

¹CERN (*Geneva, CH*)



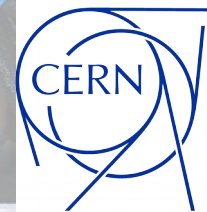
PSD13



St. Catherine's College
September 3-8, 2023



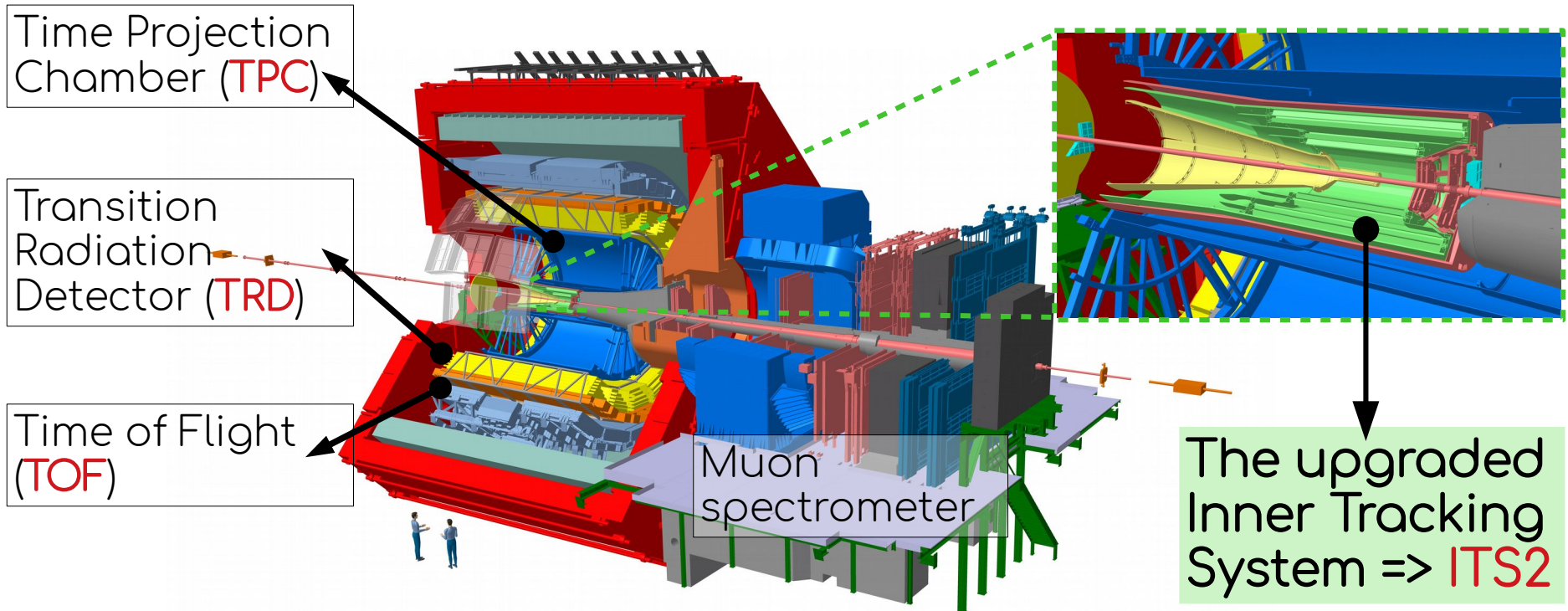
UNIVERSITY OF
OXFORD



Introduction – ALICE experiment

- Run 2 of LHC completed in 2018 with Pb-Pb collisions at $\sqrt{s_{NN}} = 5.02$ TeV
- Long-shutdown 2 started in 2018 to allow detector & computing system upgrades

Run 3
apparatus

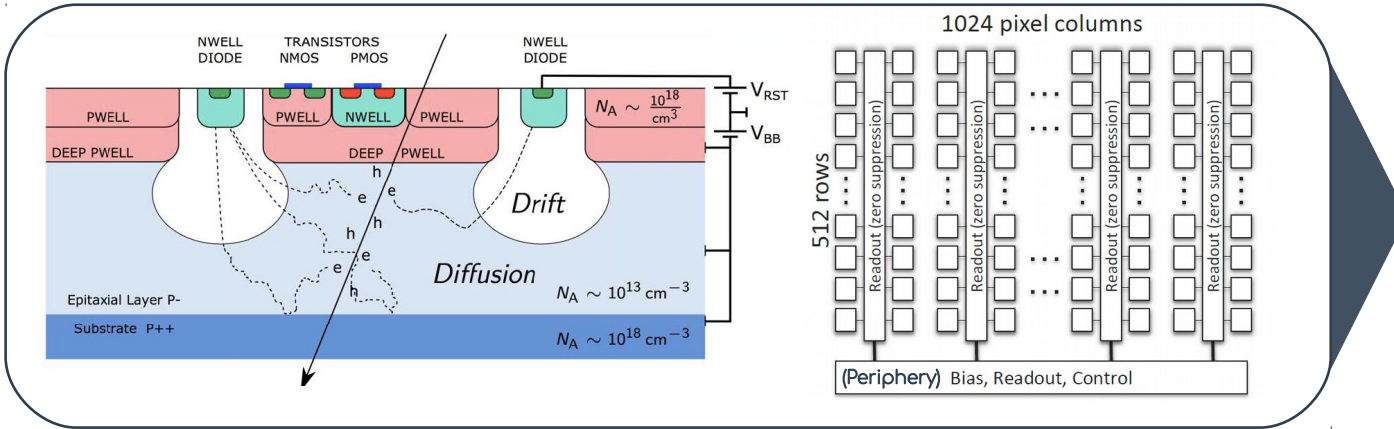


ALPIDE chip – the detector core

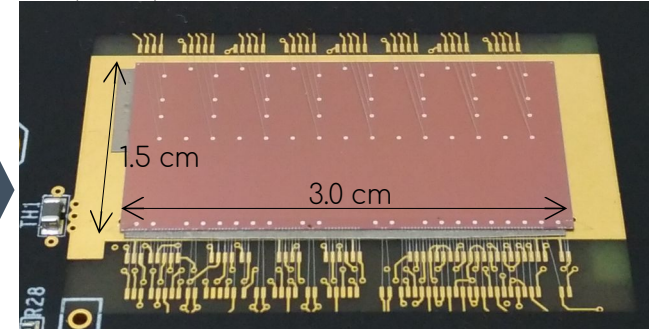
→ ALice Pixel DEtector



ALICE



Example: chip bonded on its carrier board



- TowerJazz 0.18 μm CMOS imaging process – a Monolithic Active Pixel Sensor (MAPS)

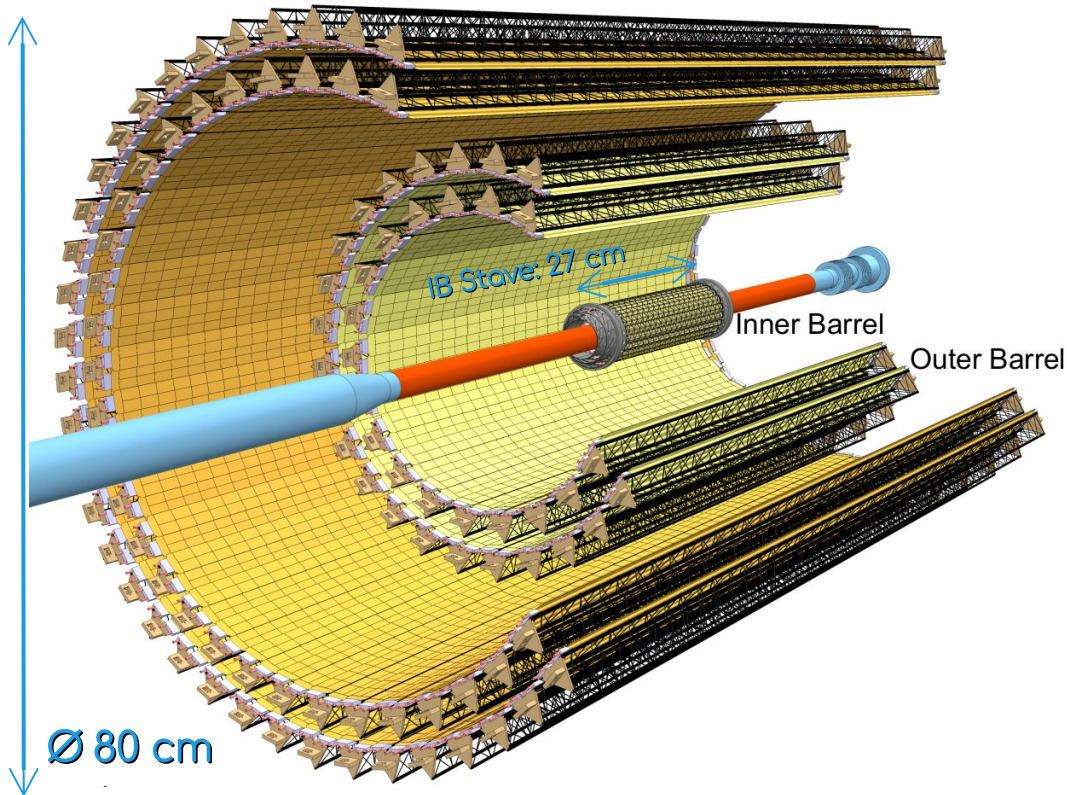
- High resistivity p-type epitaxial layer on p-type substrate
- Small n-well diode → small capacitance ~fF
- In-matrix sparsification using priority encoder
- Pixel signal amplified and digitized at a pixel level
- Low power consumption
- Pixel data sent towards periphery to the Data Transmission Unit (Serializer + PLL + LVDS driver)

Key numbers

Resistivity	$1 \div 6 \text{ k}\Omega\text{cm}$
Epitaxial layer thickness	$25 \mu\text{m}$
N-well diode diameter	$2 \mu\text{m}$
Power consumption	$\leq 47 \text{ mW/cm}^2$
Pixel size	$27 \times 29 \mu\text{m}^2$
Spatial resolution ($r_{\phi, xz}$)	$5 \times 5 \mu\text{m}^2$

Introduction – The Upgraded Tracker (ITS2)

→ 10 m² of monolithic active pixel sensors (12.5 GPixels)



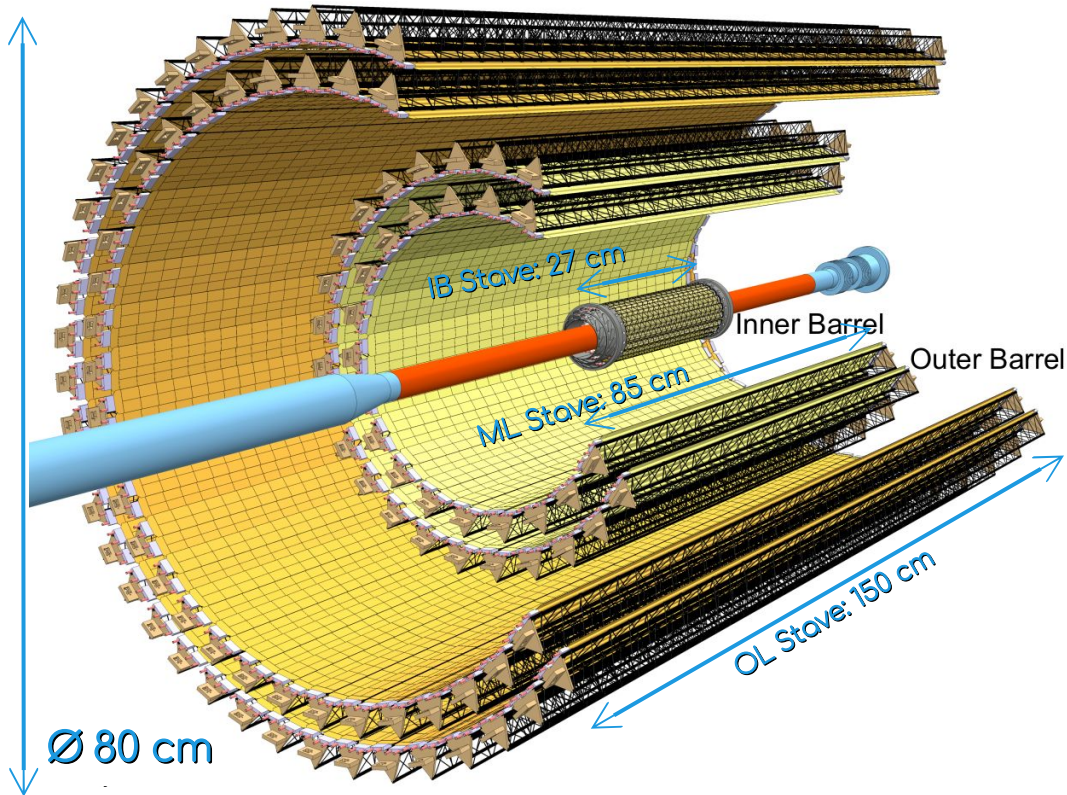
Inner Barrel (IB)

- 3 layers (L0 → L2)
- 48 Staves made of 9 ALPIDE chips each
- Material budget: 0.36% X_0
- Readout at 1200 Mb/s per chip

Inner Tracking System (ITS)

Introduction – The Upgraded Tracker (ITS2)

→ 10 m² of monolithic active pixel sensors (12.5 GPixels)



Inner Barrel (IB)

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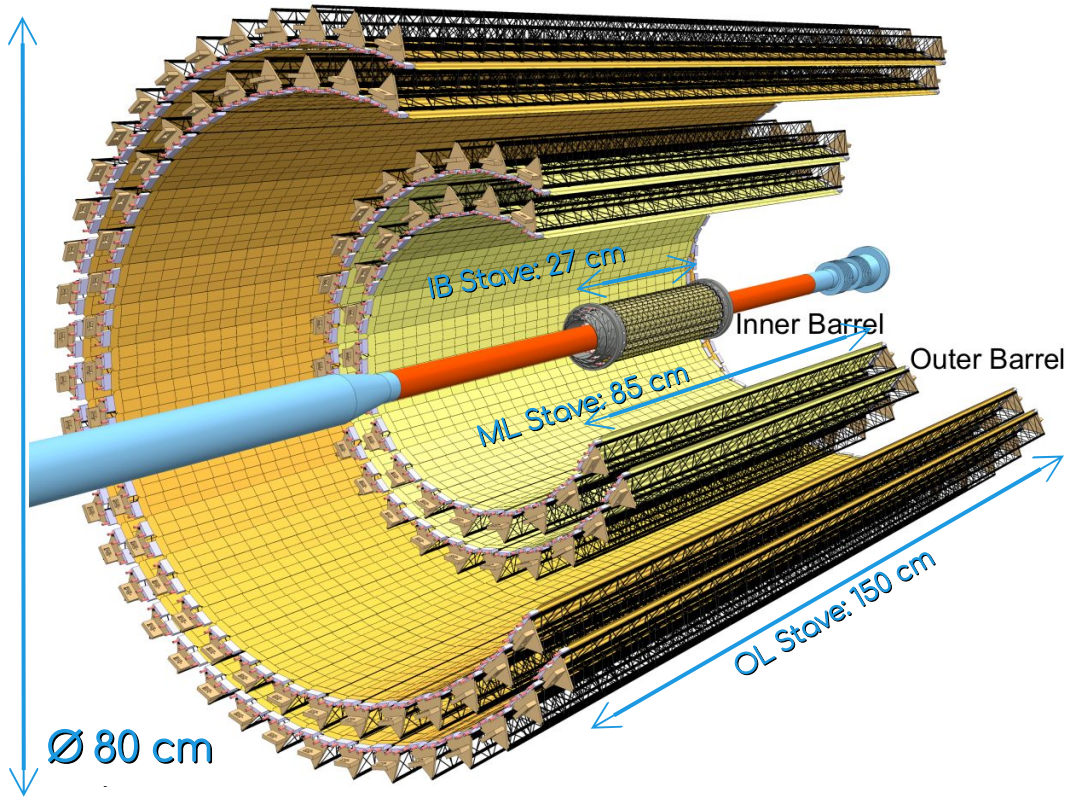
Outer Barrel (OB)

- 4 layers – 2 Middle + 2 Outer Layers (L3 → L6)
- ML(OL): 54(90) Staves with 112(196) ALPIDE chips each
- Material budget: ~1.1% X_0
- Readout of 7 chips with single link at 400 Mb/s

Inner Tracking System (ITS)

Introduction – The Upgraded Tracker (ITS2)

→ 10 m² of monolithic active pixel sensors (12.5 GPixels)



Inner Tracking System (ITS)

Inner Barrel (IB)

- 3 layers (L0 → L2)
- 48 Staves made of 9 ALPIDE chips each
- Material budget
- Readout

24k chips
12.5 Gpix

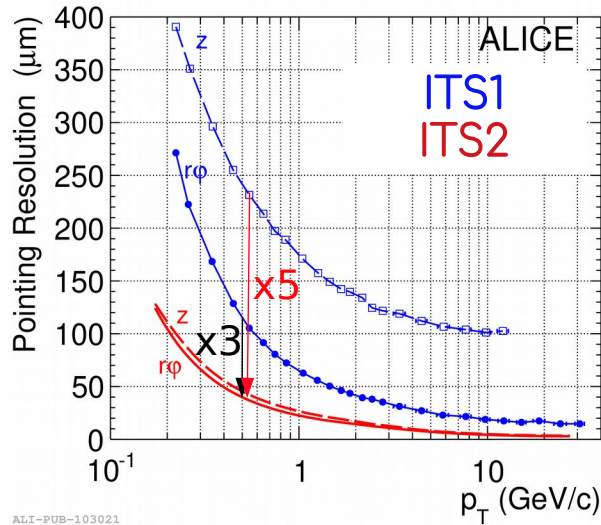
The largest MAPS-based detector ever built in HEP experiments

Outer Barrel

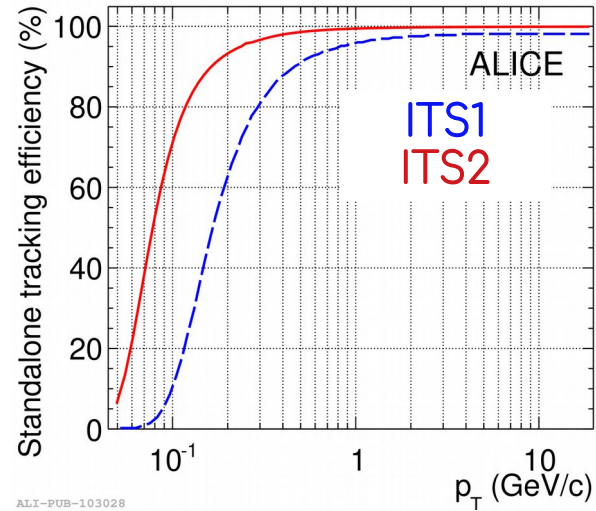
- 4 layers – 2 Middle + 2 Outer Layers (L3 → L6)
- ML(OL): 54(90) Staves with 112(196) ALPIDE chips each
- Material budget: ~1.1% X_0
- Readout of 7 chips with single link at 400 Mb/s

Expected improvements with ITS2

Pointing resolution ($r\phi$ & z)



Tracking efficiency



- Improved impact parameter resolution: factor ~ 5 (z), factor ~ 3 ($r\phi$) at $p_T = 500$ MeV/c
- Improved standalone tracking efficiency: 60% \rightarrow 90% at $p_T = 200$ MeV/c

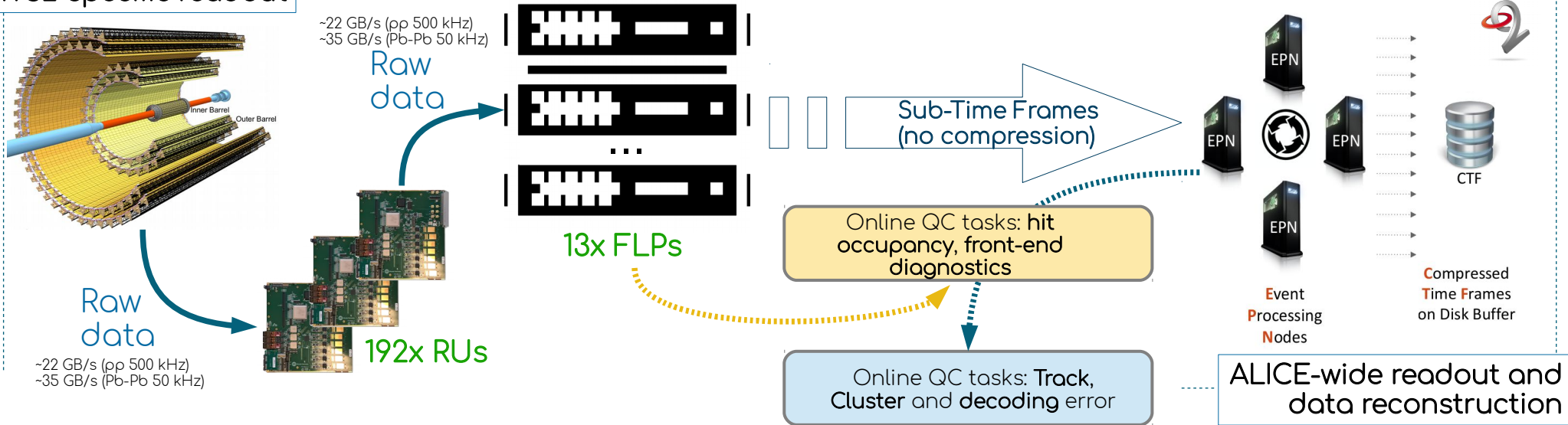
Data readout architecture and quality control (QC)

→ a simplified view



ALICE

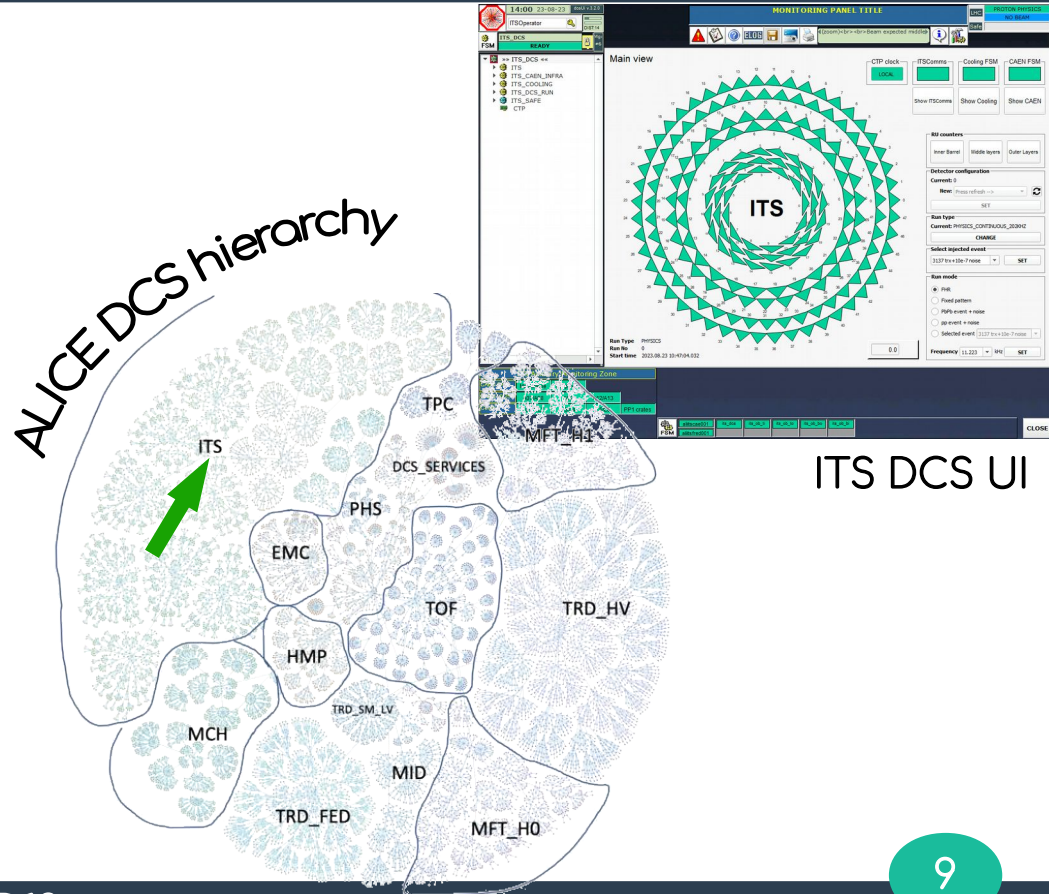
ITS2-specific readout



- **13** ITS First Level Processors (FLPs)
 - Online data quality control tasks: hit occupancy and front-end electronics diagnostics.
- **340** Event Processing Nodes (total EPN from ALICE farm)
 - Online quality control tasks: reconstructed ITS2 tracks, clusters and decoding errors.
- **Synchronous** reconstruction, calibration and data compression (→ GPUs)
- **Asynchronous** stage: reconstruction with final calibration → final Analysis Object Data (AOD)

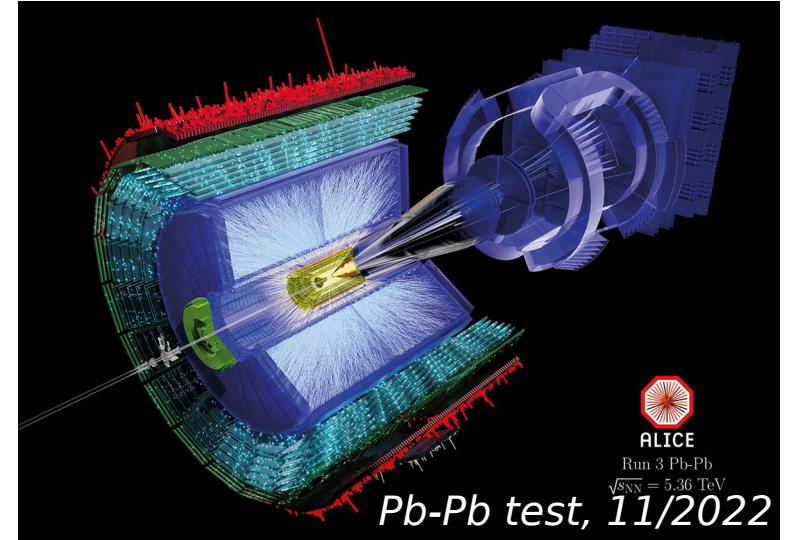
Detector Control System (DCS) - a quick view

- User Interface developed in **WinCC** – detector logic implemented in a **Finite State Machine**
- **Detector operation, monitoring and archiving of detector data.**
- Deal with **~110000 data points (ITS only)**
→ **typical monitoring frequency of 1 Hz.**
- **Built as a hierarchical system** (partitioned with system of locks) → ITS occupies a big slice of the ALICE hierarchy
- An **independent safety system** interlocks power channels based on stave temperatures and cooling status



Run 3 overview

- Started on 2022, July 5th with first pp collisions at $\sqrt{s} = 13.6$ TeV (*stable beams*)
- Pb-Pb ion test in November 2022: record energy 5.36 TeV
- Integrated luminosity so far (pp collisions):
~28 pb⁻¹
- ALICE & ITS2 numbers in data taking
 - Nominal ITS framing rate: 202 kHz
 - ALICE standard luminosity: 500 kHz (instantaneous luminosity $\sim 10^{31}$ cm⁻² s⁻¹)
 - ITS2 successfully tested up to 4 MHz interaction rate (~50 GB/s data rate).
 - Loss of acceptance during runs auto-recovered by DCS
- At every beam dump: fast ITS **threshold scan on 2% of the pixels** to evaluate the quality of the detector calibration



- Main events till next technical stop
 - Week 37: pp reference runs
 - ★ October: Pb-Pb ion runs ★

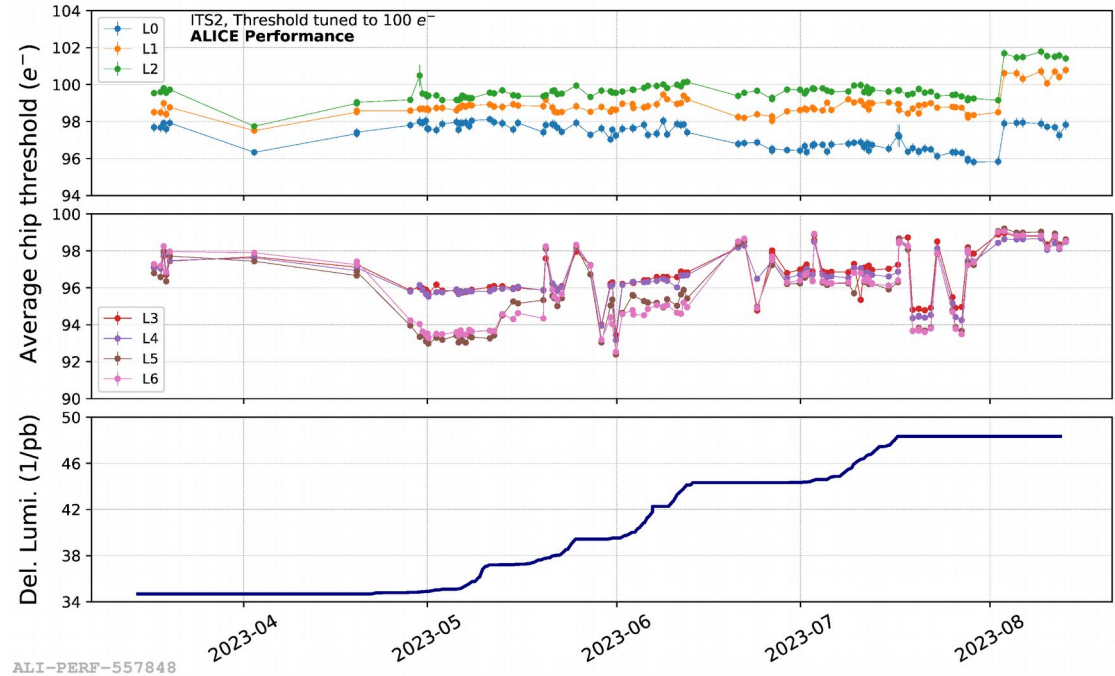
Performance results in Run 3

→ ITS2 full calibration (1)



ALICE

- Main ITS calibration parameters:
 - Masking of noisy pixels
 - Tuning of in-pixel discriminating thresholds
 - Power supply voltage
 - On-chip temperatures
- Percentage of non-working pixels: **~0.2 %**
- Threshold calibration of 24120 chips is challenging:
 - Online calibration workflow runs on 40 EPNs with parallel processing
 - Pulsing of ~1% of the pixels: ~252Ghits
 - Thresholds: tuned to 100e⁻ (in-layer RMS < 5-6 e⁻)



Thresholds are observed to be stable during 2023 operations without retuning

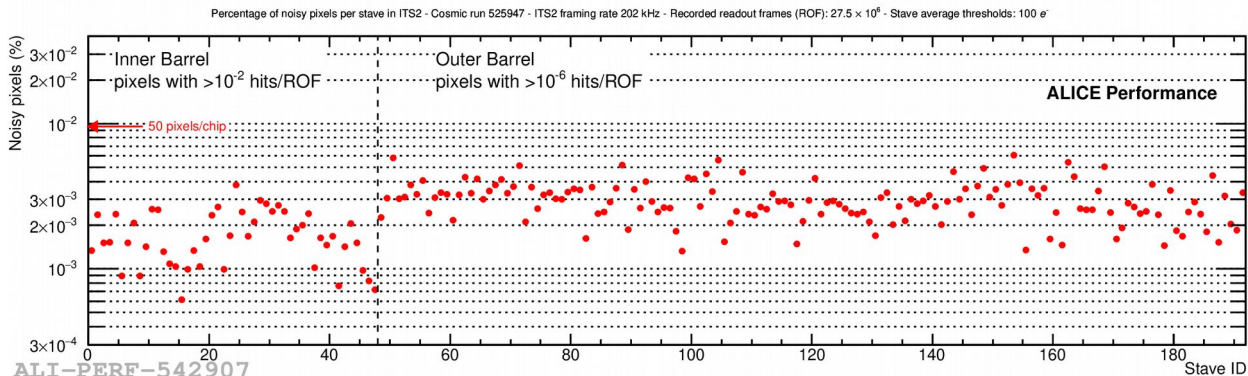
Fluctuations of 2-3 e⁻ due to optimizations of the voltage to chips

Performance results in Run 3

→ ITS2 full calibration (2)

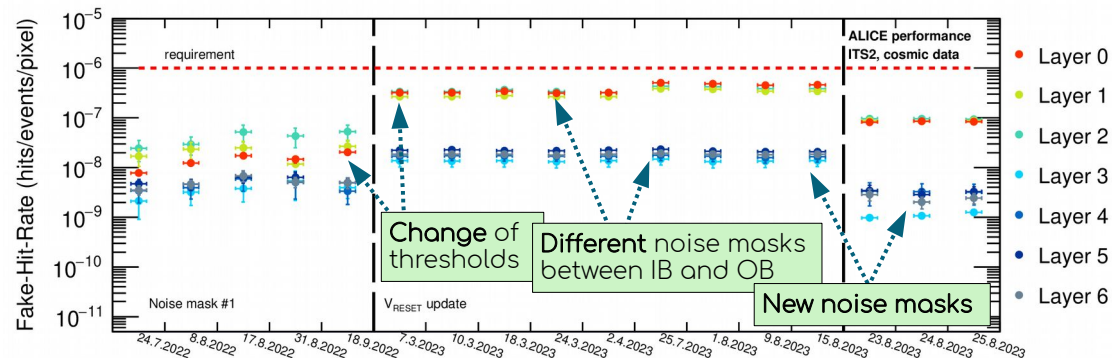


ALICE



- **Noisy pixel definition:**
 - IB: occupancy $> 10^{-2}$ hits/event
 - OB: occupancy $> 10^{-6}$ hits/event
- Percentage of noisy pixels masked per stave is **extremely small: $\sim 0.02-0.03\%$**

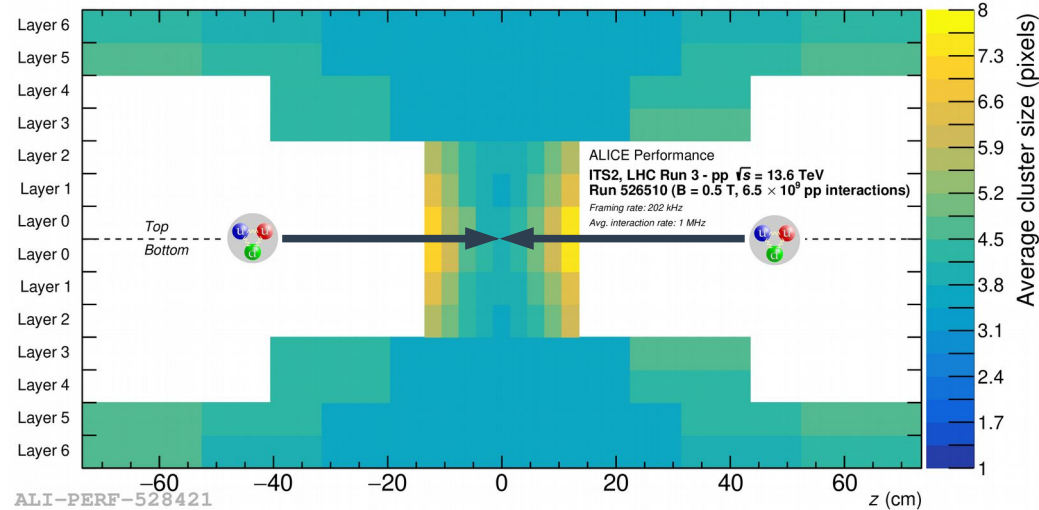
- Fake-hit rate trend during cosmic runs (tuned thresholds + noise masks)
- **Stable and $< 10^{-6}$ hits/event/pixel** (design requirement) by masking only $\sim 0.03\%$ of the pixels



Preliminary performance results in Run 3

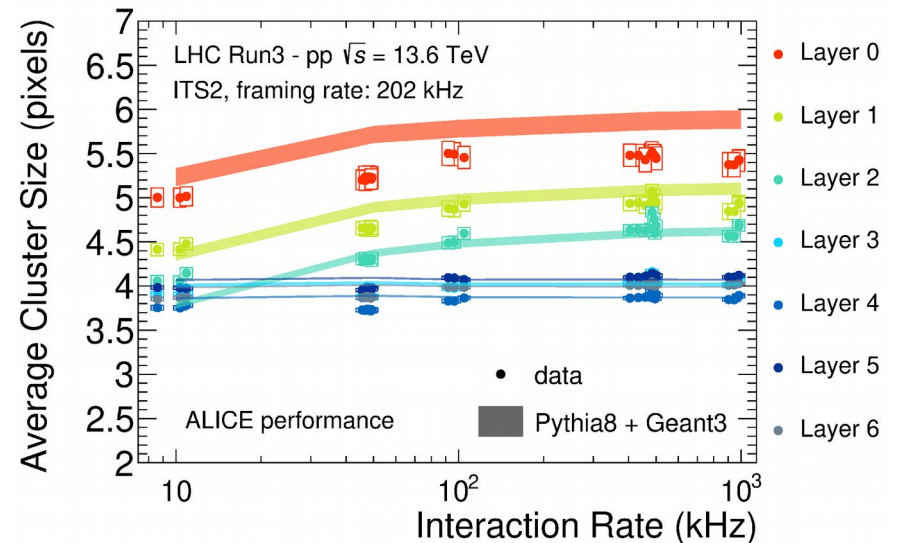
→ Cluster size and simulation

- Cluster size averaged for half barrels
 - Between 3 and 8 pixels depending on η
 - RMS ranging on the same interval
 - Observed to be stable over time
 - Independent of the interaction rate



Cluster size (pix)

- Simulation with Pythia 8 + Geant 3
 - Simulated noise: 2×10^{-8} hits/event/pix (IB), 3×10^{-9} hits/event/pix (OB)
 - Good agreement with data considering approximations:
 - Average noise per barrel and not per stove/chip.
 - Limited statistics in MC: ~20k events.

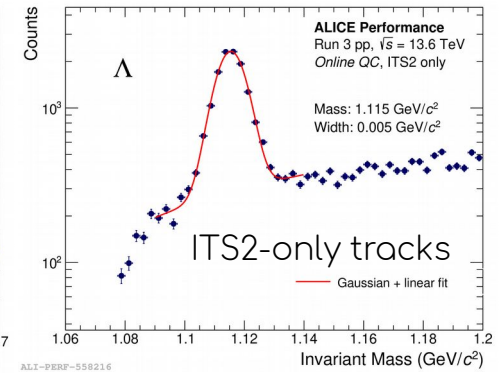
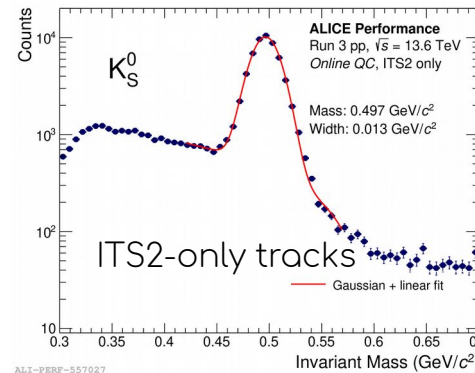
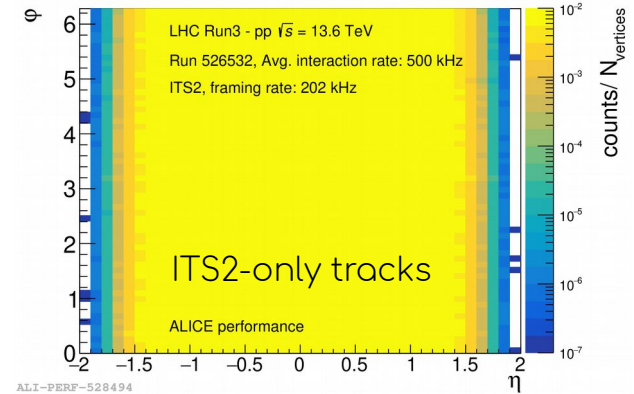


Cluster size vs IR

Preliminary performance results in Run 3

→ Detector alignment and reconstructed tracks

- ITS tracking: **excellent** performance with current detector alignment
 - Cellular automaton algorithm
 - Online tracking for quick QA of the data
 - Angular distribution of tracks of good quality → **good detector acceptance**
- **Online** physics performance from QC through Λ and K_S^0 invariant mass peaks

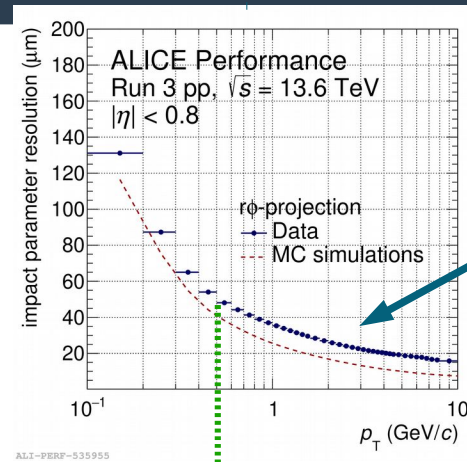


Λ , K_S^0 invariant mass peaks from **online** QC

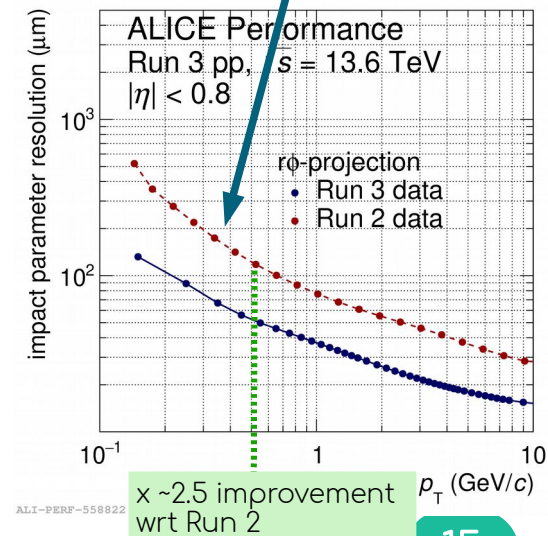
Preliminary performance results in Run 3

→ Detector alignment and reconstructed tracks

- ITS tracking: **excellent** performance with current detector alignment
 - Cellular automaton algorithm
 - Online tracking for quick QA of the data
 - Angular distribution of tracks of good quality → good detector acceptance
- **Online physics performance from QC through Λ and K^0_s invariant mass peaks**
- **Impact parameter resolution measured with Run 3 pp data → excellent performance**
 - About 2.5x improvement at $p_T = 500$ MeV/c → Detector alignment, space charge corrections and calibrations still continuously improving
 - ~20% discrepancy with MC could be related to a mismatch of sensor response in simulation and residual misalignments in data



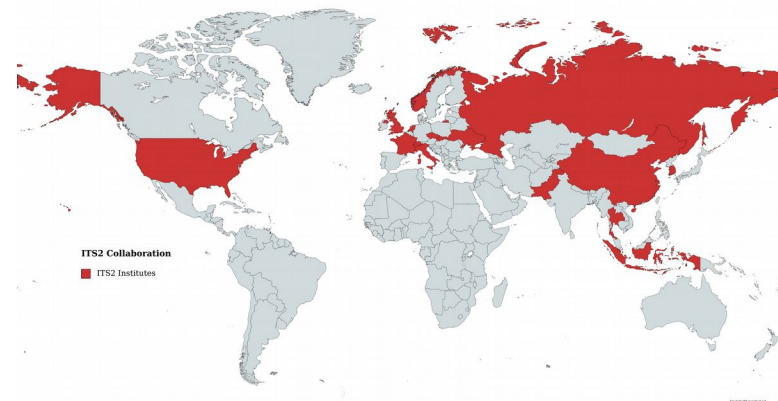
Global tracks with at least 1 hit in Inner Barrel (Run 3) or in the two innermost pixel layers (Run 2)



Conclusions

- **Run 3 started on July 5th 2022**
 - pp collisions at $\sqrt{s} = 13.6$ TeV, nominal interaction rate at 500 kHz.
- **Pb-Pb collisions in October 2023 before the year-end closure**
 - Major event of the year for ALICE
- **Excellent performance of ITS2 in Run 3**
 - ITS2 performance is within expectations → no showstoppers for upcoming Pb-Pb.
- **Detector Control System and Quality Control system**
 - Ready to monitor the detector hardware and data in Run 3

The ITS2 Collaboration



CERN (Switzerland), CCNU (China), Řež u Prahy (Czech Republic), Strasbourg (France), LIPI (Indonesia), Alessandria (Italy), Bari (Italy), Cagliari (Italy), Catania (Italy), LNF (Italy), Messina (Italy), Padova (Italy), Pavia (Italy), Torino (Italy), Trieste (Italy), Nikhef (The Netherlands), UoB/BUC (Bergen, Norway), Oslo (Norway), COMSATS (Pakistan), Inha (Republic of Korea), Yonsei (Republic of Korea), PNU (Republic of Korea), St. Petersburg (Russia), Kosice TU (Slovakia), Kosice Slovak Academy (Slovakia), SUT (Thailand), Kiev BITP (Ukraine), Liverpool (United Kingdom), Daresbury (United Kingdom), Austin (United States), LBNL (United States), ORNL (United States)

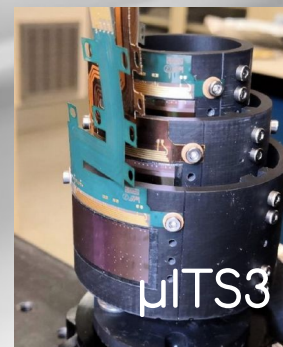
Conclusions

- Run 3 started on July 5th 2022

The ITS2 Collaboration



Interested into the future of ITS2 and 65 nm CMOS?
Don't miss these talks!
[Fri 10.30] Z. El Bitar – 65nm CMOS characterization
[Thu 15.10] R. Ricci – MAPS characterization for ITS3



system

- Ready to monitor the detector hardware and data in Run 3

(Slovakia), SUT (Thailand), Kiev BITP (Ukraine), Liverpool (United Kingdom), Daresbury (United Kingdom), Austin (United States), LBNL (United States), ORNL (United States)

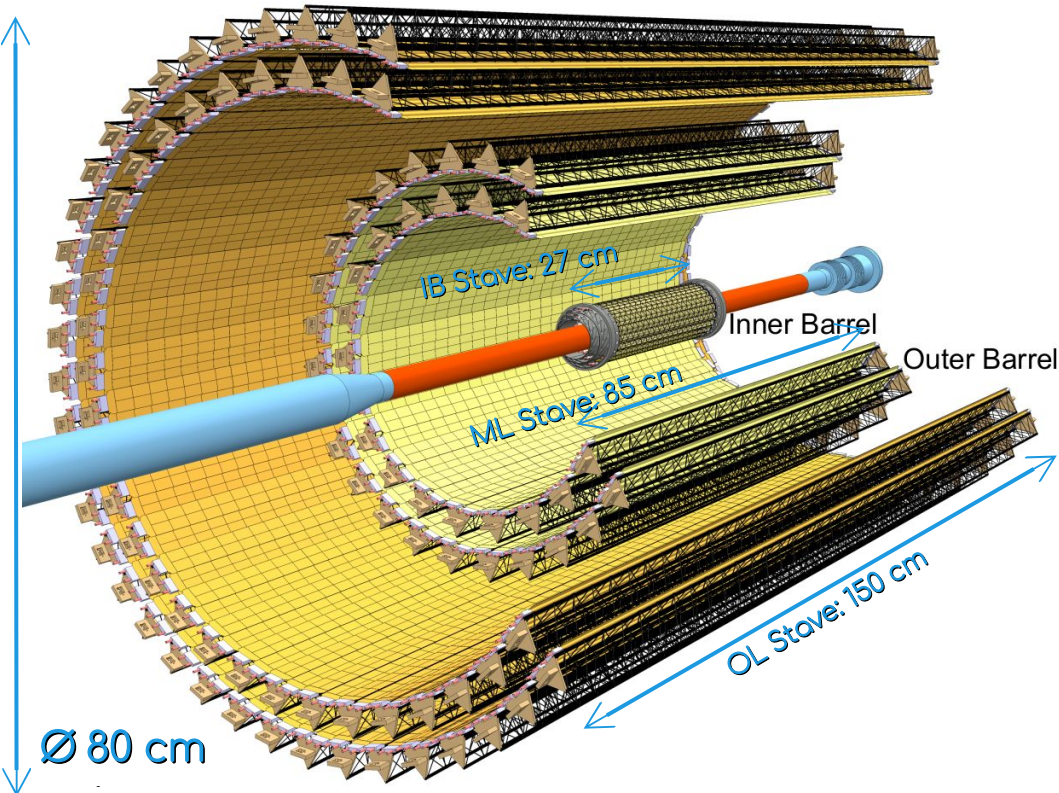
Backup slides



ITS2 - details on components

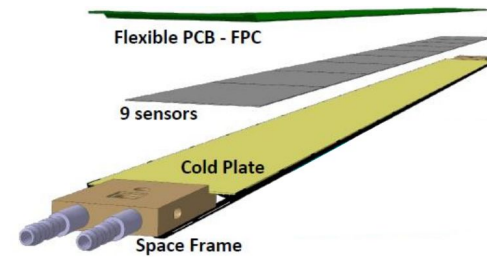


ALICE

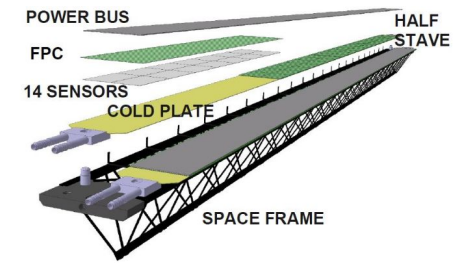


Inner Tracking System (ITS)

Inner Barrel Stave



Outer Barrel Stave



Detector Control System (DCS) - a quick view



ALICE

The screenshot shows the 'MONITORING PANEL TITLE' for the ITS DCS. At the top, it displays the time '14:00' and date '23-08-23'. The main view is a circular diagram of the ITS detector staves, with each staff represented by a colored triangle. A legend on the left indicates that the color of the staff corresponds to its specific status. To the right of the staves, there are several control panels: 'CTP clock' (LOCAL), 'ITSComms', 'Cooling FSM', and 'CAEN FSM'. Below these are 'RU counters' (Inner Barrel, Middle layers, Outer Layers), 'Detector configuration' (Current: 0, New: Press refresh -->, SET), 'Run type' (Current: PHYSICS_CONTINUOUS_202kHz, CHANGE), and 'Select injected event' (3137 trx + 10e-7 noise, SET). At the bottom, there is an 'LHC interaction rate' display (0.0) and a 'Selection of emulated events for injection' panel. A table at the bottom left shows the 'ITS Auxiliary Monitoring Zone' with columns for CAEN CR4, CAEN UX25, and Cooling, and rows for Mainframe, BB power, and Cooling. A text box at the bottom center states: 'An independent safety system interlocks power channels based on stave temperatures and cooling status'. The bottom right corner has a 'CLOSE' button.

Finite State Machine (FSM): detector status and operations

Click: stave status (voltages, currents, temperatures, readout electronics)

Auxiliary monitoring: CAEN, cooling, ...

Detector staves (color = specific status)

An independent safety system interlocks power channels based on stave temperatures and cooling status

Alarm screen: list of alarms

CAEN, Cooling and monitoring software statuses

Detector configuration and selection of run types (physics, calibration)

(Data) Quality Control (QC)

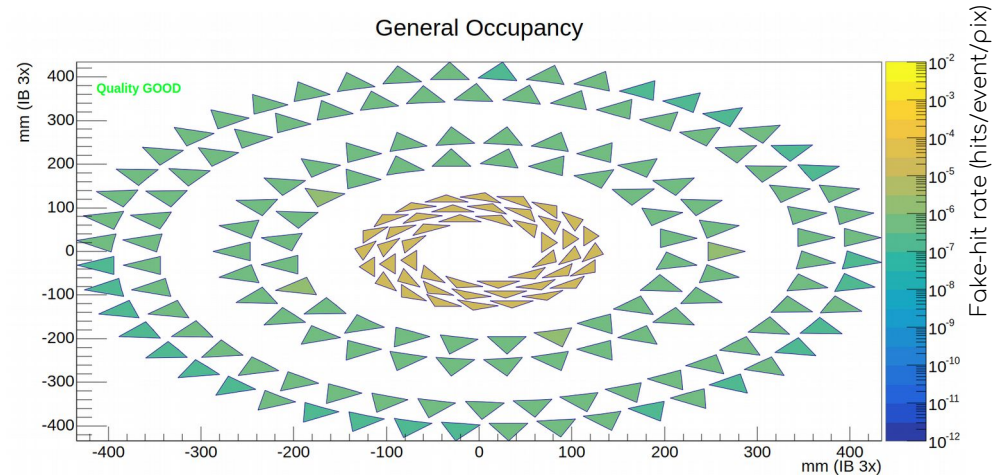
7 QC online tasks

(online monitoring on data subsets)

- **Fake-hit rate:** monitoring of detector FHR and noisy pixels
- **Noisy pixels:** for detector noise calibration
- **Calibration:** monitoring of pixel threshold and dead pixels.
- **Cluster:** monitoring cluster size, topology, etc.
- **Tracks:** monitoring of track multiplicity, angular track distribution, clusters, etc.
- **Front-end Electronics:** chips in error, trigger flags
- **Decoding errors:** summary of decoding errors per chip

... and

5 online post-processing tasks + offline post-processing framework/macros → trending vs run



Example: detector average occupancy per stave in $pp \sqrt{s} = 13.6$ TeV (500kHz IR, 202 kHz framing rate)

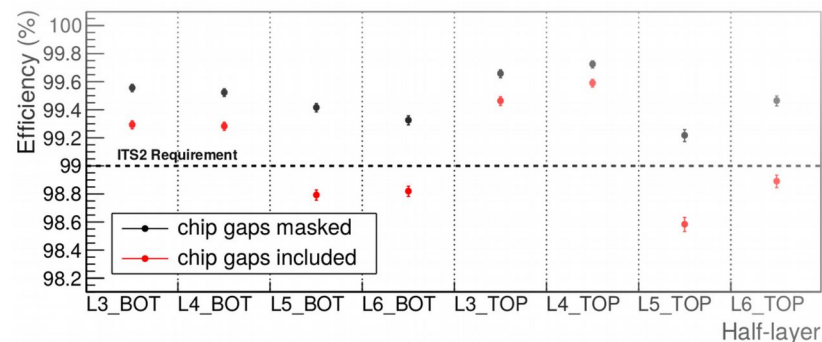
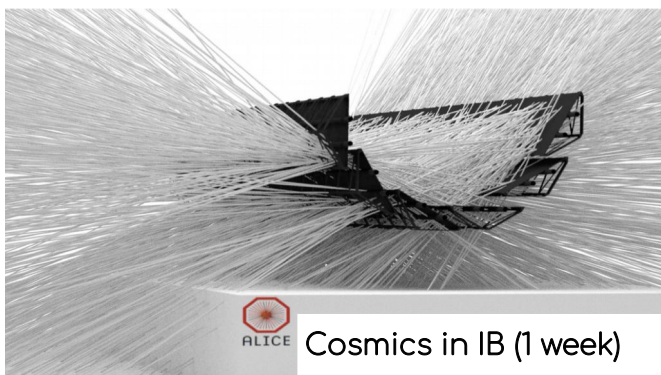
➔ QA of data done on a daily basis

On-surface commissioning overview - 2019/2020

- Commissioning of the full detector in the laboratory before installation in the ALICE cavern
 - Sept 2019 → Dec 2020
- Continuous data taking: cosmic + calibration runs
 - 24/7 shifts + operations by detector experts



Half-barrels in the CERN cleanroom



Efficiency with cosmic tracks (OB)

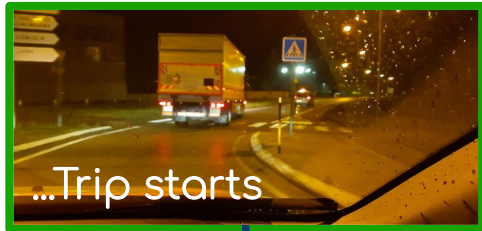
ITS installation inside ALICE cavern - 2021

→ a trip from lab to ALICE cavern

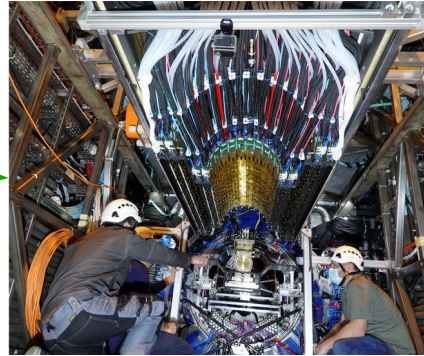


ALICE

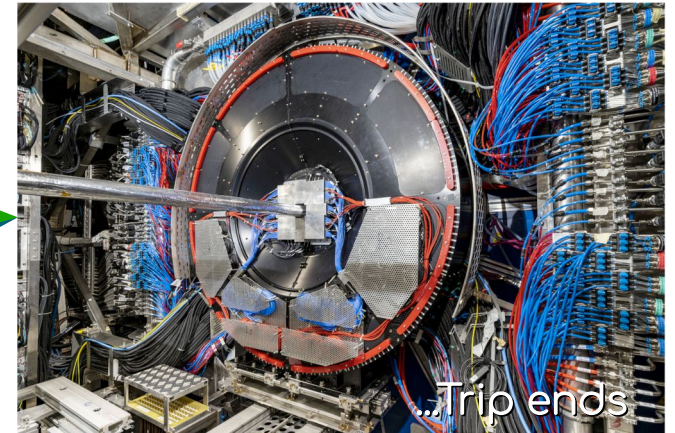
- ITS installation in ALICE cavern started in 2021 after a successful on surface commissioning from Sept/2019 to Dec/2020
- OB installation completed in **March 2021** → IB installation completed in **May 2021**



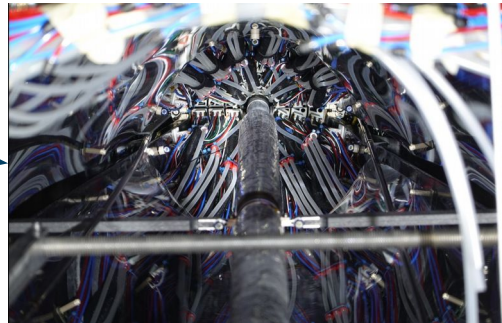
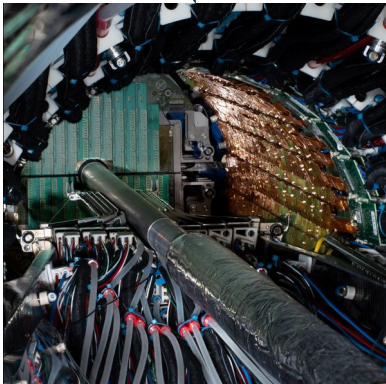
OB



Fast Interaction Trigger (FIT) in front of ITS2



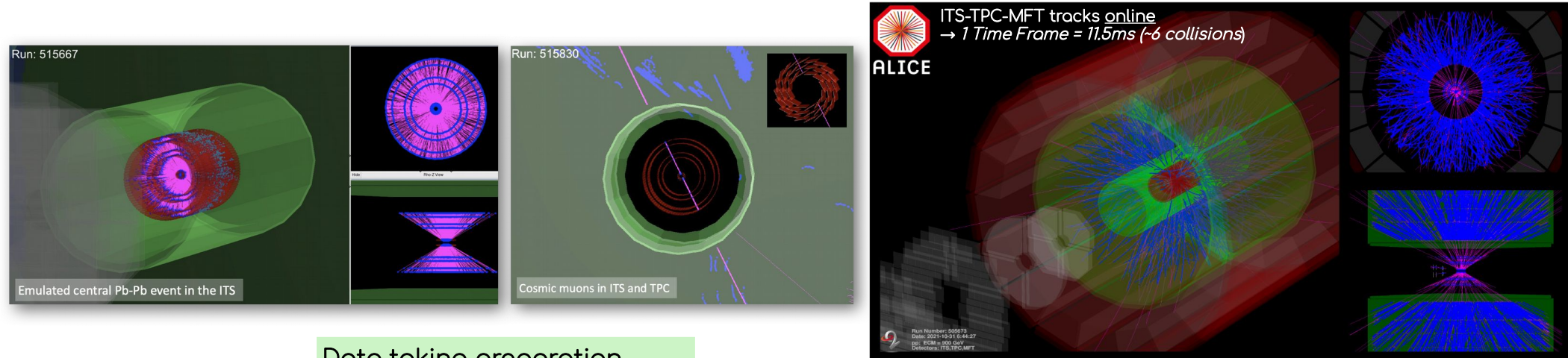
IB



The challenge:

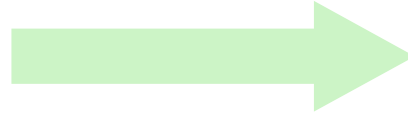
- Clearance around beam pipe: ~2 mm
- Clearance between adjacent staves: ~1.2 mm
- Manipulation from 4m distance

Verification of barrels after installation and preparation for data taking - 2021 / 2022



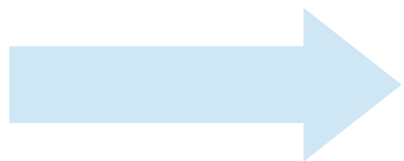
Data taking preparation

- pp @ 202 kHz
- Pb-Pb @ 45 kHz
- cosmits



Successful data taking

- pp @ $\sqrt{s} = 900$ GeV



19/07/2021: start of ALICE global commissioning with central shifts
End October 2021: first pilot collisions (pp $\sqrt{s} = 900$ GeV)
January 2022 – June 2022: pilot collisions, cosmits, software validation with emulated data

Verification of barrels after installation and preparation for data taking - 2021



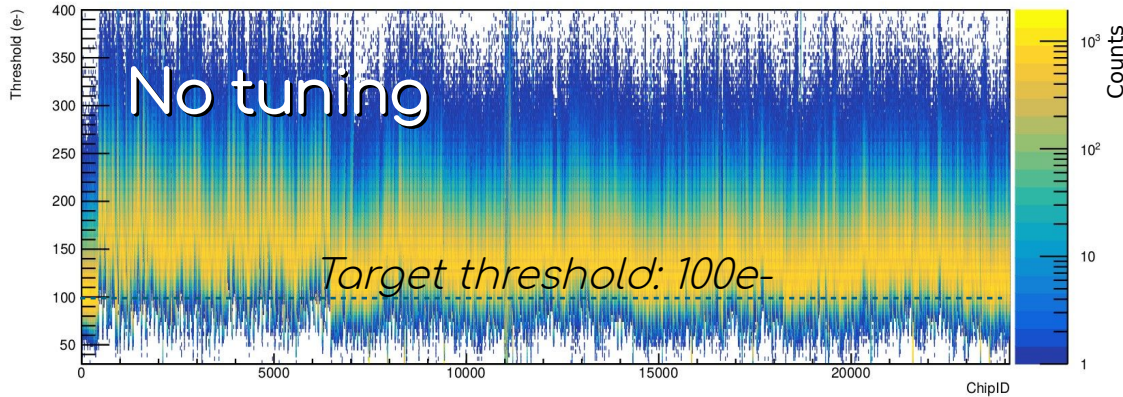
- OB installation (March 2021)
 - Full detector powered and monitored till mid-April
 - Various issues spotted and solved:
 - Problematic cables giving loose contacts or with wrong wire configuration (replaced)
 - Unstable power supplies (replaced)
 - Optimization of cooling for staves and electronics
 - Resolution of bugs into control-system user interface and code.
- IB installation (May 2021)
 - Basic verification of IB: readout tests + resistance measurement to check connections.
 - Basic verification of OB after IB installation: power and communication tests
- ITS standalone commissioning in the cavern (till July 2021)
 - Full verification of the detector: cosmic runs, data taking with emulated data patterns (pp, Pb-Pb), calibration runs.

Preliminary performance results in Run 3

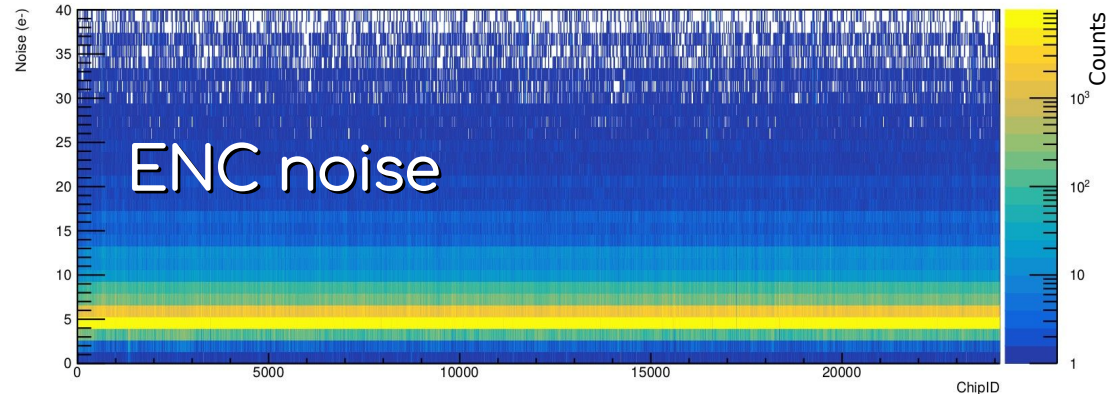
→ ITS2 full calibration (2)



ALICE



- Distribution of pixel thresholds per chip
- Thresholds not tuned: detector already ~100% efficient
- ENC noise: ~5e- (stable for 24k chips)

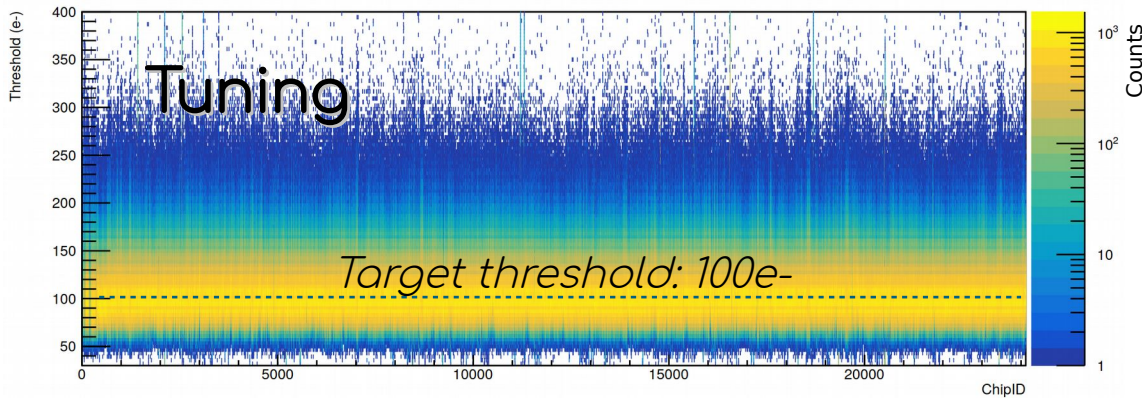


Preliminary performance results in Run 3

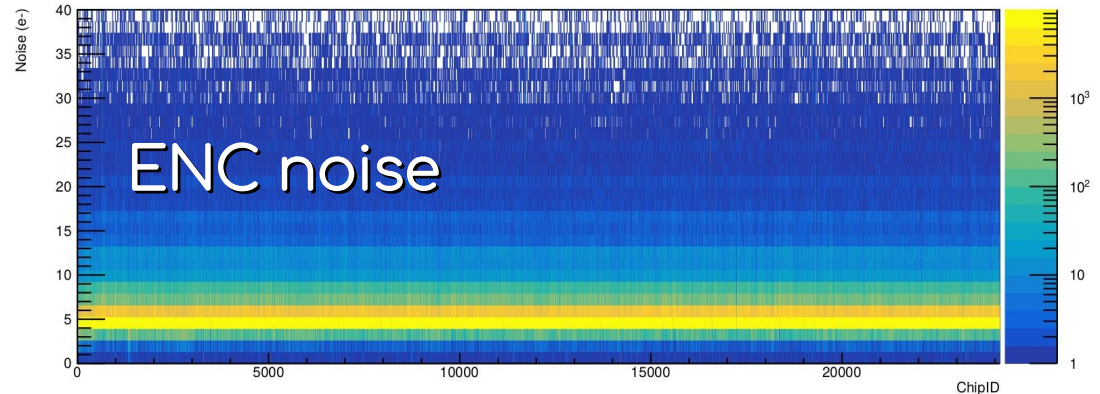
→ ITS2 full calibration (3)



ALICE



- Distribution of pixel thresholds per chip
- Thresholds not tuned: detector already ~100% efficient
- ENC noise: ~5e- (stable for 24k chips)
- Threshold tuned: 100e- (stable for 24k chips)
- RMS ~20e- (compatible with what measured during production)

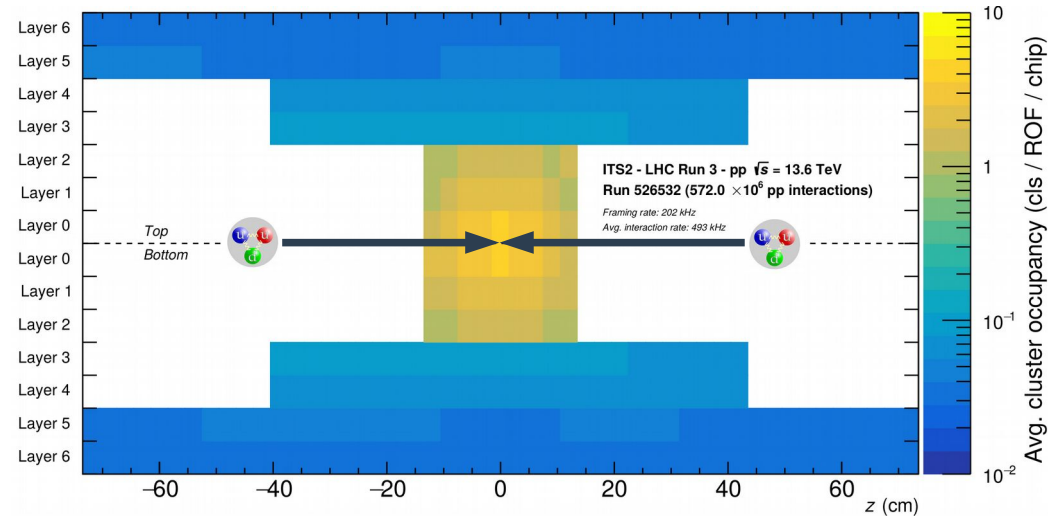


Preliminary performance results in Run 3

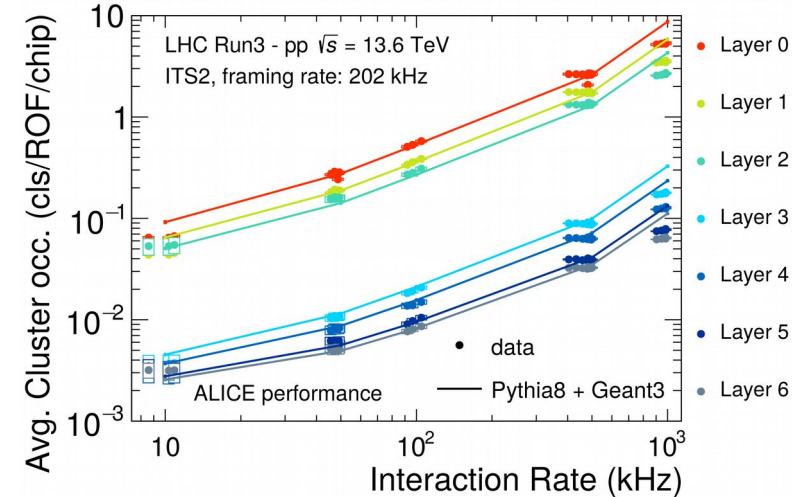
→ Cluster occupancy and simulation

- Cluster occupancy per readout frame (ROF) and per chip
 - Between 0.1 and 10 clusters/ROF/chip @ 1 MHz (202 kHz framing rate)
 - Observed to be stable over time (at the same IR)
 - Dependent on the interaction rate

- Simulation with Pythia 8 + Geant 3
 - Simulated noise: 2×10^{-8} hits/event/pix (IB), 3×10^{-9} hits/event/pix (OB)
 - Good agreement with data considering approximations:
 - Average noise per barrel and not per stave/chip.
 - Limited statistics in MC: ~20k events.



Cluster occupancy



Cluster occ. vs IR

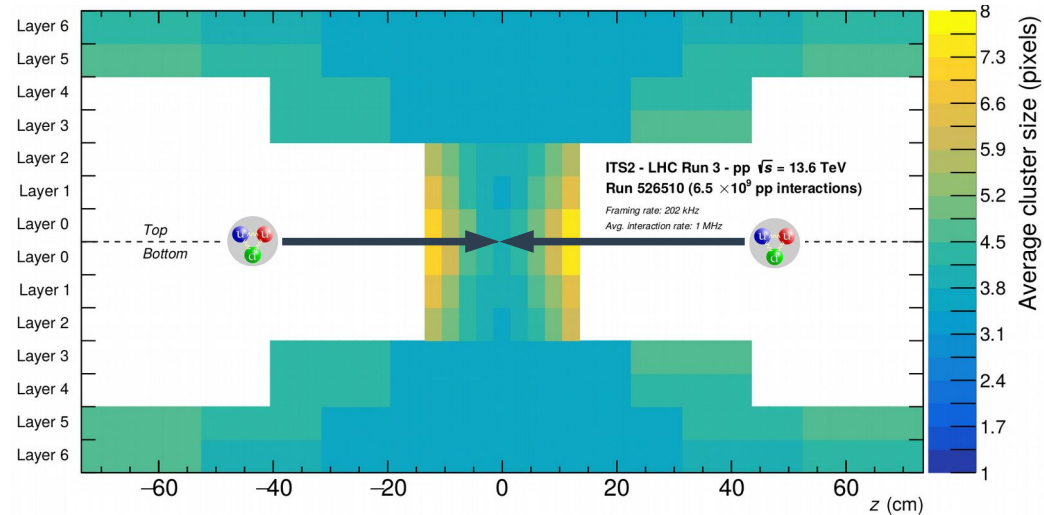
Preliminary performance plots in Run 3

→ Cluster size

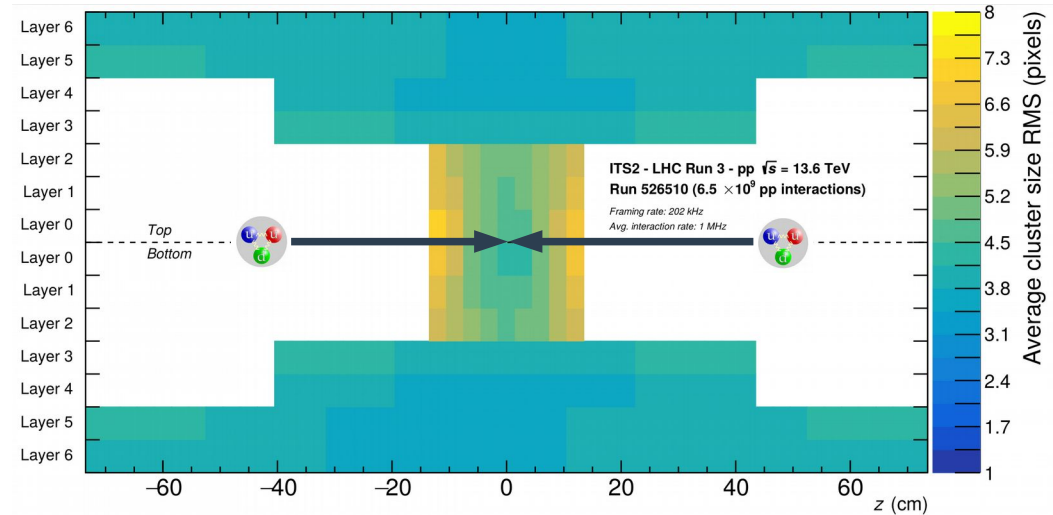


ALICE

- Cluster size averaged for half barrels
 - Between 3 and 8 pixels depending on η
 - RMS ranging on the same interval
 - Observed to be stable over time
 - Independent of the interaction rate
 - PID studies with machine learning techniques are ongoing

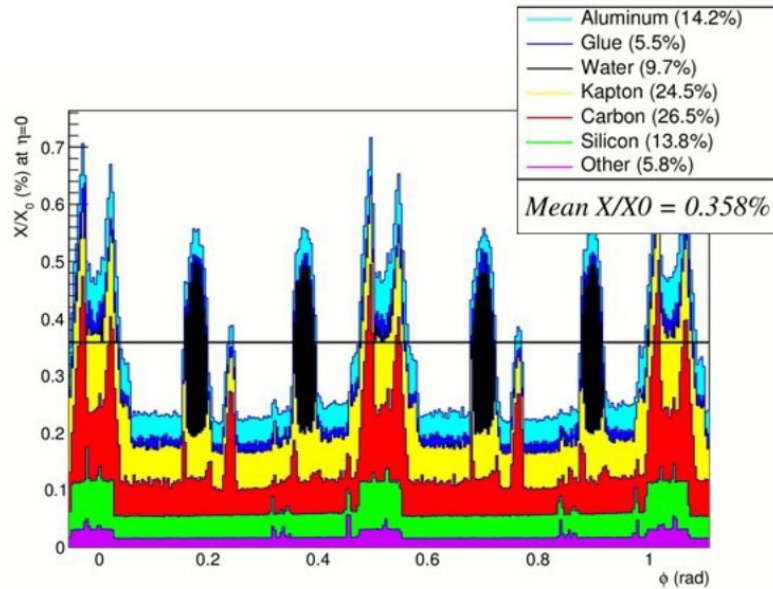


Cluster size (pix)



Cluster size RMS (pix)

Inner Barrel



Outer Barrel

