

# The MONOLITH project - picosecond time stamping capabilities in fully monolithic highly granular silicon pixel detectors

Thursday 7 September 2023 14:30 (20 minutes)

The MONOLITH H2020 ERC Advanced project aims at producing a monolithic silicon pixel ASIC with 50 $\mu$ m pixel pitch and picosecond-level time stamping. The two main ingredients of the project are fast and low-noise SiGe BiCMOS electronics and a novel sensor concept, the Picosecond Avalanche Detector (PicoAD). The PicoAD uses a patented multi-PN junction to engineer the electric field and produce a continuous gain layer deep in the sensor volume. The result is an ultra-fast current signal with low intrinsic jitter in a full fill factor and highly granular monolithic detector.

Testbeam measurements of the proof-of-concept PicoAD prototype, based on a 2019 ASIC design, shows full efficiency and time resolutions of 13ps at the center of the pixel and 25ps at the pixel edge, for an average of 17ps over the pixel surface.

A new monolithic prototype with improved SiGe BiCMOS electronics was produced in 2022 on a 350 $\Omega$ cm substrate. Although this ASIC does not have an internal gain layer, it provided 20ps time resolution in a testbeam with pions. A version of this new prototype that includes the PicoAD gain layer is under production. First results on radiation tolerance will be presented.

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**Session Classification:** Advances in Pixel Detectors

**Track Classification:** Position Sensitive Fast Timing Detectors