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Radiation hardness of the ITkPixV1 and RD53A chips

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The ITkPixV1 chip is the pre-production pixel readout chip for the Phase-2 Upgrade of the ATLAS experiment at the HL-LHC. The harsh environment of HL-LHC, including a peak luminosity of $5 \times 10^{34} \text{cm}^{-2}\text{s}^{-1}$ and an estimated total ionising dose (TID) of 1 Grad throughout its lifetime is placing strong requirements on the radiation tolerance of the chip. This contribution outlines investigations into the radiation tolerance of ITkPixV1. The impact of TID damage to the digital and analog front-end up to total doses of 1 Grad (at dose rate 4 Mrad/h) is reported.

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