

Session Program

2-4 Feb 2026



2nd Workshop of the CPAN Network on Instrumentation and Detectors

WG6 Electronics

CIEMAT, Salón de Actos "Margarita Salas" (Edificio 1, Planta Baja)
Avenida Complutense, 40 28040 Madrid Spain

Tuesday 3 February

11:30

WG6 Electronics

Session |

Location: CIEMAT, Salón de Actos "Margarita Salas" (Edificio 1, Planta Baja), Avenida Complutense, 40 28040 Madrid

Speakers: Fernando Arteché, Fernando Jose Arteché Gonzalez, Diego Real, Santiago Folgueras

11:30-11:42

Overview of the electronic's WG

Speakers

Diego Real, Fernando Arteché, Fernando Jose Arteché Gonzalez, Santiago Folgueras

11:45-11:57

Dealing with FPGAs when precise & deterministic timing is required : CIEMAT developments for future experiments rooted in HL-LHC upgrades know-how.

Speaker

Ignacio Redondo Fernandez

12:00-12:12

Real-Time Trigger Architectures for the CTAO LST Advanced Camera: From Logic to CNNs

Speakers

Jorge Buces Sáez, María Molina Delicado

12:15-12:30

Design-Space Exploration and Integer Quantization of Graph Neural Networks for Real-Time FPGA Track Finding

Speaker

Pelayo Leguina

12:30