

Session Program

25-29 May 2026

**25th IEEE Real Time Conference - La Biodola,
Elba, Italy**

***Front-End Electronics, Fast Digitizers, Fast
Transfer Links & Networks - PS***

La Biodola - Isola d'Elba (Italy)

Monday 25 May

14:45

Front-End Electronics, Fast Digitizers, Fast Transfer Links & Networks - PS: A

Poster Session | Location: Hotel Hermitage, Elena Room

Performance evaluation of ITER Synchronous Data Network using Time Sensitive Network

Speaker

David Andrino-Izquierdo

A Reliable Data Processing Board for the front-end electronics of the Hyper-Kamiokande Experiment

Speaker

Alejandro Gómez Gambín

Readout Electronics System for the CsI(Tl) Calorimeter of the VLAST-P Payload

Speaker

Qian Chen

Design and prototyping of the X-ray Fluorescence Spectrometer for online component analysis

Speaker

jiayun chen

A prototype detector for photon timing measurement with MCP-PMT

Speaker

Xiongbo Yan

Design of a Low-Power Electronics System for Low Energy X-ray Polarization Detection Based on Multi-Detector Units

Speaker

Mr Shi Qiang Zhou

Design of a Dual-Detector Gamma Spectrometer for In Situ Marine Radioactivity Monitoring

Speaker

Mr Tao Liu

FPGA-Based Multi-Channel Real-Time Readout System for the STCF Muon Detector Prototype

Speakers

Ms Qian He, Mr Hao Dong, Mr Bo Wang, Ms Xiaohan Sun, Feng LI, Kun Hu, Ms Yuying Li

GAROP3: A Gated Readout ASIC for the Proton Beam Monitor of the COMET Experiment

Speaker

Mr Xiang-Yu Xu

Development of the SAMIDARE Board: A New Acquisition System for TPCs

<p>Speaker Claudio Santonastaso</p>
<p>A Real-Time Feed-Forward System for Heralded Quantum Entanglement</p> <p>Speaker Zhizhen Qin</p>
<p>A High-precision Frequency-adaptive Clock Synchronization System</p> <p>Speaker Jiajun Qin</p>
<p>Benchmarking SRO Performance of CAEN Digitizers</p> <p>Speaker Dr Giovanni Cerretani</p>
<p>Precision analysis and development of timing measurement ASICs for strip LGAD readout</p> <p>Speakers Chuanye Wang, Xiaoting Li</p>
<p>Tapped Delay Lines Time-to-digital converter design and performance in Versal architecture</p> <p>Speaker Julien Rossignol</p>
<p>A Timing-Oriented Pixel Detector ASIC With Delay-Chain-Based Outputs for Three-Dimensional Particle Track Reconstruction</p> <p>Speaker chunlai Dong</p>
<p>Millimeter-Wave-Based Wireless Transmission System for CEPC Detector</p> <p>Speaker Dr Jun Hu</p>
<p>Progress in Readout Electronics for Scintillator-Based Multi-Neutron Detector Array</p> <p>Speaker Chengjun Zhang</p>
<p>Long-Time Coherent Integration for High Precision Weak Signal LiDAR on Lunar Rover</p> <p>Speaker Wenhao Duan</p>
<p>A 10-ps Resolution Direct-Digitizing Time Measurement ASIC for Fast Detectors</p> <p>Speaker Zhuang Li</p>
<p>Extension of a wired time-synchronization protocol for sub-nanosecond accuracy to multiple FPGA families</p> <p>Speaker Mr Eitaro Hamada</p>
<p>FPGA-Based Front-End Electronics for AC-LGAD Detector</p>

Speaker
Ms Liyan Jin

Development of UDP based data-streamer and slow-control protocol for streaming readout FEE in SPADI alliance

Speaker
Ryotaro Honda

A 25 Gbps VCSEL Driving ASIC for Detector Front-end Readout

Speakers
Cong Zhao, Prof. Di Guo

A low jitter 2.56 Gbps reference-less CDR ASIC in 55 nm for NICA Multi-Purpose Detector Project

Speakers
Cong Zhao, Prof. Di Guo

Development of a prototype front-end readout ASIC for gas pixel detectors

Speaker
Zhuo Zhou

An 11-Gbps edge-pre-emphasis SST transmitter in 55 nm for detector front-end data transmission

Speakers
Qingkang Wu, Xiaoting Li

15:45

Thursday 28 May

11:25

Front-End Electronics, Fast Digitizers, Fast Transfer Links & Networks - PS: B

Poster Session | Location: Hotel Hermitage, Elena Room

Design of Prototype Readout Electronics of the High Counting Rate Main Drift Chamber in Particle Physics Experiments

Speaker

Yilin Ma

A Timing Measurement Prototype for LGAD Using the LATRIC ASIC

Speaker

Xiongbo Yan

A Modular MPSoC Platform for Real-time Data Processing for Climate Observation Instruments

Speaker

Georg Schardt

A 14-bit 100 Ms/s Pipeline ADC for Silicon Pixel Sensor in Gaseous Detectors

Speaker

Haoqing Xie

Performance Test of the SAMIDARE Board with Mini-TPC using heavy-Ion Beams

Speaker

Fumitaka ENDO

Design of a Novel Pipelined SAR ADC for Multi-channel Front-end ASICs

Speakers

Mr Haoqing Xie, Prof. Chengxin Zhao

Prototype Readout Electronics System for a LET Spectrometer in Space Radiation

Speaker

Mr Wenrui Sun

Toward Efficient Wireless Monitoring in Nuclear Facilities Based on Named Data Networking

Speaker

Kai Shi

A Multi-Mode Waveform Digitization System Based on Switched Capacitor Array Chips

Speaker

Dingjun Li

A Full on-Chip LDO Regulator with self-adapting compensation networks for Front-end Readout Circuit

Speakers

Mr xiayu wang, xiayu wang

The μ MUX readout electronic system of the ALiCPT

Speakers

Prof. Xiangxiang Ren, Zhekai Cheng

Low-Noise Room-Temperature Readout Electronics for TDM-SQUID TES Arrays in the ALiCPT-40G Telescope

Speakers

Mr Tangchong Kuang, Prof. Xiangxiang Ren

Operations and Performance of the ATLAS Tile Calorimeter Phase-II Upgrade Demonstrator in Run 3

Speaker

Fernando Carrio Argos

12:30