

Using computer simulations to identify the source of electromagnetic disturbances occurring in the high-current laboratory

Tuesday 12 September 2023 11:00 (20 minutes)

This paper presents the application of ANSYS circuit simulator for the identification and evaluation of the potential sources of electromagnetic disturbances in the high- current laboratory environment. Use of the computer model of the test circuit and comparison of the simulation results with the measurements allowed to confirm the hypothesis that the characteristic patterns of transient signals are caused by the non- simultaneous closing of the switch contacts.

Authors: STARZYŃSKI, Jacek (Warsaw University of Technology); SROKA, Jan (Warsaw University of Technology); SADURA, Jolanta

Presenter: SROKA, Jan (Warsaw University of Technology)

Session Classification: Poster Session

Track Classification: Computational models of electrical systems